

TYPE	INTRINSIC STANDOFF RATIO η		INTERBASE RESISTANCE r_{BB}		PEAK-POINT CURRENT I_P	EMITTER REV. CURRENT $I_{E020 @ V_{BE}}$			VALLEY-POINT CURRENT I_V	BASE-PEAK VOLTAGE V_{0B1}	CASE
	MIN.	MAX.	MIN.	MAX.	MAX.	MAX.			MIN.	MIN.	
			k Ω	k Ω	mA	μ A	V		mA	V	
2N2417	0.5	0.62	47	8.8	12	2.0	80	80	—	—	
2N2417A	0.61	0.62	47	8.8	12	2.0	80	80	3.0	—	
2N2417B	0.61	0.62	47	8.8	6.1	0.2	30	80	3.0	—	
2N2418	0.61	0.62	6.2	9.1	12	20	80	8.0	—	—	
2N2418A	0.81	0.62	6.2	9.1	12	20	60	8.0	3.0	—	
2N2418B	0.81	0.62	42	9.1	8.0	0.2	30	8.0	20	—	
2N2419	0.68	0.68	47	6.8	12	20	80	8.0	—	—	
2N2419A	0.68	0.68	47	6.8	12	80	80	80	30	—	
2N2419B	0.68	0.68	47	6.8	6.0	0.2	30	8.0	30	—	
2N2420	0.66	0.68	6.2	9.1	12	80	80	8.0	—	—	
2N2420A	0.66	0.68	42	9.1	12	20	80	8.0	30	—	
2N2420B	0.66	0.68	6.2	9.1	6.1	0.2	30	8.0	30	—	
2N2421	0.62	0.75	47	8.8	12	20	80	8.0	—	—	
2N2421A	0.62	0.75	47	8.8	12	20	30	8.0	30	—	
2N2421B	0.62	0.75	47	8.8	6.1	0.2	30	8.0	30	—	
2N2422	0.62	0.76	6.2	9.1	12	20	80	8.0	—	—	
2N2422A	0.62	0.75	6.2	9.1	12	2.0	80	8.0	30	—	
2N2422B	0.62	0.75	6.2	9.1	6.0	0.2	80	8.0	30	—	
2N2646	0.66	0.76	47	9.1	5.0	12	30	4.0	30	—	
2N2840	0.68	0.72	47	9.1	2.0	0.2	30	8.0	8.0	—	
2N2841	0.62	—	47	9.1	10	10	30	20	—	—	
2N3960	0.68	0.82	4.0	9.1	2.0	0.01	30	1.0	6.0	—	
2N4851	0.66	0.78	4.7	9.1	20	0.1	30	20	3.0	—	
2N4852	0.70	0.85	4.7	9.1	20	0.1	30	4.0	5.0	—	
2N4853	0.70	0.85	4.7	9.1	44	0.08	30	6.0	0.1	—	
2N4847	0.61	0.69	4.1	9.1	30	0.07	30	40	3.0	—	
2N4848	0.65	0.82	6.0	12	20	0.01	30	2.0	40	—	
2N4849	0.74	0.86	4.0	12	10	0.01	30	2.0	30	—	
2N5431	0.72	0.80	6.0	8.1	0.4	0.01	30	2.0	1.0	—	
MU20	0.50	0.85	4.0	10	50	10	30	1.0	3.0	—	
MU2646M	0.66	0.78	47	9.1	50	12	30	2.0	30	—	

*Typical values

TABLE C JIN JUNCTION TRANSISTORS TO-18 CASE

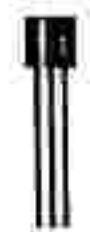
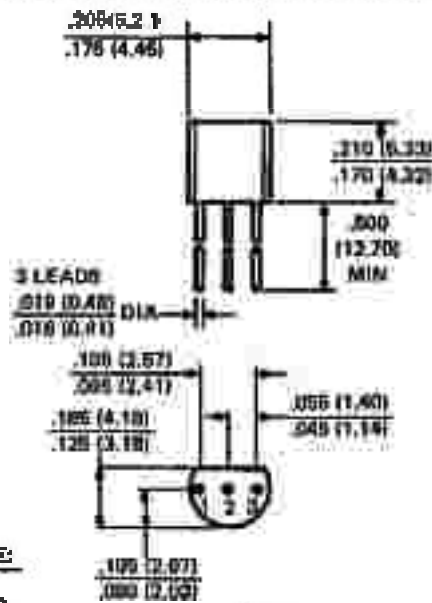
TYPE	INTRINSIC STANDOFF RATIO η		INTERBASE RESISTANCE r_{BB}		PEAK-POINT CURRENT I_P	EMITTER REV. CURRENT $I_{E020 @ V_{BE}}$			VALLEY-POINT CURRENT I_V	BASE-PEAK VOLTAGE V_{0B1}	CASE
	MIN.	MAX.	MIN.	MAX.	MAX.	MAX.			MIN.	MIN.	
			k Ω	k Ω	mA	μ A	V		mA	V	
2N4870	0.66	0.75	4.0	9.1	5.0	10	30	2.0	3.0	—	
2N4871	0.70	0.85	4.0	9.1	5.0	10	30	4.0	5.0	—	
MU10	0.60	0.85	4.0	10	5.0	10	30	1.0	3.0	—	
MU2646	0.66	0.76	47	9.1	50	12	30	4.0	3.0	—	
MU4801	0.66	0.62	4.0	9.1	5.0	0.01	30	2.0	3.0	—	
MU4802	0.61	0.69	4.0	9.1	2.0	0.01	30	2.0	3.0	—	
MU4803	0.65	0.82	4.0	12	3.0	0.01	30	2.0	6.0	—	
MU4804	0.74	0.86	4.0	12	1.0	0.01	30	2.0	3.0	—	

TABLE D PROGRAMMABLE UNIJUNCTION TRANSISTORS TO-18 CASE

TYPE	MAXIMUM RATINGS		GATE TO ANODE LEAKAGE CURRENT $I_{GAD @ 40V}$	PEAK CURRENT I_P		VALLEY CURRENT I_V		CASE
	GATE TO ANODE REVERSE VOLTAGE V_{GAR}	DC ANODE CURRENT I_T		$R_G = 10k\Omega$	$R_G = 1.0M\Omega$	$R_G = 10k\Omega$	$R_G = 1.0M\Omega$	
MAX.	MAX.	MAX.	MIN.	MAX.				
V	mA	nA	μA	μA	μA	μA		
2N8027	40	150	10	5.0	2.0	70	50	
2N8028	40	150	10	1.0	0.15	25	25	
A7T8027	40	150	10	5.0	2.0	70	50	
A7T8028	40	150	10	1.0	0.15	25	25	

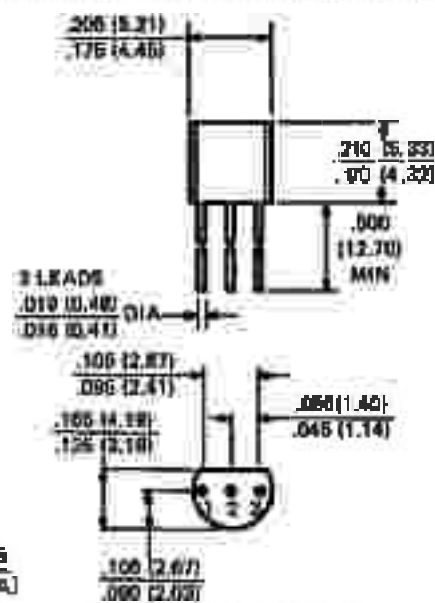


CASE OUTLINE DRAWINGS



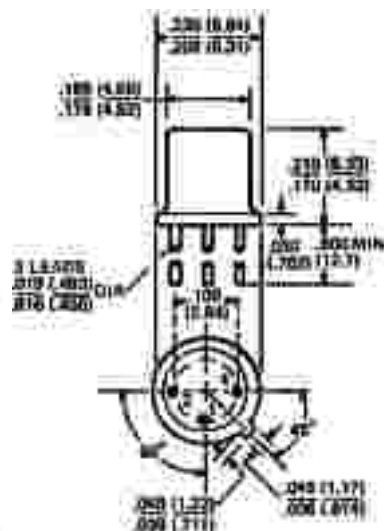
LEAD CODE:
 1. BASE 1
 2. EMITTER
 3. BASE 2

TO-18 (UJT)



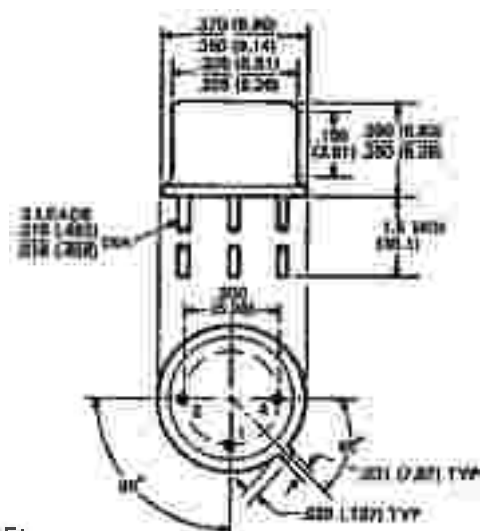
LEAD CODE:
 1. ANODE (A)
 2. GATE (G)
 3. CATHODE (K)

TO-18 (PUT)



LEAD CODE:
 1. EMITTER
 2. BASE 1
 4. BASE 2

TO-18*



LEAD CODE:
 1. EMITTER
 2. BASE 1
 4. BASE 2

TO-5*

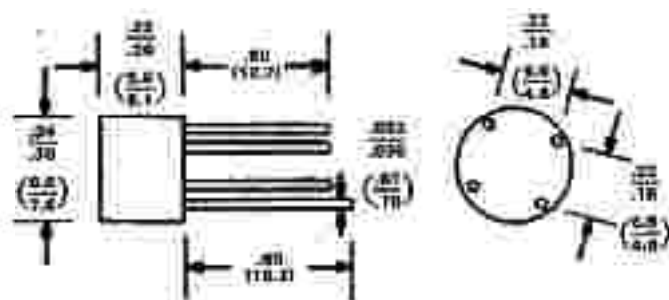
DIMENSIONS IN INCHES (MILLIMETERS)

DRAWINGS NOT TO SCALE

*Conforms to JEDEC outline except for lead configuration.

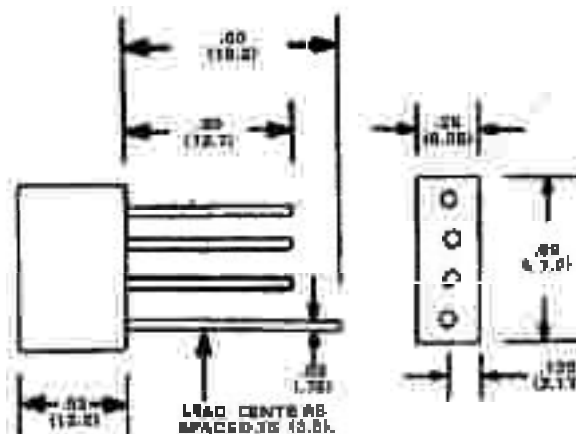
Central Semiconductor Corp.

CASE OUTLINE DRAWINGS



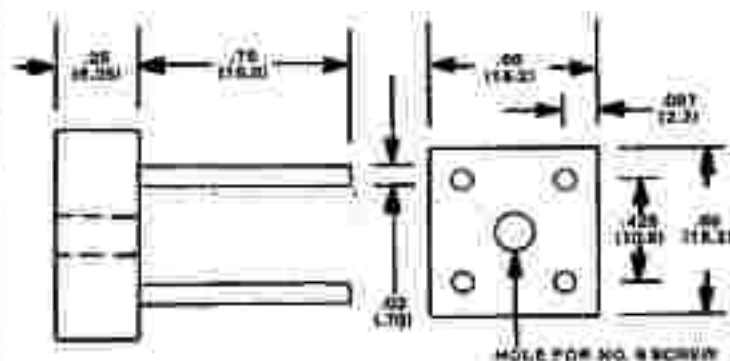
CASE A

CBR1 Series
CBR2 Series



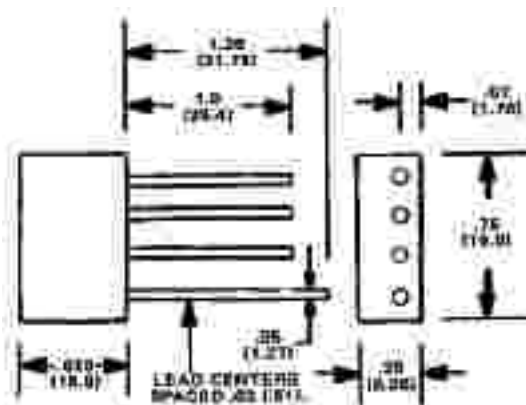
CASE B

CBR1-LS Series
CBR2-L Series



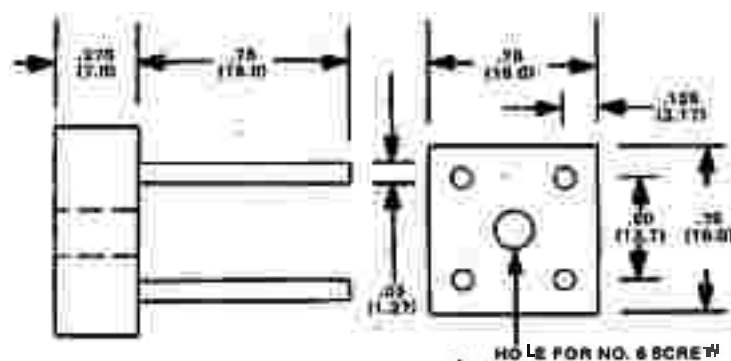
CASEC

CBR3-P Series

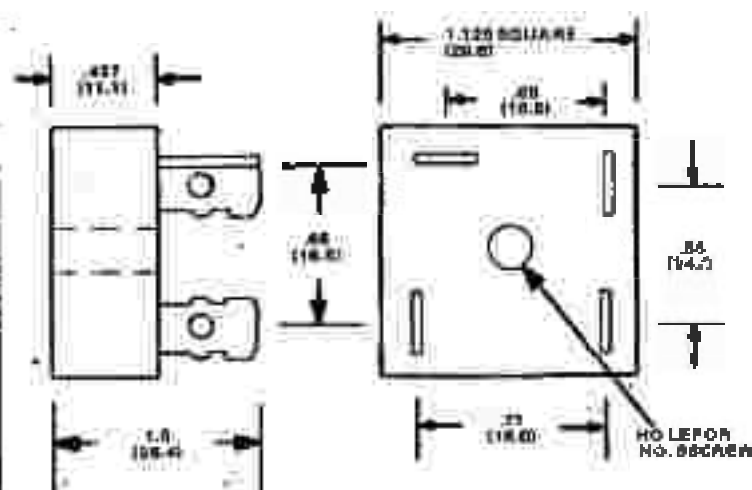


CASE D

CBM4-L Series



CASE E

CAR2 Series

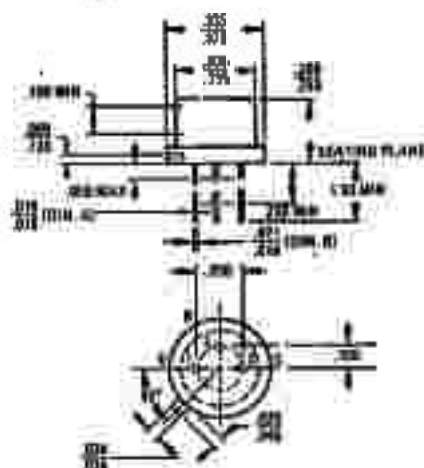
CASE F

CBR10 Series, CBR25 Series
CBR12 Series, CBR30 Series

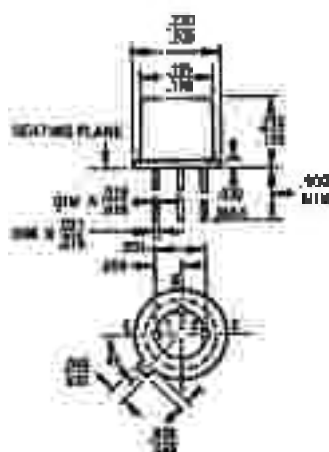
All Dimensions in Inches (Millimeters)

MECHANICAL OUTLINE DRAWINGS

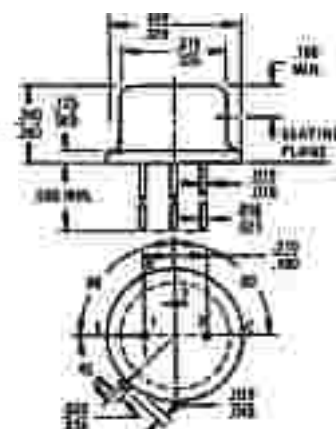
TO-5



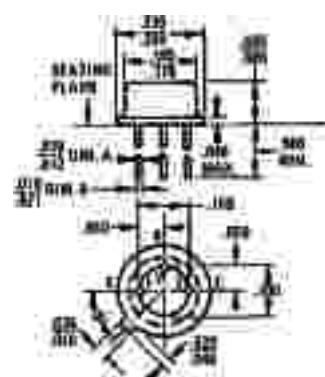
TO-18



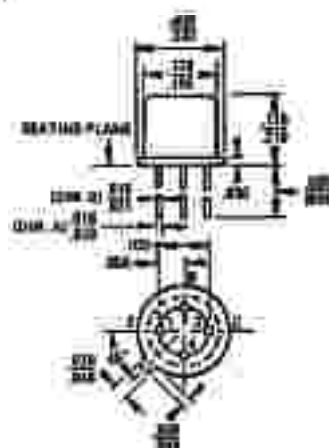
TO-39



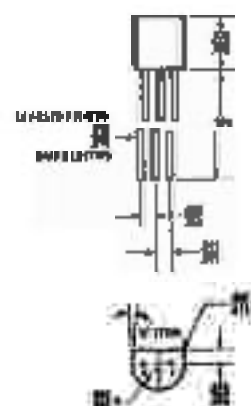
T0-46



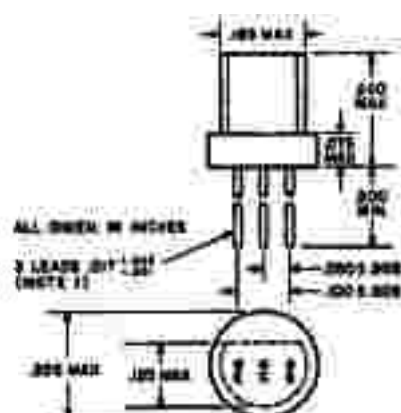
TO-72



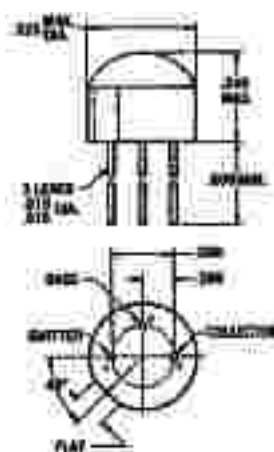
TO-92



TC-98



TD-106



TD-106

