

DESCRIPTION

- With TO-3PFM package
- High speed switching
- High breakdown voltage

APPLICATIONS

- Character display horizontal deflection output

PINNING

PIN	DESCRIPTION
1	Base
2	Collector
3	Emitter

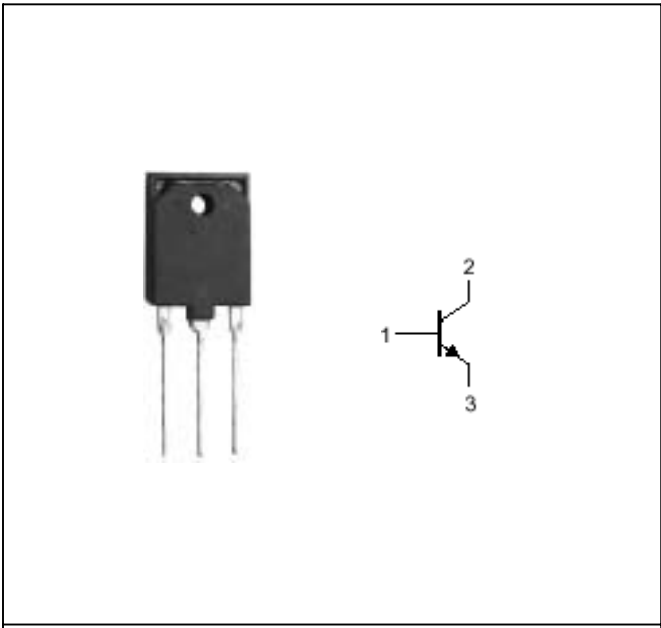


Fig.1 simplified outline (TO-3PFM) and symbol

Absolute maximum ratings(Ta=)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	1500	V
V _{CEO}	Collector-emitter voltage	Open base	800	V
V _{EBO}	Emitter-base voltage	Open collector	6	V
I _C	Collector current		10	A
I _{C(surge)}	Collector current-surge		20	A
P _C	Collector power dissipation	T _C =25	50	W
T _j	Junction temperature		150	
T _{stg}	Storage temperature		-55~150	

Silicon NPN Power Transistors

2SC4747

CHARACTERISTICS

Tj=25 unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
$V_{(BR)CEO}$	Collector-emitter breakdown voltage	$I_C=10mA ; R_{BE}=0$	800			V
$V_{(BR)EBO}$	Emitter-base breakdown voltage	$I_E=10mA ; I_C=0$	6			V
I_{CES}	Collector cut-off current	$V_{CE}=1500V; R_{BE}=0$			0.5	mA
h_{FE}	DC current gain	$I_C=1A ; V_{CE}=5V$	8		30	
$V_{CE(sat)}$	Collector-emitter saturation voltage	$I_C=8A ; I_B=1.6A$			5	V
$V_{BE(sat)}$	Base-emitter saturation voltage	$I_C=8A ; I_B=1.6A$			1.5	V
t_f	Fall time	$I_{CP}=7A; I_{B1}=1.4A$			0.3	μs

PACKAGE OUTLINE

