

isc Silicon NPN Darlington Power Transistor

2SD617

DESCRIPTION

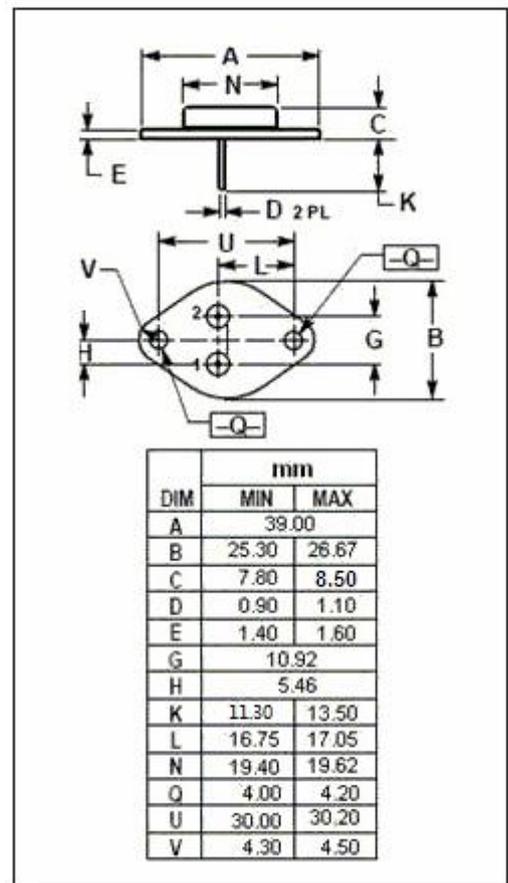
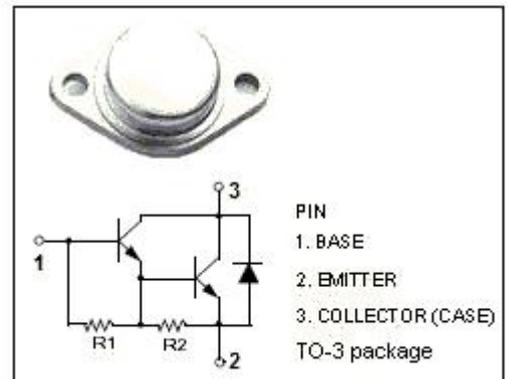
- Low Collector Saturation Voltage
- High DC Current Gain
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for audio output stages and general amplifier and switching applications

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	120	V
V_{CEO}	Collector-Emitter Voltage	100	V
V_{EBO}	Emitter-Base Voltage	6	V
I_C	Collector Current	8	A
I_{CM}	Collector Current-Peak	12	A
P_C	Collector Power Dissipation @ $T_c=25^\circ\text{C}$	100	W
T_j	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-65~150	$^\circ\text{C}$



isc Silicon NPN Darlington Power Transistor**2SD617**

ELECTRICAL CHARACTERISTICS**T_c=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEO(SUS)}	Collector-Emitter Sustaining Voltage	I _C = 50mA ; I _B =0	100			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 4A; I _B = 40mA			1.5	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 4A; I _B = 40mA			2.0	V
I _{CBO}	Collector Cutoff Current	V _{CB} =120V; I _E =0			0.2	mA
I _{CEO}	Collector Cutoff Current	V _{CE} = 100V; I _B = 0			0.5	mA
I _{EBO}	Emitter Cutoff Current	V _{EB} = 5V; I _C = 0			5	mA
h _{FE-1}	DC Current Gain	I _C = 1A; V _{CE} = 4V	2500			
h _{FE-2}	DC Current Gain	I _C = 3A ; V _{CE} = 4V	1000			
h _{FE-2}	DC Current Gain	I _C = 8A ; V _{CE} = 4V	750			