

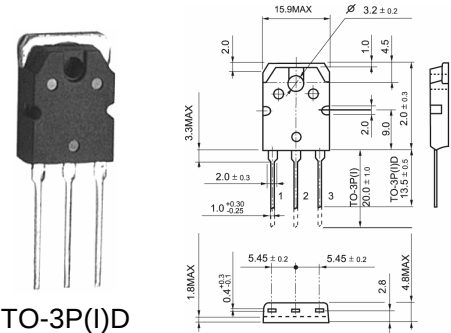


2SD716

SILICON EPITAXIAL PLANAR TRANSISTOR

GENERAL DESCRIPTION

Silicon NPN high frequency, high power transistors in a plastic envelope, primarily for use in audio and general purpose



QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V_{CESM}	Collector-emitter voltage peak value	$V_{BE} = 0V$	-	100	V
V_{CEO}	Collector-emitter voltage (open base)		-	100	V
I_C	Collector current (DC)		-	6	A
I_{CM}	Collector current peak value		-		A
P_{tot}	Total power dissipation	$T_{mb} \leq 25^\circ C$	-	60	W
V_{CEsat}	Collector-emitter saturation voltage	$I_C = 3.0A; I_B = 0.3A$	-	2	V
V_F	Diode forward voltage	$I_F = 3.0A$	1.5	2.0	V
t_f	Fall time			-	μS

LIMITING VALUES

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V_{CESM}	Collector-emitter voltage peak value	$V_{BE} = 0V$	-	100	V
V_{CEO}	Collector-emitter voltage (open base)		-	100	V
V_{EBO}	Emitter-base oltage (open collector)			5	V
I_C	Collector current (DC)		-	6	A
I_B	Base current (DC)		-	1.5	A
P_{tot}	Total power dissipation	$T_{mb} \leq 25^{\circ}C$	-	60	W
T_{str}	Storage temperature		-55	150	$^{\circ}C$
T_j	Junction temperature		-	150	$^{\circ}C$

ELECTRICAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
I_{CBO}	Collector-base cut-off current	$V_{CB}=100V$	-	0.2	mA
I_{EBO}	Emitter-base cut-off current	$V_{EB}=5V$	-	0.2	mA
$V_{(BR)CEO}$	Collector-emitter breakdown voltage	$I_C=1mA$	100		V
V_{CESat}	Collector-emitter saturation voltages	$I_C = 3.0A; I_B = 0.3A$	-	2	V
h_{FE}	DC current gain	$I_C = 1A; V_{CE} = 5V$	50	250	
f_T	Transition frequency at $f = 5MHz$	$I_C = 1A; V_{CE} = 12V$	12	-	MHz
C_c	Collector capacitance at $f = 1MHz$	$V_{CB} = 10V$		150	pF
t_{on}	On times				us
t_s	Turn-off storage time				us
t_f	Fall time				us