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2SK2225

Silicon N-Channel MOS FET

HITACHI

ADE-208-140
1st. Edition

Application

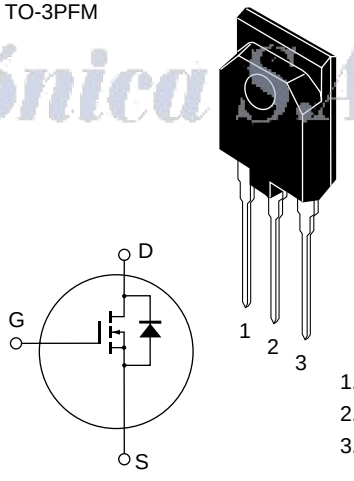
High speed power switching

Features

- High breakdown voltage (V_{DSS} = 1500 V)
- High speed switching
- Low drive current
- No Secondary Breakdown
- Suitable for Switching regulator, DC-DC converter

Outline

TO-3PFM



1. Gate
2. Drain
3. Source

Hitachi semiconductor

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Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	1500	V
Gate to source voltage	V_{GSS}	±20	V
Drain current	I_D	2	A
Drain peak current	$I_{D(pulse)}$*[1]	7	A
Body to drain diode reverse drain current	I_{DR}	2	A
Channel dissipation	Pch*[2]	50	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	–55 to +150	°C

Notes 1. PW ≤ 10 µs, duty cycle ≤ 1 %
2. Value at Tc = 25 °C

Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	1500	—	—	V	I_D = 10 mA, V_{GS} = 0
Gate to source leak current	I_{GSS}	—	—	±1	µA	V_{GS} = ±20 V, V_{DS} = 0
Zero gate voltage drain current	I_{DSS}	—	—	500	µA	V_{DS} =1200 V, V_{GS} = 0
Gate to source cutoff voltage	$V_{GS(off)}$	2.0	—	4.0	V	I_D = 1 mA, V_{DS} = 10 V
Static drain to source on state resistance	$R_{DS(on)}$	—	9	12	Ω	I_D = 1 A V_{GS} = 15 V*[1]
Forward transfer admittance	$\|y_{fs}\|$	0.45	0.75	—	S	I_D = 1 A V_{DS} = 20 V*[1]
Input capacitance	Ciss	—	990	—	pF	V_{DS} = 10 V
Output capacitance	Coss	—	125	—	pF	V_{GS} = 0
Reverse transfer capacitance	Crss	—	60	—	pF	f = 1 MHz
Turn-on delay time	$t_{d(on)}$	—	17	—	ns	I_D = 1 A
Rise time	t_r	—	50	—	ns	V_{GS} = 10 V
Turn-off delay time	$t_{d(off)}$	—	150	—	ns	R_L = 30 Ω
Fall time	t_f	—	50	—	ns	
Body to drain diode forward voltage	V_{DF}	—	0.9	—	V	I_F = 2 A, V_{GS} = 0
Body to drain diode reverse recovery time	t_{rr}	—	1750	—	ns	I_F = 20 A, V_{GS} = 0, di_F / dt = 100 A / µs

Note 1. Pulse Test

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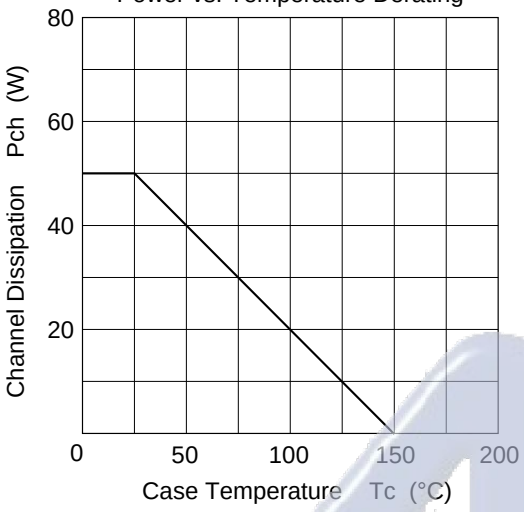
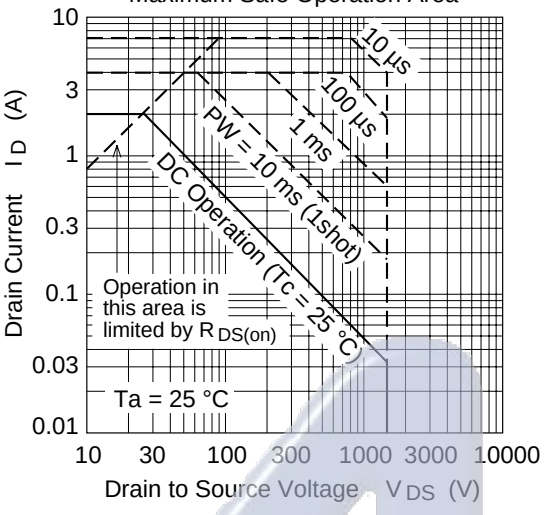
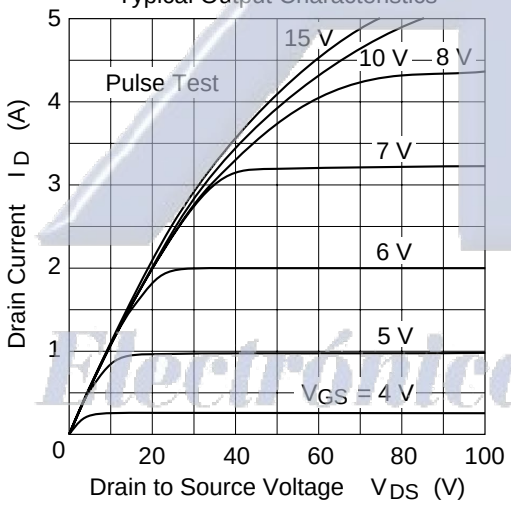
Power vs. Temperature Derating



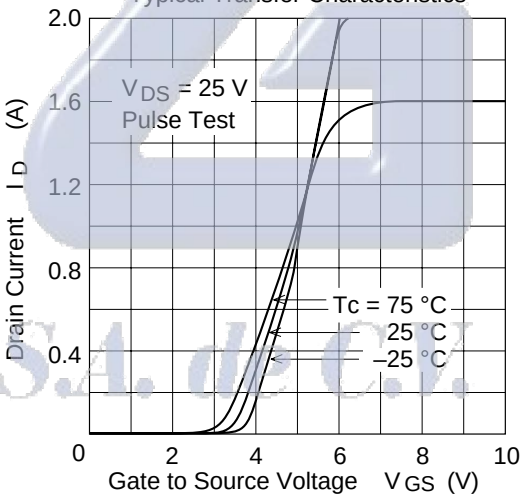
Maximum Safe Operation Area



Typical Output Characteristics



Typical Transfer Characteristics



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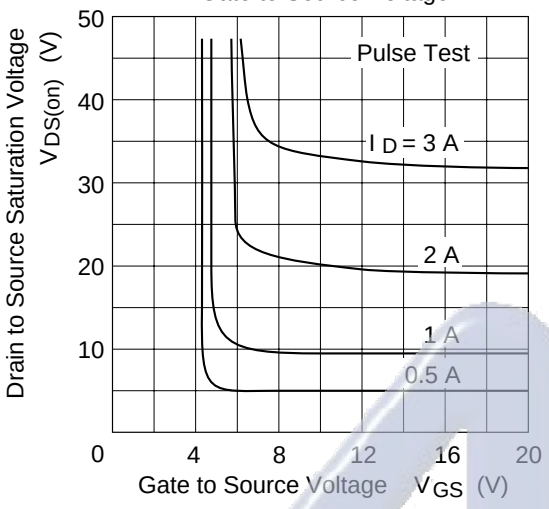
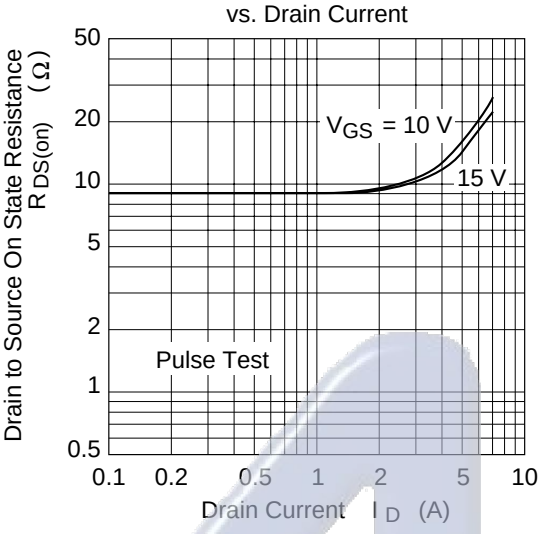
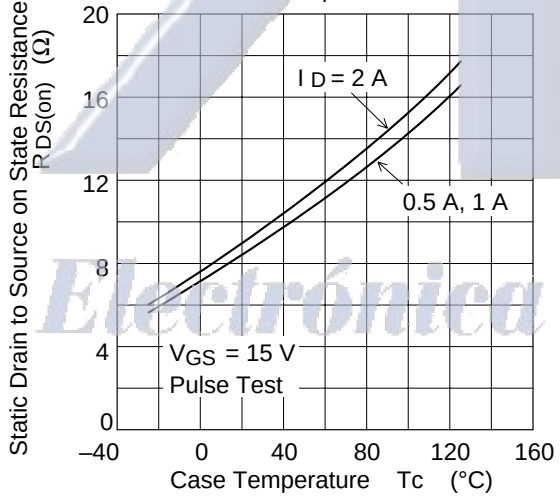
Drain to Source Saturation Voltage vs. Gate to Source Voltage



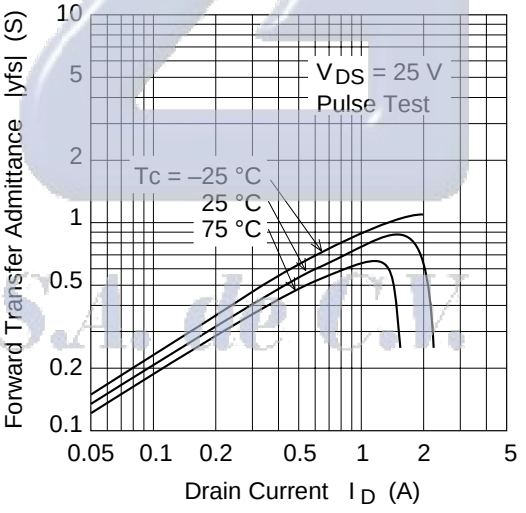
Static Drain to Source State Resistance vs. Drain Current



Static Drain to Source on State Resistance vs. Temperature



Forward Transfer Admittance vs. Drain Current



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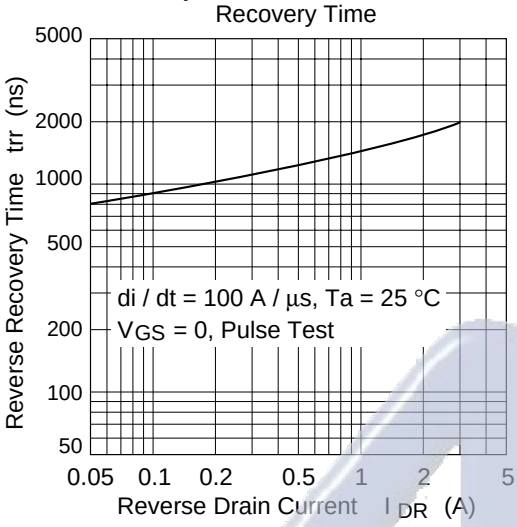
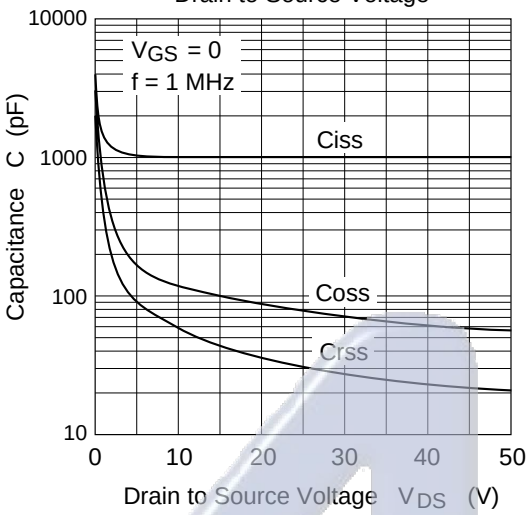
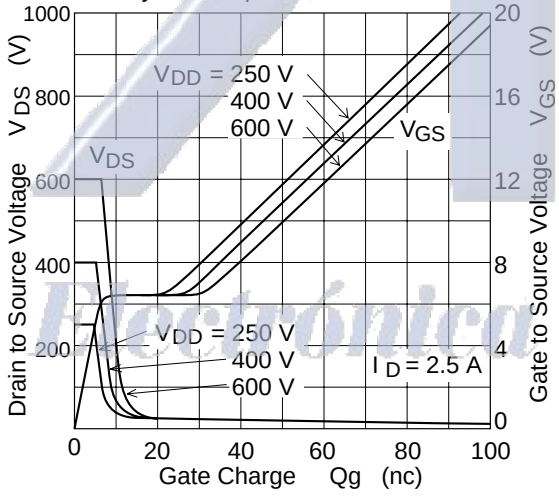
Body to Drain Diode Reverse Recovery Time



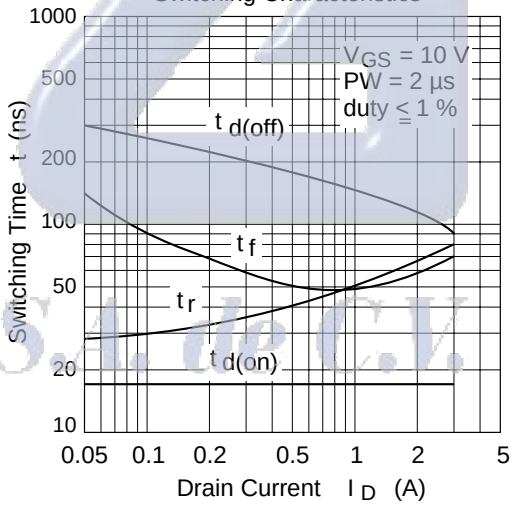
Typical Capacitance vs. Drain to Source Voltage



Dynamic Input Characteristics



Switching Characteristics



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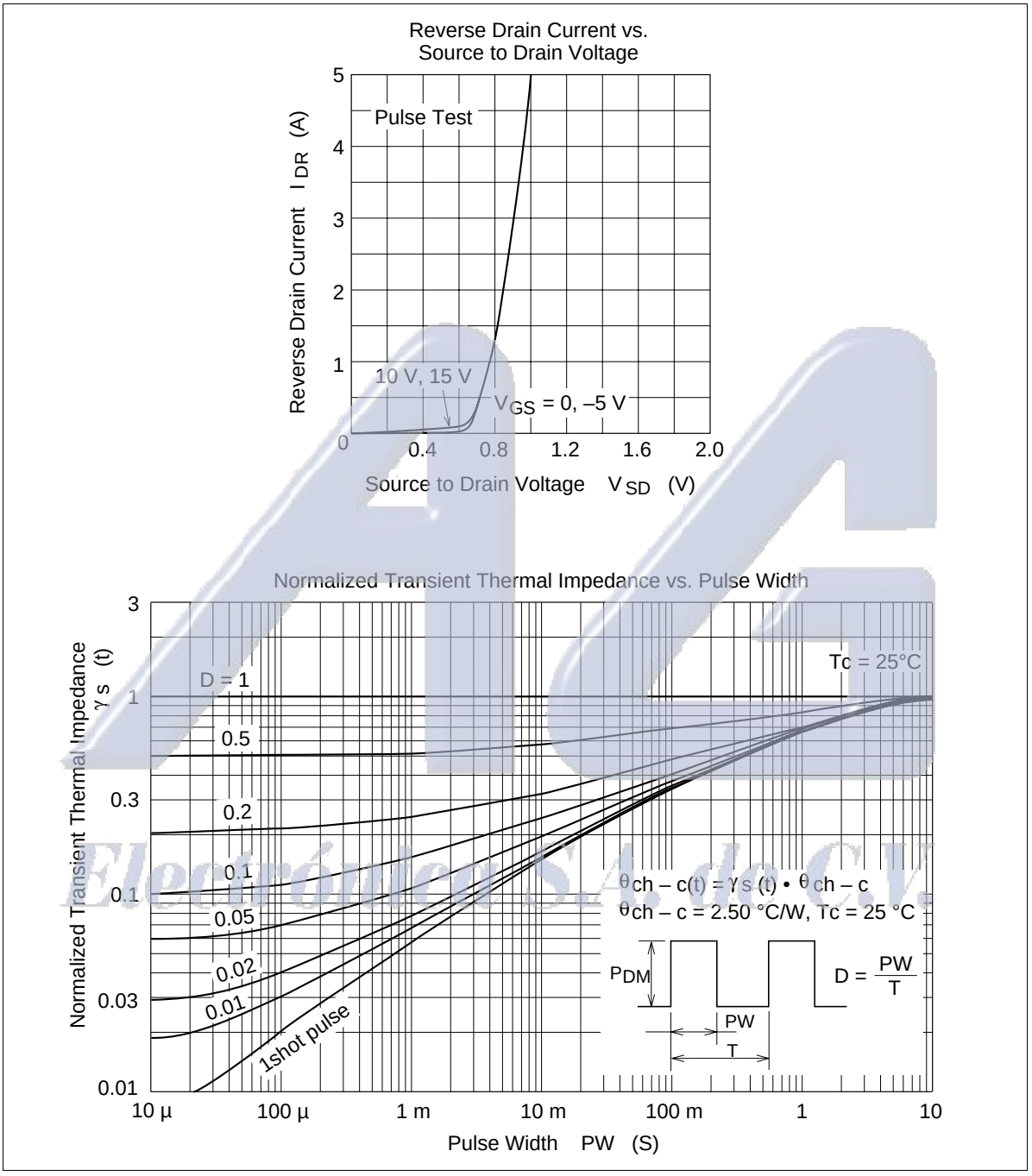
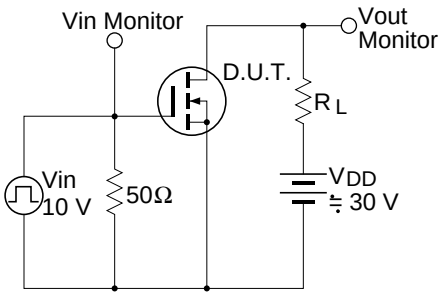
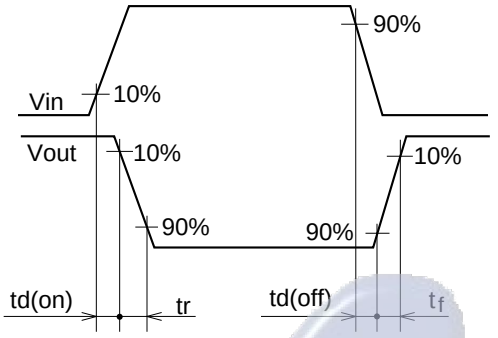


Switching Time Test Circuit



Waveform



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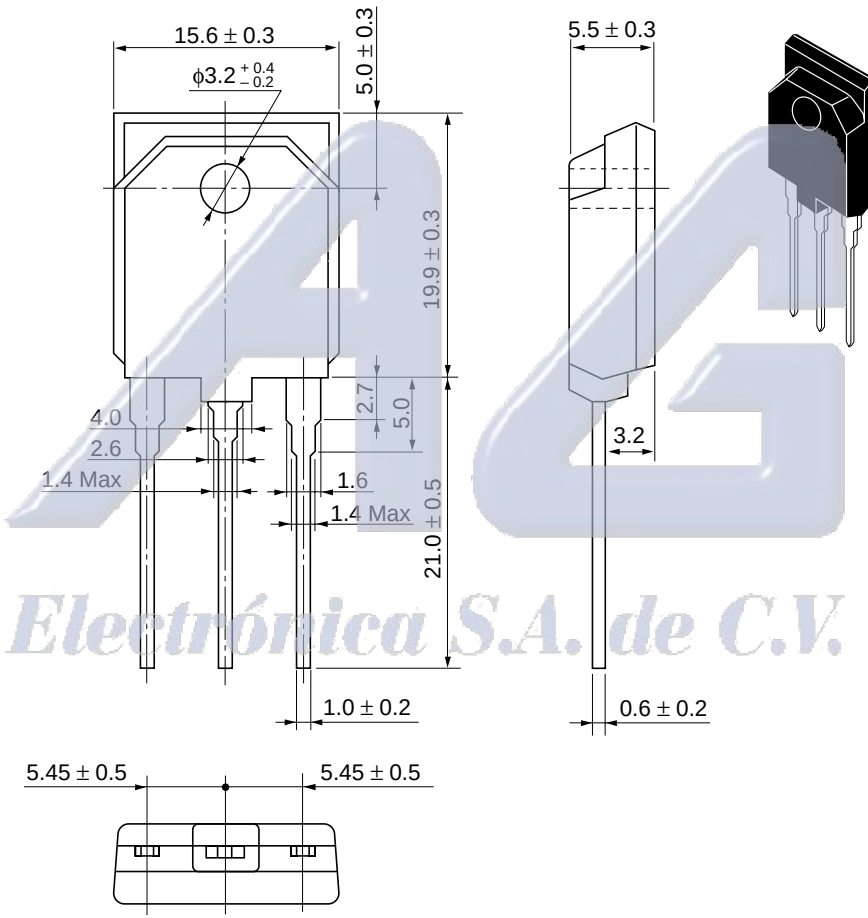
Unit: mm



Hitachi Code	TO-3PFM
JEDEC	—
EIAJ	—
Weight (reference value)	5.6 g

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