

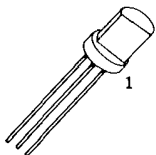
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SPECIAL SILICON PRODUCTS SILICON CONTROL SWITCHES—(SCS)

GE Type	V_{AK} Anode Voltage Blocking (V)	I_F Continuous DC Forward Current (mA)	Peak Recurrent Forward Current @ 100 μ sec (A)	Cathode Gate Peak Current (mA)	P_T (mW)	Cutoff Charac- teristics	Con- ducting Charac- teristics	Max. Gate Ratings	Gate triggering Characteristics					Package Outline No.	Specifi- cation Sheet No.
						I_B @ V_{AK} $R_{GK} = 10K\Omega$ 150°C (μ A)	I_H $R_{GK} = 10K\Omega$ (mA)		V_{GK} $I_{GK} = 20\mu A$ (V)	V_{GA} $I_{GA} = 1\mu A$ (V)	I_{GK} @ $V_{AK} = 40V$, $R_L = 800\Omega$, $R_{GA} = \infty$ (μ A)	V_{GK} (V)	I_{GTA} @ $V_{AK} = 40V$, $R_L = 800\Omega$, $R_{GK} = 10K\Omega$ (mA)		
3N81	65	200	1.0	500	400	20	1.5	5	65	1.0	.4 to .65	1.5	-.4 to -.8	28	65.16
3N82	100	200	1.0	500	400	20	1.5	5	100	1.0	.4 to .65	1.5	-.4 to -.8	28	65.16
3N83	70	50	0.1	50	200	20 *	4.0 †	5	70	150 †	.4 to .80	—	—	28	65.17
3N84	40	175	0.5	100	320	20 *	2.0	5	40	10	.4 to .65	—	—	28	65.18
3N85	100	175	0.5	100	320	20 *	2.0	5	100	10	.4 to .65	—	—	28	65.18
3N86	65	200	1.0	500	400	20	0.2	5	65	1.0	.4 to .65	0.1	-.4 to -.8	28	65.19

* Measured @125°C.

† Measured in special test circuit (See specification sheet).



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SPECIAL SILICON PRODUCTS SILICON ASYMMETRICAL SWITCH (SAS)

A Monolithic Integrated Switch for Bistable Application Where Stability of Switching Voltage is Required

GE Type	V_{S2} Switching Voltage		V_{S1} Switching Voltage		I_{S2}, I_{S1} Switching Current Max. (μ A)	V_{F2} Forward Voltage Drop @ 100 mA Max. (V)	V_{F1} Forward Voltage Drop @ 100 mA Min. (V) Max. (V)		Temperature Coefficient of V_S —55 to +125°C Typical	Package Outline No.	Specification Sheet No.
	Min. (V)	Max. (V)	Min. (V)	Max. (V)			Min. (V)	Max. (V)			
D13H1	7	9	14	18	80	1.6	7	10	.03%/°C	1	65.34

SELECTOR GUIDE—UNIJUNCTIONS, TRIGGERS AND SWITCHES

Device		Unijunctions			Triggers		
		Conventional	Complementary	Programmable	SUS 2N4983-90	SBS 2N4991-93	SAS D13H
		2N489-94, 2N2417-22 2N1671, 2N2160	2N2646 2N2647	D5K1 D5K2	2N6027 2N6028		
Trigger for SCR's	DC, Lo Cost	P	F	P	E	E	N
	DC, Hi Perf.	F	F	F	E	F	N
	DC, Volt Regulator	P	P	F	F	E	N
	DC, Inverter	F	F	E	E	F	N
	DC, Hi $\Delta I/\Delta T$	P	P	P	E ¹	P	N
	AC, ϕ , Hi Perf.	F	F	E	E ¹	F	N
	AC, ϕ , Hi f	F	F	F	E	P	N
	AC, Lo RFI	P	P	F	F	E	N
	AC, ϕ , Lo Cost	P	F	P	E	E	N
Trigger for Triac	AC, ϕ , Hi Perf.	P	P	P	F	P	E ¹
	AC, ϕ , Lo Cost	P	P	P	P	P	E
	AC, Lo RFI	P	P	P	P	P	E
Timers	>1 hr.	F ¹	P	F ¹	E ¹	N	N
	>1 min, Lo Cost	P	F	P	E	N	N
	>1 min, Stable	F	P	E	P	N	N
	<1 min, Lo Cost	P	F	P	E	F	N
	<1 min, Stable	F	P	E	P	F	N
	<10V	P	P	F	E	N	N
	10V-25V	E	E	E	E	F	N
	>25V	P	P	P	E	F	N
	Stability	F	F	E	F	N	N
Oscillators	Cost	P	F	P	E	N	N
	Adjust. Range	E	E	F	F ¹	N	N
	Markets						
Markets	Military	E	P	F	F ²	P	P
	Hi-Rel	E	P	E	F ²	F	P
	Economy	P	F	P	E	E	E

E = Excellent, F = Fair, P = Poor, N = Not Applicable

¹ With additional circuitry² Hermetic version