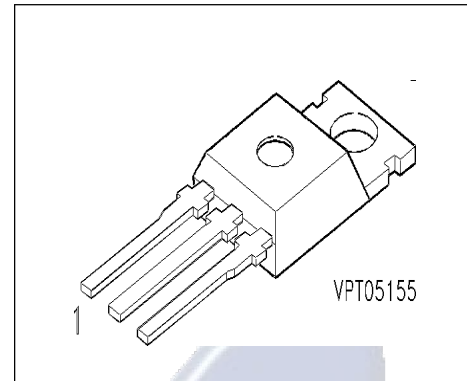


BUP 400

IGBT

Preliminary data

- Low forward voltage drop
- High switching speed
- Low tail current
- Latch-up free
- Avalanche rated



Pin 1	Pin 2	Pin 3
G	C	E

Type	V_{CE}	I_C	Package	Ordering Code
BUP 400	600V	22A	TO-220 AB	C67078-A4403-A2

Maximum Ratings

Parameter	Symbol	Values	Unit
Collector-emitter voltage	V_{CE}	600	V
Collector-gate voltage	V_{CGR}	600	
$R_{GE} = 20 \text{ k}\Omega$			
Gate-emitter voltage	V_{GE}	± 20	
DC collector current	I_C	22	A
$T_C = 25 \text{ }^\circ\text{C}$			
$T_C = 90 \text{ }^\circ\text{C}$		14	
Pulsed collector current, $t_p = 1 \text{ ms}$	I_{Cpuls}	44	
$T_C = 25 \text{ }^\circ\text{C}$			
$T_C = 90 \text{ }^\circ\text{C}$		28	
Avalanche energy, single pulse	E_{AS}	18	mJ
$I_C = 10 \text{ A}$, $V_{CC} = 50 \text{ V}$, $R_{GE} = 25 \text{ }\Omega$			
$L = 350 \text{ }\mu\text{H}$, $T_j = 25 \text{ }^\circ\text{C}$			
Power dissipation	P_{tot}	100	W
$T_C = 25 \text{ }^\circ\text{C}$			
Chip or operating temperature	T_j	-55 ... + 150	$^\circ\text{C}$
Storage temperature	T_{stg}	-55 ... + 150	

Maximum Ratings

Parameter	Symbol	Values	Unit
DIN humidity category, DIN 40 040	-	E	-
IEC climatic category, DIN IEC 68-1	-	55 / 150 / 56	

Thermal Resistance

Thermal resistance, chip case	R_{thJC}	≤ 1.25	K/W
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Electrical Characteristics, at $T_j = 25\text{ }^{\circ}\text{C}$, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	

Static Characteristics

Gate threshold voltage $V_{GE} = V_{CE}, I_C = 0.35\text{ mA}$	$V_{GE(th)}$	4.5	5.5	6.5	V
Collector-emitter saturation voltage $V_{GE} = 15\text{ V}, I_C = 10\text{ A}, T_j = 25\text{ }^{\circ}\text{C}$	$V_{CE(sat)}$	-	2.1	2.7	
$V_{GE} = 15\text{ V}, I_C = 10\text{ A}, T_j = 125\text{ }^{\circ}\text{C}$		-	2.2	2.8	
$V_{GE} = 15\text{ V}, I_C = 20\text{ A}, T_j = 25\text{ }^{\circ}\text{C}$		-	3	-	
$V_{GE} = 15\text{ V}, I_C = 20\text{ A}, T_j = 125\text{ }^{\circ}\text{C}$		-	3.3	-	
Zero gate voltage collector current $V_{CE} = 600\text{ V}, V_{GE} = 0\text{ V}, T_j = 25\text{ }^{\circ}\text{C}$	I_{CES}	-	-	40	μA
Gate-emitter leakage current $V_{GE} = 25\text{ V}, V_{CE} = 0\text{ V}$	I_{GES}	-	-	100	nA

AC Characteristics

Transconductance $V_{CE} = 20\text{ V}, I_C = 10\text{ A}$	g_{fs}	2	-	-	S
Input capacitance $V_{CE} = 25\text{ V}, V_{GE} = 0\text{ V}, f = 1\text{ MHz}$	C_{iss}	-	570	760	pF
Output capacitance $V_{CE} = 25\text{ V}, V_{GE} = 0\text{ V}, f = 1\text{ MHz}$	C_{oss}	-	80	120	
Reverse transfer capacitance $V_{CE} = 25\text{ V}, V_{GE} = 0\text{ V}, f = 1\text{ MHz}$	C_{rss}	-	50	75	

Electrical Characteristics, at $T_j = 25\text{ }^{\circ}\text{C}$, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	

Switching Characteristics, Inductive Load at $T_j = 125\text{ }^{\circ}\text{C}$

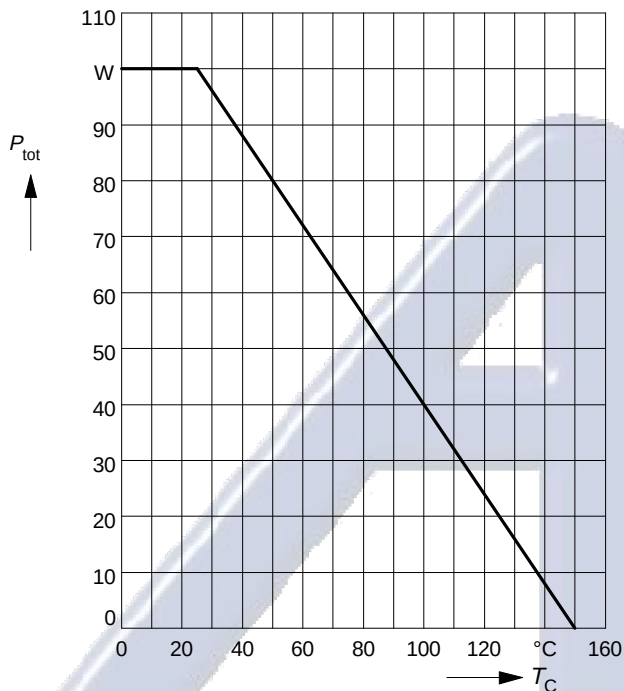
Turn-on delay time $V_{CC} = 300\text{ V}$, $V_{GE} = 15\text{ V}$, $I_C = 10\text{ A}$ $R_{Gon} = 100\text{ }\Omega$	$t_{d(on)}$	-	45	70	ns
Rise time $V_{CC} = 300\text{ V}$, $V_{GE} = 15\text{ V}$, $I_C = 10\text{ A}$ $R_{Gon} = 100\text{ }\Omega$	t_r	-	60	90	
Turn-off delay time $V_{CC} = 300\text{ V}$, $V_{GE} = -15\text{ V}$, $I_C = 10\text{ A}$ $R_{Goff} = 100\text{ }\Omega$	$t_{d(off)}$	-	250	340	
Fall time $V_{CC} = 300\text{ V}$, $V_{GE} = -15\text{ V}$, $I_C = 10\text{ A}$ $R_{Goff} = 100\text{ }\Omega$	t_f	-	500	680	

Electrónica S.A. de C.V.

Power dissipation

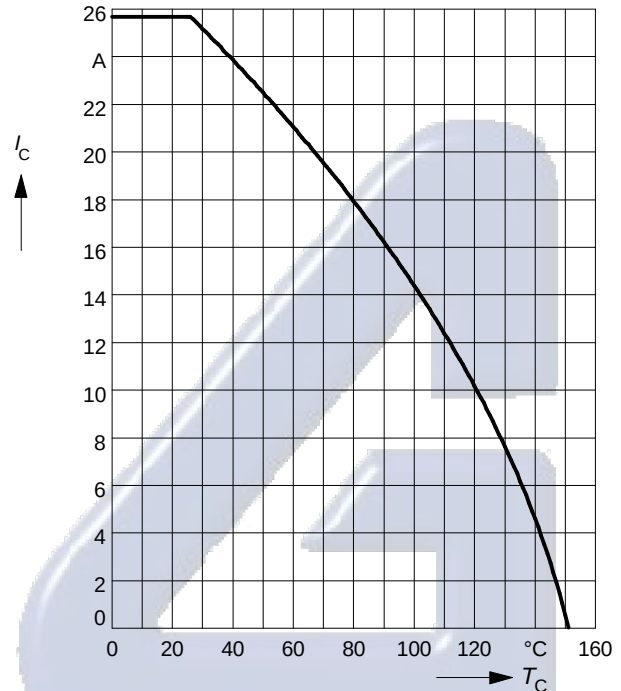
$$P_{\text{tot}} = f(T_C)$$

parameter: $T_j \leq 150^\circ\text{C}$

**Collector current**

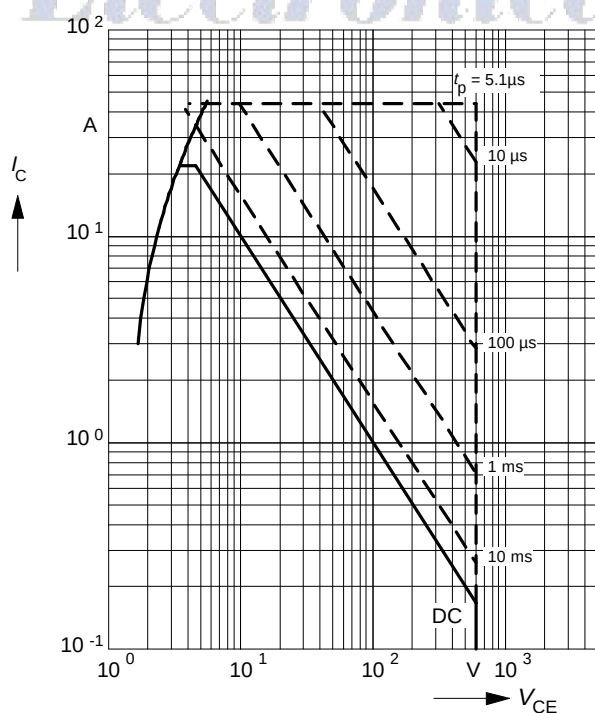
$$I_C = f(T_C)$$

parameter: $V_{GE} \geq 15\text{ V}$, $T_j \leq 150^\circ\text{C}$

**Safe operating area**

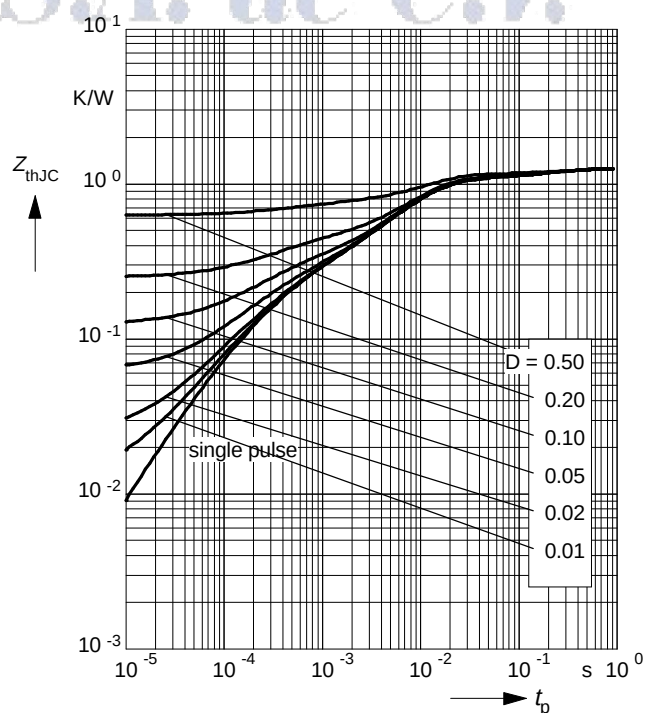
$$I_C = f(V_{CE})$$

parameter: $D = 0$, $T_C = 25^\circ\text{C}$, $T_j \leq 150^\circ\text{C}$

**Transient thermal impedance IGBT**

$$Z_{thJC} = f(t_p)$$

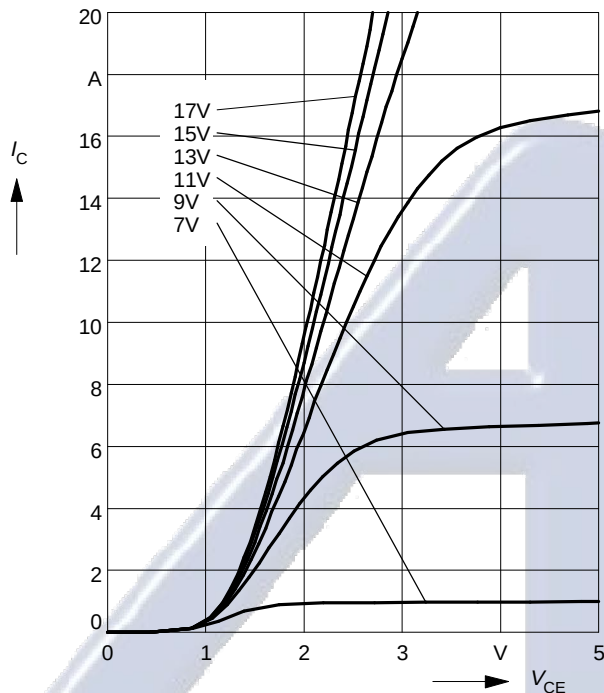
parameter: $D = t_p / T$



Typ. output characteristics

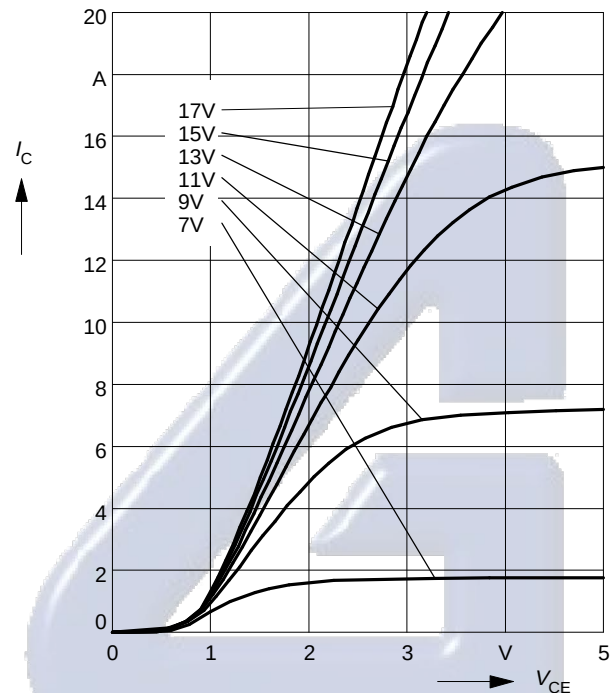
$$I_C = f(V_{CE})$$

parameter: $t_p = 80 \mu s$, $T_j = 25^\circ C$

**Typ. output characteristics**

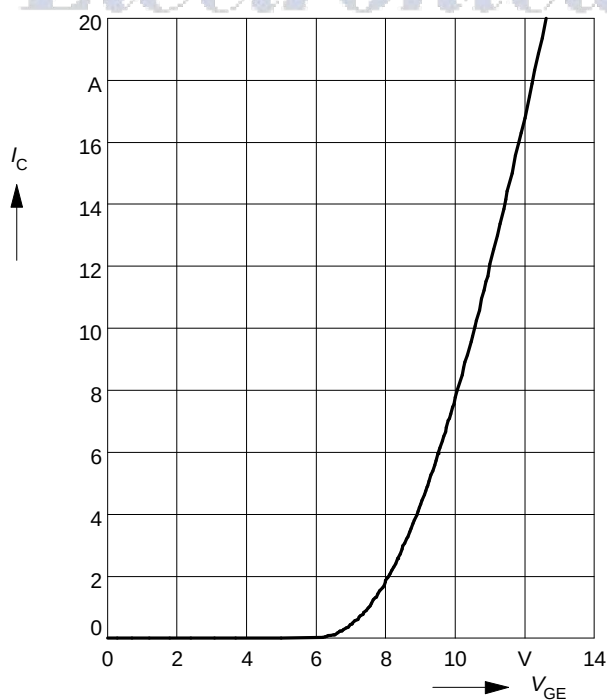
$$I_C = f(V_{CE})$$

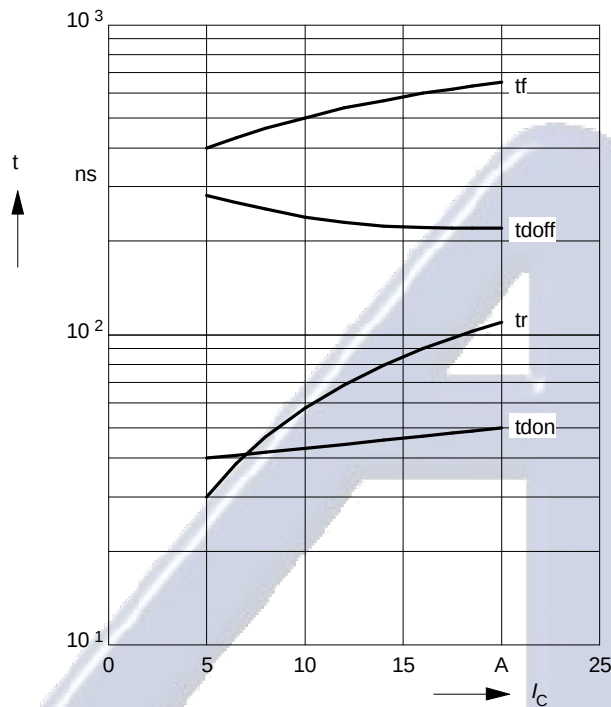
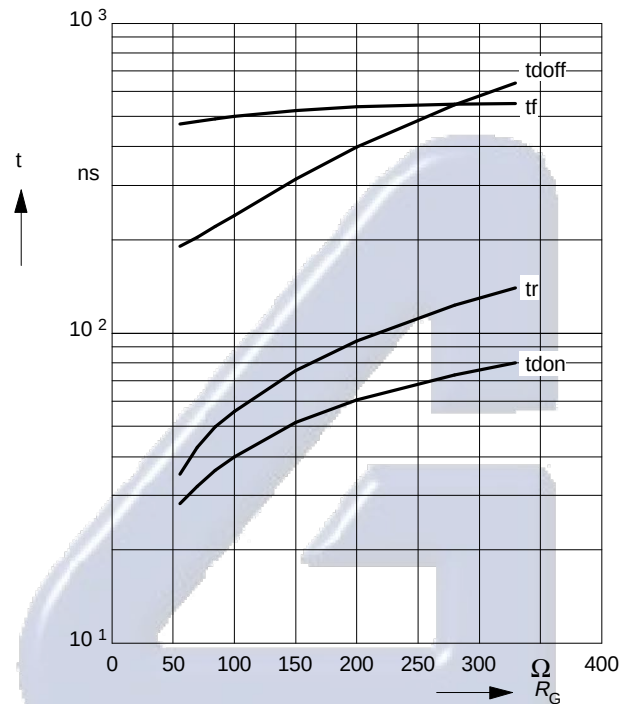
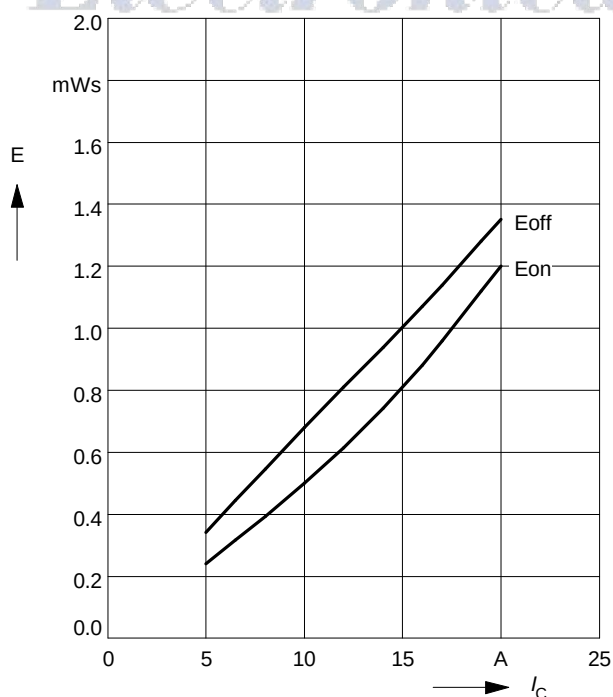
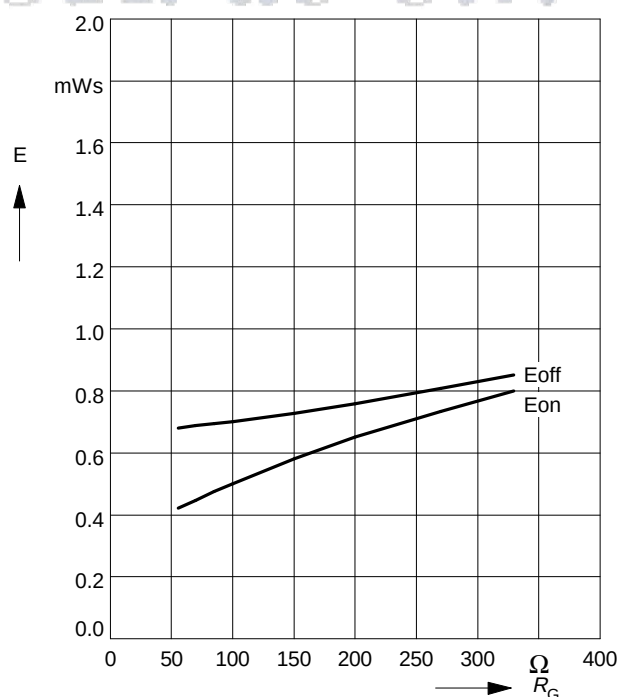
parameter: $t_p = 80 \mu s$, $T_j = 125^\circ C$

**Typ. transfer characteristics**

$$I_C = f(V_{GE})$$

parameter: $t_p = 80 \mu s$, $V_{CE} = 20 V$

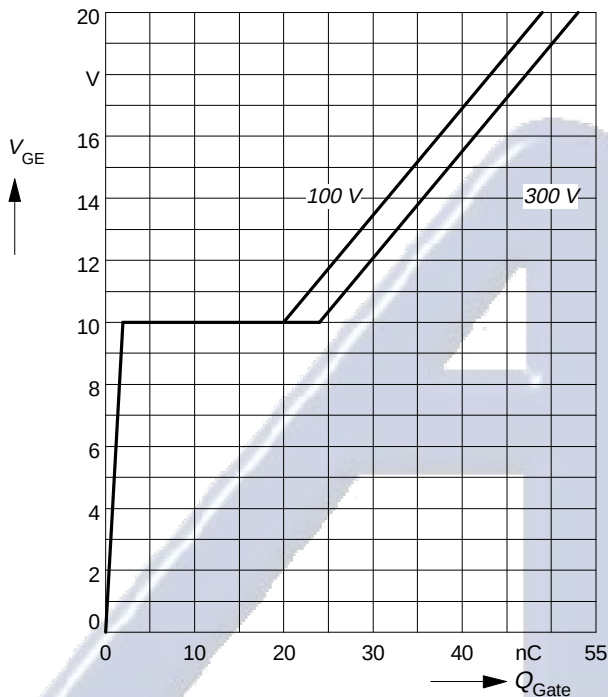


Typ. switching time $t = f(I_C)$, inductive load, $T_j = 125^\circ\text{C}$ par.: $V_{CE} = 300\text{ V}$, $V_{GE} = \pm 15\text{ V}$, $R_G = 100\ \Omega$ **Typ. switching time** $t = f(R_G)$, inductive load, $T_j = 125^\circ\text{C}$ par.: $V_{CE} = 300\text{ V}$, $V_{GE} = \pm 15\text{ V}$, $I_C = 10\text{ A}$ **Typ. switching losses** $E = f(I_C)$, inductive load, $T_j = 125^\circ\text{C}$ par.: $V_{CE} = 300\text{ V}$, $V_{GE} = \pm 15\text{ V}$, $R_G = 100\ \Omega$ **Typ. switching losses** $E = f(R_G)$, inductive load, $T_j = 125^\circ\text{C}$ par.: $V_{CE} = 300\text{ V}$, $V_{GE} = \pm 15\text{ V}$, $I_C = 10\text{ A}$ 

Typ. gate charge

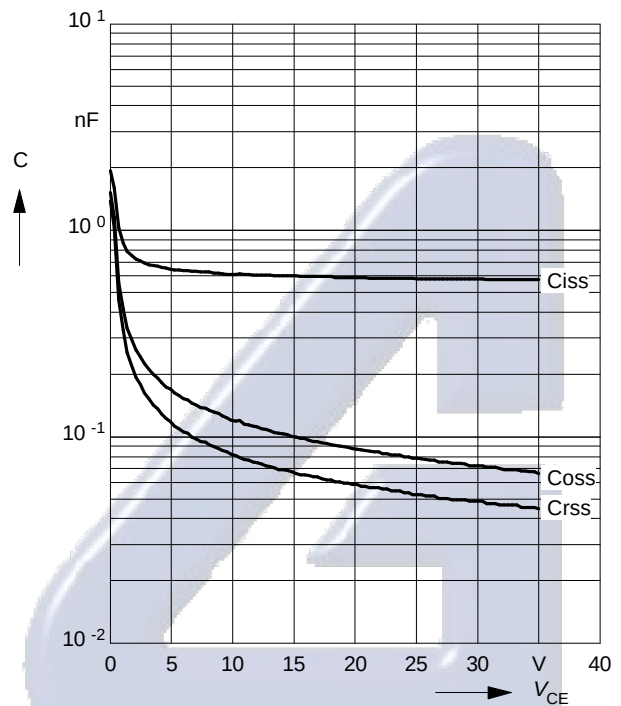
$$V_{GE} = f(Q_{Gate})$$

parameter: $I_{C\ puls} = 10\ A$

**Typ. capacitances**

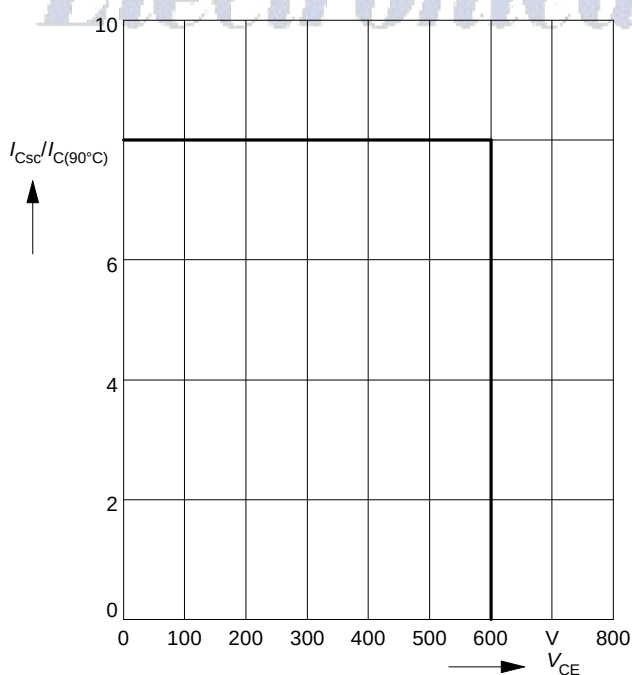
$$C = f(V_{CE})$$

parameter: $V_{GE} = 0\ V, f = 1\ MHz$

**Short circuit safe operating area**

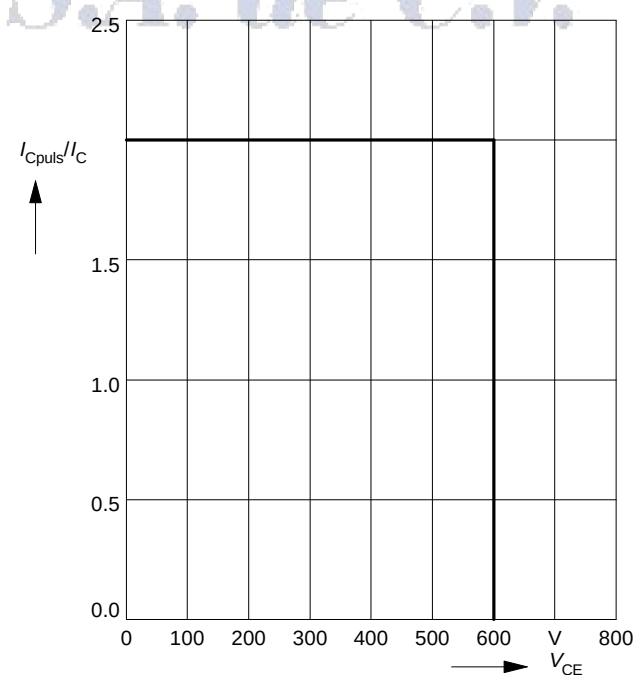
$$I_{Csc} = f(V_{CE}), T_j = 150^\circ C$$

parameter: $V_{GE} = \pm 15\ V, t_{sc} \leq 10\ \mu s, L < 50\ nH$

**Reverse biased safe operating area**

$$I_{Cpuls} = f(V_{CE}), T_j = 150^\circ C$$

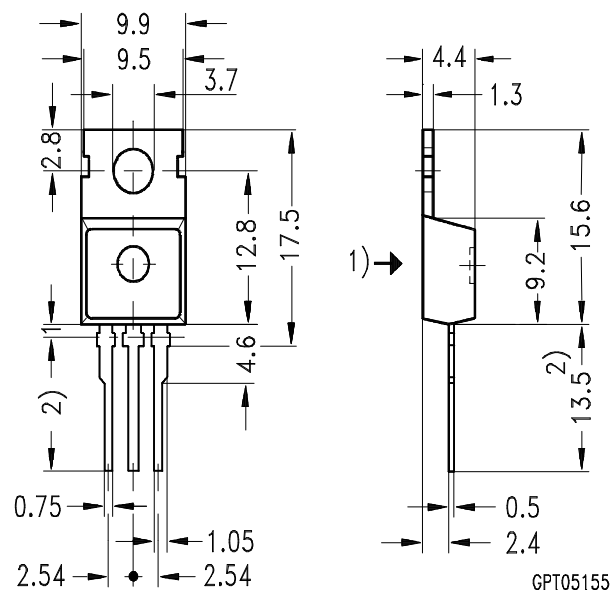
parameter: $V_{GE} = 15\ V$



Package Outlines

Dimensions in mm

Weight:



1) punch direction, burr max. 0.04

2) dip tinning

3) max. 14.5 by dip tinning press burr max. 0.05