HLK-1D1212

CONVERTIDOR DC-DC ENTRADA 5V SALIDA 12V 84mA 1W

DC/DC1W-12V power module selection table
MULTIPLE SPECIFICATIONS AVAILABLE
Constant voltage 12V (±10%Vin) input/isolated unregulated output

<table>
<thead>
<tr>
<th>MODEL</th>
<th>POWER</th>
<th>VOLTAGE</th>
<th>ELECTRICITY</th>
</tr>
</thead>
<tbody>
<tr>
<td>HLK-1D1205</td>
<td>1W</td>
<td>5V</td>
<td>200mA</td>
</tr>
<tr>
<td>HLK-1D1212</td>
<td>1W</td>
<td>12V</td>
<td>84mA</td>
</tr>
<tr>
<td>HLK-1D1215</td>
<td>1W</td>
<td>15V</td>
<td>67mA</td>
</tr>
<tr>
<td>HLK-1D1224</td>
<td>1W</td>
<td>24V</td>
<td>42mA</td>
</tr>
</tbody>
</table>

*The same type of power module package size
*International standard pin, appearance size 11.5×10×6mm

Product features

- Ultra-miniature standard package SIP package and standard pins
- Constant voltage input, isolated unregulated single output, 1W
- Input and output isolation withstand voltage 1500VDC
- Low ripple/noise (20MH bandwidth) < 100mVp-p
- High efficiency, conversion efficiency up to 80% (MAX)
- Operating temperature range: -40°C~+85°C
- Good output short circuit and over current protection and self-recovery
- High reliability, long life design, MTBF≥3500000H
- Potting and sealing with high-quality environmentally friendly waterproof and thermal conductive glue, dustproof, moistureproof, shockproof and flame retardant
- Meet UL/CE/EMC and safety testing requirements
- It can be used in medical, industrial control, electric power, instrumentation, communication, railway and other fields
Product characteristic curve

![Error Envelope Curve](image)

Typical application circuit

Recommended test circuit

If it is required to further reduce the input and output ripple, a capacitor filter network can be connected to the input and output ends. The application circuit is shown in the figure. But care should be taken to select an appropriate filter capacitor. If the capacitance is too large, it may cause startup problems. For each output, under the condition of ensuring safe and reliable operation, the recommended capacitive load value is shown in the attached table.

![Typical Application Circuit](image)
Recommended capacitive load values are detailed (Table 1)

**EMI typical application circuit**

Recommended parameters:

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Vin (Vdc)</td>
<td>5/12/15/24</td>
</tr>
<tr>
<td>Cin</td>
<td>4.7u F/50V</td>
</tr>
<tr>
<td>Count</td>
<td>Refer to Table 1</td>
</tr>
<tr>
<td>Lin</td>
<td>4.7uH</td>
</tr>
<tr>
<td>Lout</td>
<td>4.7uH</td>
</tr>
</tbody>
</table>

Recommended EMI reference circuit values are detailed (Table 2)

**Output load requirement**

In order to ensure that the module can work efficiently and reliably, when in use, its minimum output load cannot be less than 10% of the rated load. If the power you need is really small, please connect a resistor in parallel between the positive and negative poles of the output (the sum of the actual power used by the resistors is greater than or equal to 10% of the rated power and the rated power of the selected resistor must be greater than 5 times the actual power used. Otherwise the temperature of the resistor will be higher).
Ripple & noise test: twisted pair method 20MHz bandwidth

Testing method:

1. Ripple noise is connected using 12# twisted pair, the oscilloscope bandwidth is set to 20MHz, 100M bandwidth probe, and 0.1uF polypropylene capacitor and 47uF high frequency low resistance electrolytic capacitor are connected in parallel to the probe end, and the oscilloscope sampling uses Sample sampling mode.

2. Connect the power input terminal to the input power supply, and the power output is connected to the electronic load through the fixture board. For testing, use a 30cm±2cm sampling line to sample directly from the power output port. The power line selects the corresponding wire diameter wire with insulation according to the output current. (As shown in FIG)
Product application notes

1. Input requirements: ensure that the output voltage fluctuation range of the power supply does not exceed the input requirements of the DC/DC module itself, and the output power of the input power supply must be greater than the output power of the DC/DC module;

2. Recommended circuit 1: For occasions with general requirements for ripple and noise, a filter capacitor can be connected in parallel to the input and output ends. The external circuit is shown in the above figure (1). The recommended value of the filter capacitor is shown in Table (1). Output load requirements: Try to avoid no-load use. When the actual power consumption of the load is less than 10% of the module’s output rated power or there is no-load phenomenon, it is recommended to connect a dummy load to the output end. The dummy load (resistance) can be in accordance with the rated power of the module. 5~10% calculation, resistance value=Uout/(1W*10%);

3. Overload protection: Under normal working conditions, the output circuit of this product has no protection against overload. The easiest way is to connect a self-recovery fuse in series at the input end, or add a circuit breaker to the circuit;

4. The capacitance value of the external capacitor at the output end should not be too large, otherwise it will easily cause overcurrent or poor startup when the module starts;

5. If the product works below the minimum required load, it cannot be guaranteed that the product performance meets all the performance indicators in this manual;

6. The maximum capacitive load is tested under the input voltage range and full load conditions;

7. Except for special instructions, all indicators in this manual are measured at Ta=25°C/humidity < 75%RH, nominal input voltage and output rated load;

8. All index test methods in this product manual are based on our company's standards;

9. Our company can provide product customization, and you can directly contact our technical staff for specific information;

10. Product specifications are subject to change without notice.
**Dimensions**

Front View:
- **11.50 (0.453)**
- **10.00 (0.394)**
- **1.94 (0.076)**

Side View:
- **6.00 (0.236)**
- **4.10 (0.161)**
- **0.90 (0.035)**

UNIT: mm
Printed board vertical view
Lattic spacing: 2.54mm (0.1inch)
Unmarked dimensional tolerances ± 0.25mm

**Pin description:**

1 : GND  ;  2 : +Vin  ;  3 : -Vo  ;  4 : +Vo  ;

*Note: If the definition of each pin of the power module is not consistent with the selection manual, the marking on the physical label shall prevail.*