



# Multi-System Power Supply for Audio Equipment

### Overview

The LA5617 is a multi-system power supply IC with a built-in on/off control function. It is optimal for use as the power supply IC in CD players, mini-component stereo systems, and other microcontroller controlled audio equipment.

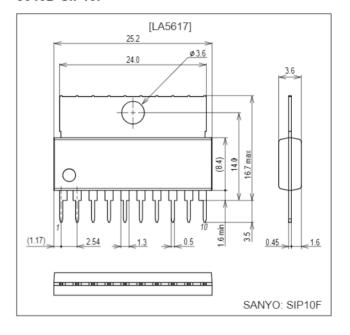
### **Functions**

- Power supply IC with ±7.5 V outputs (±1.5 A) and an on/off control function.
- The LA5617 is pin compatible with the LA5618.

## **Package Dimensions**

unit: mm

#### 3046D-SIP10F



## **Specifications**

Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Input voltage	V <sub>CC</sub> /V <sub>EE</sub> max		±18	V
Allowable power dissipation	Pd max	With no heat sink	2.0	W
Operating temperature	Topr		-20 to +85	°C
Storage temperature	Tstg		-55 to +150	°C

Note: On a glass epoxy printed circuit board (114.3 · 76.1 · 1.6 mm)

#### Operating Conditions at Ta = 25°C

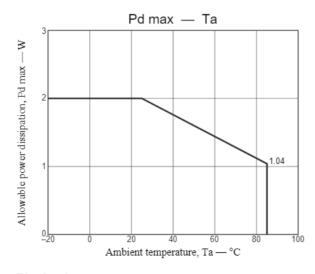
Parameter	Symbol	Conditions	Ratings	Unit
Input voltage	V <sub>CC</sub> /V <sub>EE</sub>		±9.5 to ±16	V
Output current	I <sub>OUT</sub> 1		0 to 1.5	А
Output current	I <sub>OUT</sub> 2		-1.5 to 0	А

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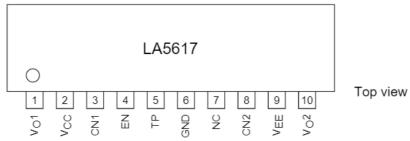
### LA5617

# Operating Characteristics at Ta= 25°C, $V_{CC}/V_{EE}$ = $\pm 9.5$ V, in the specified test circuit.

Parameter	Cumbel	Conditions		Ratings		
Parameter	Symbol	cool Conditions		typ	max	Unit
[+7.5 V Power Supply Block] I <sub>OUT</sub> 1	= 500 mA, C	<sub>OUT</sub> 1 = 100 μF				
Output voltage	V <sub>O</sub> 1		7.0	7.5	8.0	V
Dropout voltage -	V <sub>DROP</sub> 1-1			1.5	2.0	V
	V <sub>DROP</sub> 1-2	I <sub>OUT</sub> 1 = 300 mA		1.0	1.5	V
Line regulation	ΔV <sub>OLN</sub> 1	9 V ≤ V <sub>CC</sub> ≤ 16 V		20	100	mV
Load regulation	ΔV <sub>OLD</sub> 1	5 mA ≤ I <sub>OUT</sub> 1 ≤ 1 A		80	200	mV
Peak output current	I <sub>OP</sub> 1	V <sub>CC</sub> /V <sub>EE</sub> = ±12 V	1.5	1.8		А
Output short current	I <sub>OSC</sub> 1			1.0		Α
Output off voltage	V <sub>O</sub> 1 <sub>OFF</sub>	V <sub>EN</sub> = 0.4 V			0.3	V
Ripple rejection	Rrej1	f = 120 Hz, $8.5 \text{ V} \le \text{V}_{CC} \le 16 \text{ V}$ , CN1 = 1 $\mu\text{F}$		65		dB
[-7.5 V Power Supply Block] I <sub>OUT</sub> 2	= 500 mA, C	OUT <sup>2</sup> = 100 μF				
Output voltage	V <sub>O</sub> 2		-8.0	-7.5	-7.0	V
Dropout voltage	V <sub>DROP</sub> 2-1			1.5	2.0	V
	V <sub>DROP</sub> 2-2	I <sub>OUT</sub> 2 = -300 mA		1.0	1.5	V
Line regulation	ΔV <sub>OLN</sub> 2	-16 V ≤ V <sub>EE</sub> ≤ -9 V		200	300	mV
Load regulation	ΔV <sub>OLD</sub> 2	-1 A ≤ I <sub>OUT</sub> 2 ≤ -5 mA		80	200	mV
Peak output current	I <sub>OP</sub> 2	V <sub>CC</sub> /V <sub>EE</sub> = ±12 V		-1.8	-1.5	А
Output short current	I <sub>OSC</sub> 2			-1.0		Α
Output off voltage	V <sub>O</sub> 2 off	V <sub>EN</sub> = 0.4 V	-0.3			V
Ripple rejection	Rrej2	$f = 120 \text{ Hz}, 16 \text{ V} \le \text{V}_{\text{EE}} \le -8.5 \text{ V}, \text{CN2} = 1 \mu\text{F}$		50		dB
[Common Circuit Block] C <sub>OUT</sub> 1 = 10	00 μF, C <sub>OUT</sub> 2	2 = 100 μF				
Output off control voltage	V <sub>ENL</sub>	V <sub>O</sub> 1, V <sub>O</sub> 2: Off			0.4	V
Current drain (positive voltage power supply block)	I <sub>QP</sub> 1	I <sub>OUT</sub> 1 = 0, I <sub>OUT</sub> 2 = 0		5.0		mA
	I <sub>QP</sub> 2	I <sub>OUT</sub> 1 = 1.5 A, I <sub>OUT</sub> 2 = 0		7.0		mA
Current drain (negative voltage power supply block)	I <sub>QM</sub> 1	I <sub>OUT</sub> 1 = 0, I <sub>OUT</sub> 2 = 0		-5.0		mA
	I <sub>QM</sub> 2	I <sub>OUT</sub> 1 = 0, I <sub>OUT</sub> 2 = -1.5 A		-12.0		mA

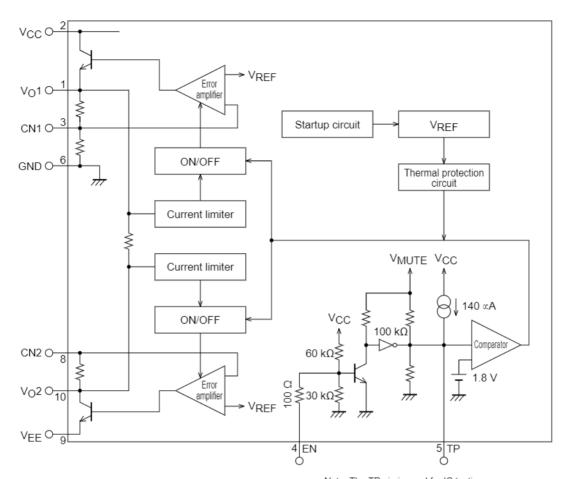


## Pin Assignment



Note: The TP pin is used for IC testing. It must be left open during normal operation.

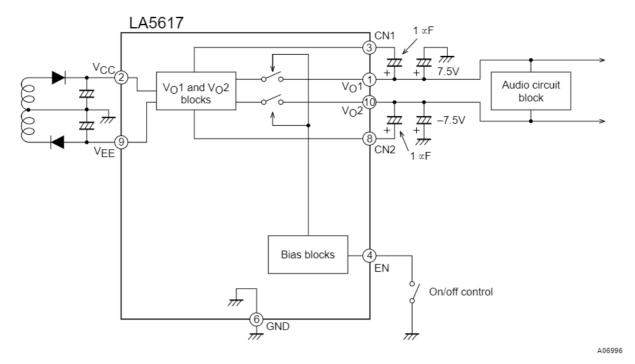
## **Equivalent Circuit Block Diagram**



Note: The TP pin is used for IC testing. It must be left open during normal operation.

A06995

### Sample Application Circuit: Mini-component stereo system power supply



Notes: 1. A capacitor with a low temperature coefficient must be used as the EN DELAY delay capacitor.

- The V<sub>O</sub>1 and V<sub>O</sub>2 output capacitors must have values of at least 100 μF and capacitors with low temperature coefficients must be used to prevent oscillation at low temperatures.
- 3. External noise can be suppressed and ripple rejection improved by adding capacitors between CN1 and V<sub>0</sub>1 and between CN2 and V<sub>0</sub>2.
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