LDO Voltage Regulator -Adjustable Output, Load Dump Protection

60 V, 100 mA

The LM2931 series consists of positive fixed and adjustable output voltage regulators that are specifically designed to maintain proper regulation with an extremely low input-to-output voltage differential. These devices are capable of supplying output currents in excess of 100 mA and feature a low bias current of 0.4 mA at 10 mA output.

Designed primarily to survive in the harsh automotive environment, these devices will protect all external load circuitry from input fault conditions caused by reverse battery connection, two battery jump starts, and excessive line transients during load dump. This series also includes internal current limiting, thermal shutdown, and additionally, is able to withstand temporary power-up with mirror-image insertion.

Due to the low dropout voltage and bias current specifications, the LM2931 series is ideally suited for battery powered industrial and consumer equipment where an extension of useful battery life is desirable. The 'C' suffix adjustable output regulators feature an output inhibit pin which is extremely useful in microprocessor-based systems.

Features

- Input-to-Output Voltage Differential of < 0.6 V @ 100 mA
- Output Current in Excess of 100 mA
- Low Bias Current
- 60 V Load Dump Protection
- -50 V Reverse Transient Protection
- Internal Current Limiting with Thermal Shutdown
- Temporary Mirror-Image Protection
- Ideally Suited for Battery Powered Equipment
- Economical 5-Lead TO-220 Package with Two Optional Leadforms
- Available in Surface Mount SOP-8, D²PAK and DPAK Packages
- High Accuracy (±2.5%) Reference (LM2931AC) Available
- NCV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable
- Pb-Free Packages are Available

Applications

- Battery Powered Consumer Products
- Hand-held Instruments
- Camcorders and Cameras



ON Semiconductor®

http://onsemi.com

FIXED OUTPUT VOLTAGE

TO-92 **Z SUFFIX CASE 29**



Pin 1. Output 2. Ground 3. Input

T SUFFIX CASE 221AB

DΡΔK

DT SUFFIX

CASE 369C

TO-220



Pin 1. Input

D²PAK **D2T SUFFIX CASE 936**



2. Ground 3. Output

SOT-223 ST SUFFIX **CASE 318H**



ADJUSTABLE OUTPUT VOLTAGE

TO-220 **TH SUFFIX CASE 314A**



TO-220 **TV SUFFIX CASE 314B**



TO-220 T SUFFIX CASE 314D



D²PAK **D2T SUFFIX CASE 936A**



Pin 1. Adjust

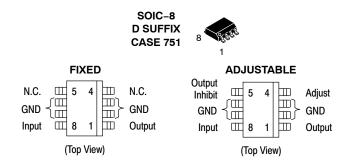
- 2. Output Inhibit
- 3. Ground 4. Input
- 5. Output

ORDERING INFORMATION

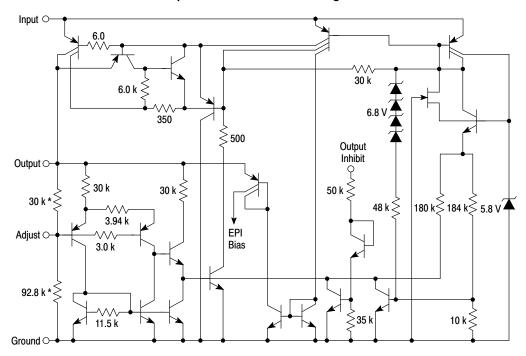
See detailed ordering and shipping information in the package dimensions section on page 12 of this data sheet.

DEVICE MARKING INFORMATION

See general marking and heatsink information in the device marking section on page 15 of this data sheet.



Representative Schematic Diagram



^{*}Deleted on Adjustable Regulators

This device contains 26 active transistors.

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Input Voltage Continuous	VI	40	Vdc
Transient Input Voltage ($\tau \le 100 \text{ ms}$)	V _I (τ)	60	Vpk
Transient Reverse Polarity Input Voltage 1.0% Duty Cycle, τ ≤ 100 ms	-V _I (τ)	-50	Vpk
Electrostatic Discharge Sensitivity (ESD) Human Body Model (HBM) Class 2, JESD22 A114-C Machine Model (MM) Class A, JESD22 A115-A Charged Device Model (CDM), JESD22 C101-C	- - -	2000 200 2000	V V
Power Dissipation Case 29 (TO-92 Type) T _A = 25°C Thermal Resistance, Junction-to-Ambient Thermal Resistance, Junction-to-Case Case 221A, 314A, 314B and 314D (TO-220 Type)	P _D R _{θJA} R _{θJC}	Internally Limited 178 83	W °C/W °C/W
T _A = 25°C Thermal Resistance, Junction-to-Ambient Thermal Resistance, Junction-to-Case Case 318H (SOT-223)	P _D R _{θJA} R _{θJC}	Internally Limited 65 5.0	W °C/W °C/W
T _A = 25°C Thermal Resistance, Junction-to-Ambient Thermal Resistance, Junction-to-Case	P _D R _{θJA} R _{θJC}	Internally Limited 242 21	W °C/W °C/W
Case 369A (DPAK) (Note 1) T _A = 25°C Thermal Resistance, Junction-to-Ambient Thermal Resistance, Junction-to-Case	P _D R _{θJA} R _{θJC}	Internally Limited 92 6.0	W °C/W °C/W
Case 751 (SOP-8) (Note 2) T _A = 25°C Thermal Resistance, Junction-to-Ambient Thermal Resistance, Junction-to-Case	P _D R _{θJA} R _{θJC}	Internally Limited 160 25	W °C/W °C/W
Case 936 and 936A (D ² PAK) (Note 3) T _A = 25°C Thermal Resistance, Junction-to-Ambient Thermal Resistance, Junction-to-Case	P _D R _{θJA} R _{θJC}	Internally Limited 70 5.0	W °C/W °C/W
Operating Ambient Temperature Range	T _A	-40 to +125	°C
Operating Die Junction Temperature	T _J	+150	°C
Storage Temperature Range	T _{stg}	-65 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

- 1. DPAK Junction-to-Ambient Thermal Resistance is for vertical mounting. Refer to Figure 25 for board mounted Thermal Resistance.
- 2. SOP-8 Junction-to-Ambient Thermal Resistance is for minimum recommended pad size. Refer to Figure 24 for Thermal Resistance variation versus pad size.
- 3. D²PAK Junction to Ambient Thermal Resistance is for vertical mounting. Refer to Figure 26 for board mounted Thermal Resistance.
- 4. NCV rated devices are subjected to and meet the AECQ-100 quality standards.

$\textbf{ELECTRICAL CHARACTERISTICS} \ (V_{in} = 14 \ V, \ I_O = 10 \ mA, \ C_O = 100 \ \mu F, \ C_{O(ESR)} = 0.3 \ \Omega, \ T_A = 25^{\circ}C \ [Note \ 5])$

		LM2931-5.0/NCV2931-5.0		LM2931A	-5.0/NCV2	931A-5.0		
Characteristic	Symbol	Min	Тур	Max	Min	Тур	Max	Unit
FIXED OUTPUT		•		•			•	
Output Voltage	Vo							V
V _{in} = 14 V, I _O = 10 mA, T _A = 25°C		4.75	5.0	5.25	4.81	5.0	5.19	
V_{in} = 6.0 V to 26 V, $I_O \le$ 100 mA, $T_A = -40^\circ$ to +125°C		4.50	-	5.50	4.75	-	5.25	
Line Regulation	Reg _{line}							mV
V _{in} = 9.0 V to 16 V		_	2.0	10	-	2.0	10	
V _{in} = 6.0 V to 26 V		_	4.0	30	-	4.0	30	
Load Regulation (I _O = 5.0 mA to 100 mA)	Reg _{load}	_	14	50	-	14	50	mV
Output Impedance	Z _O							mΩ
I_{O} = 10 mA, ΔI_{O} = 1.0 mA, f = 100 Hz to 10 kHz		_	200	-	-	200	-	
Bias Current	Ι _Β							mA
V _{in} = 14 V, I _O = 100 mA, T _A = 25°C		_	5.8	30	-	5.8	30	
V_{in} = 6.0 V to 26 V, I_O = 10 mA, T_A = -40° to $+125^{\circ}C$		_	0.4	1.0	-	0.4	1.0	
Output Noise Voltage (f = 10 Hz to 100 kHz)	V _n	_	700	-	-	700	_	μVrms
Long Term Stability	S	_	20	-	-	20	_	mV/kHR
Ripple Rejection (f = 120 Hz)	RR	60	90	-	60	90	_	dB
Dropout Voltage	V_I – V_O							V
I _O = 10 mA		_	0.015	0.2	-	0.015	0.2	
I _O = 100 mA		-	0.16	0.6	-	0.16	0.6	
Over-Voltage Shutdown Threshold	V _{th(OV)}	26	29.5	40	26	29.5	40	V
Output Voltage with Reverse Polarity Input $(V_{in} = -15 \text{ V})$	-V _O	-0.3	0	-	-0.3	0	-	V

^{5.} Low duty cycle pulse techniques are used during test to maintain junction temperature as close to ambient as possible.6. NCV devices are qualified for automotive use.

 $\textbf{ELECTRICAL CHARACTERISTICS} \ (V_{in} = 14 \ V, \ I_O = 10 \ \text{mA}, \ C_O = 100 \ \mu\text{F}, \ C_{O(ESR)} = 0.3 \ \Omega, \ T_A = 25^{\circ}\text{C} \ [\text{Note 7}])$

		LM2931C/NCV2931C		LM2931AC/NCV2931AC				
Characteristic	Symbol	Min	Тур	Max	Min	Тур	Max	Unit
ADJUSTABLE OUTPUT								
Reference Voltage (Note 8, Figure 18) $I_O = 10 \text{ mA}, T_A = 25^{\circ}\text{C}$ $I_O \leq 100 \text{ mA}, T_A = -40 \text{ to } +125^{\circ}\text{C}$	V _{ref}	1.14 1.08	1.20 –	1.26 1.32	1.17 1.15	1.20 -	1.23 1.25	V
Output Voltage Range	V _{O range}	3.0 to 24	2.7 to 29.5	-	3.0 to 24	2.7 to 29.5	-	V
Line Regulation (V _{in} = V _O + 0.6 V to 26 V)	Reg _{line}	-	0.2	1.5	-	0.2	1.5	mV/V
Load Regulation (I _O = 5.0 mA to 100 mA)	Reg _{load}	-	0.3	1.0	-	0.3	1.0	%/V
Output Impedance $I_O = 10$ mA, $\Delta I_O = 1.0$ mA, $f = 10$ Hz to 10 kHz	Z _O	-	40	-	-	40	ı	mΩ/V
Bias Current $I_{O} = 100 \text{ mA}$ $I_{O} = 10 \text{ mA}$ Output Inhibited $(V_{th(OI)} = 2.5 \text{ V})$	I _B	- -	6.0 0.4 0.2	- 1.0 1.0	- - -	6.0 0.4 0.2	- 1.0 1.0	mA
Adjustment Pin Current	I_{Adj}	-	0.2	-	-	0.2	-	μΑ
Output Noise Voltage (f = 10 Hz to 100 kHz)	V _n	-	140	-	-	140	-	μVrms/V
Long-Term Stability	S	-	0.4	-	-	0.4	-	%/kHR
Ripple Rejection (f = 120 Hz)	RR	0.10	0.003	-	0.10	0.003	-	%/V
Dropout Voltage $I_O = 10 \text{ mA}$ $I_O = 100 \text{ mA}$	V _I –V _O	- -	0.015 0.16	0.2 0.6	- -	0.015 0.16	0.2 0.6	V
Over-Voltage Shutdown Threshold	$V_{th(OV)}$	26	29.5	40	26	29.5	40	V
Output Voltage with Reverse Polarity Input (V _{in} = -15 V)	-V _O	-0.3	0	-	-0.3	0	-	V
Output Inhibit Threshold Voltages Output "On": $T_A = 25^{\circ}C$ $T_A = -40^{\circ}$ to +125°C Output "Off": $T_A = 25^{\circ}C$ $T_A = -40^{\circ}$ to +125°C	V _{th(OI)}	- - 2.50 3.25	2.15 - 2.26 -	1.90 1.20 - -	- - 2.50 3.25	2.15 - 2.26 -	1.90 1.20 - -	V
Output Inhibit Threshold Current (V _{th(OI)} = 2.5 V)	I _{th(OI)}	_	30	50	-	30	50	μΑ

Low duty cycle pulse techniques are used during test to maintain junction temperature as close to ambient as possible.
 The reference voltage on the adjustable device is measured from the output to the adjust pin across R₁.

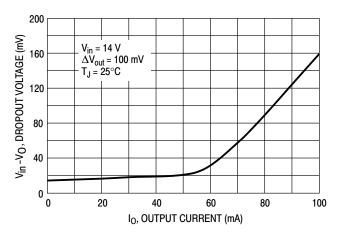


Figure 1. Dropout Voltage versus Output Current

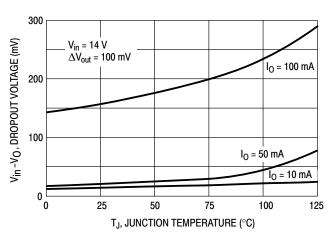


Figure 2. Dropout Voltage versus Junction Temperature

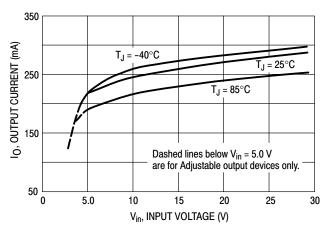


Figure 3. Peak Output Current versus Input Voltage

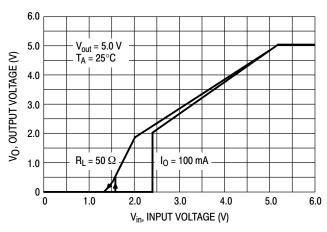


Figure 4. Output Voltage versus Input Voltage

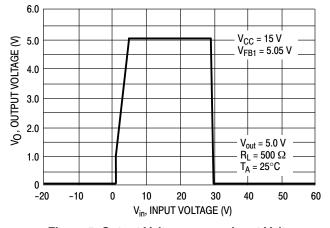


Figure 5. Output Voltage versus Input Voltage

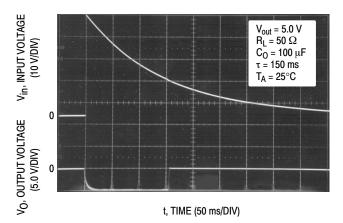


Figure 6. Load Dump Characteristics

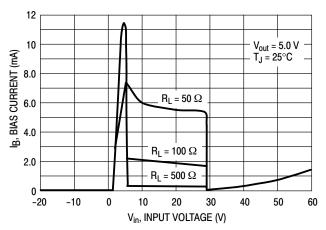


Figure 7. Bias Current versus Input Voltage

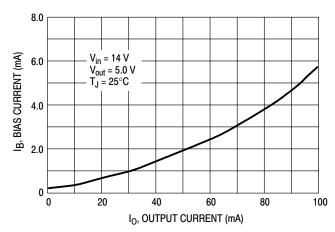


Figure 8. Bias Current versus Output Current

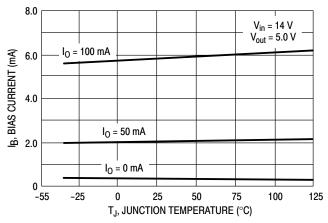


Figure 9. Bias Current versus Junction Temperature

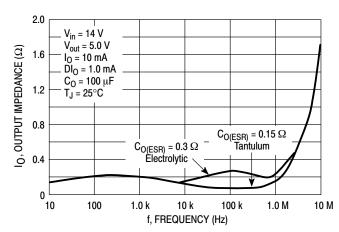


Figure 10. Output Impedance versus Frequency

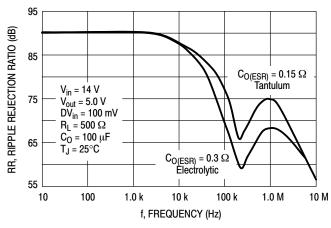


Figure 11. Ripple Rejection versus Frequency

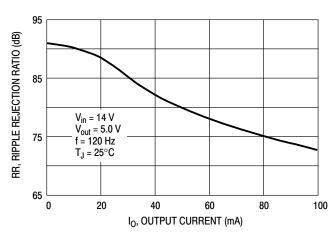


Figure 12. Ripple Rejection versus Output Current

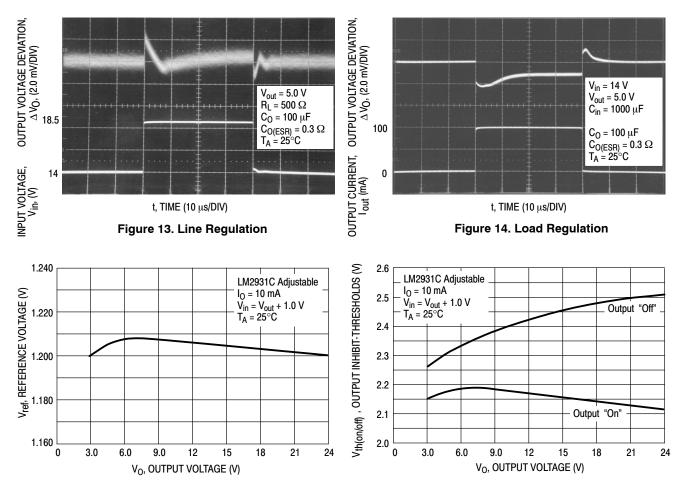


Figure 15. Reference Voltage versus Output Voltage

Figure 16. Output Inhibit-Thresholds versus Output Voltage

APPLICATIONS INFORMATION

The LM2931 series regulators are designed with many protection features making them essentially blow-out proof. These features include internal current limiting, thermal shutdown, overvoltage and reverse polarity input protection, and the capability to withstand temporary power-up with mirror-image insertion. Typical application circuits for the fixed and adjustable output device are shown in Figures 17 and 18.

The input bypass capacitor C_{in} is recommended if the regulator is located an appreciable distance ($\geq 4''$) from the supply input filter. This will reduce the circuit's sensitivity to the input line impedance at high frequencies.

This regulator series is not internally compensated and thus requires an external output capacitor for stability. The capacitance value required is dependent upon the load current, output voltage for the adjustable regulator, and the type of capacitor selected. The least stable condition is encountered at maximum load current and minimum output voltage. Figure 22 shows that for operation in the "Stable" region, under the conditions specified, the magnitude of the output capacitor impedance $|Z_O|$ must not exceed 0.4 Ω . This

limit must be observed over the entire operating temperature range of the regulator circuit.

With economical electrolytic capacitors, cold temperature operation can pose a serious stability problem. As the electrolyte freezes, around $-30^{\circ}\mathrm{C}$, the capacitance will decrease and the equivalent series resistance (ESR) will increase drastically, causing the circuit to oscillate. Quality electrolytic capacitors with extended temperature ranges of -40° to $+85^{\circ}\mathrm{C}$ and -55° to $+105^{\circ}\mathrm{C}$ are readily available. Solid tantalum capacitors may be a better choice if small size is a requirement, however, the maximum $\left|Z_{\mathrm{O}}\right|$ limit over temperature must be observed.

Note that in the stable region, the output noise voltage is linearly proportional to $\left| \left. Z_O \right| \right|$. In effect, C_O dictates the high frequency roll–off point of the circuit. Operation in the area titled "Marginally Stable" will cause the output of the regulator to exhibit random bursts of oscillation that decay in an under–damped fashion. Continuous oscillation occurs when operating in the area titled "Unstable". It is suggested that oven testing of the entire circuit be performed with maximum load, minimum input voltage, and minimum ambient temperature.

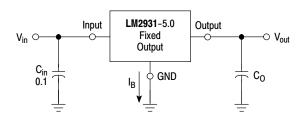
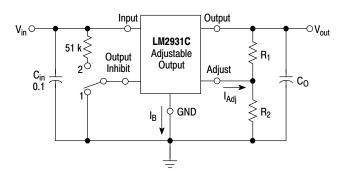


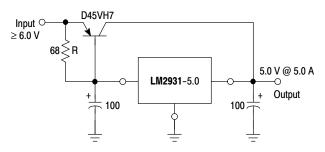
Figure 17. Fixed Output Regulator



Switch Position 1 = Output "On", 2 = Output "Off"

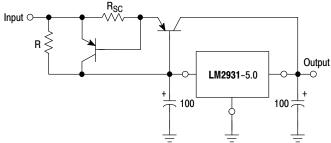
$$V_{out} \ = \ V_{ref} \left(1 \ + \ \frac{R_2}{R_1} \right) \ + \ I_{Adj} \, R_2 \qquad 22.5 \, k \ \ge \ \frac{R_1 \, R_2}{R_1 \ + \ R_2}$$

Figure 18. Adjustable Output Regulator



The LM2931 series can be current boosted with a PNP transistor. The D45VH7, on a heatsink, will provide an output current of 5.0 A with an input to output voltage differential of approximately 1.0 V. Resistor R in conjunction with the V_{BE} of the PNP determines when the pass transistor begins conducting. This circuit is not short circuit proof.

Figure 19. (5.0 A) Low Differential Voltage Regulator



The circuit of Figure 19 can be modified to provide supply protection against short circuits by adding the current sense resistor R_{SC} and an additional PNP transistor. The current sensing PNP must be capable of handling the short circuit current of the LM2931. Safe operating area of both transistors must be considered under worst case conditions.

Figure 20. Current Boost Regulator with Short Circuit Projection

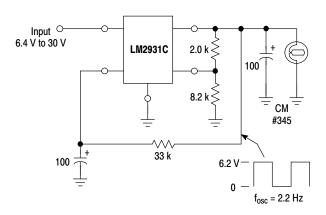
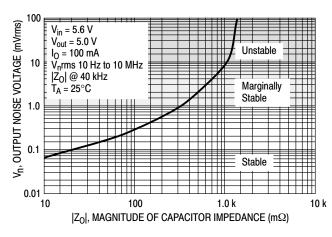


Figure 21. Constant Intensity Lamp Flasher



10 μF 22 μF 0.50 Unstable **47** μF 100 μΕ 0.40 ESR (Ohms) 0.30 Stable 0.20 Note - Optimum stability uses a 22 μ F 0.10 output capacitor. Output capacitor values below 10 µF are not recommended. 0.00 10 20 30 40 50 60 70 80 90 100 0 **OUTPUT CURRENT (mA)**

Figure 22. Output Noise Voltage vs.
Output Capacitor Impedance

Figure 23. Output Capacitor ESR Stability vs.
Output Load Current

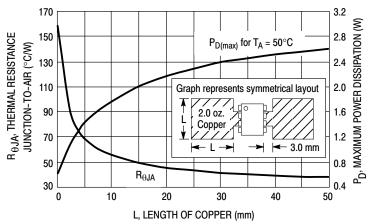


Figure 24. SOP-8 Thermal Resistance and Maximum Power Dissipation versus P.C.B. Copper Length

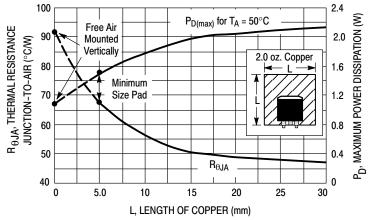


Figure 25. DPAK Thermal Resistance and Maximum Power Dissipation versus P.C.B. Copper Length

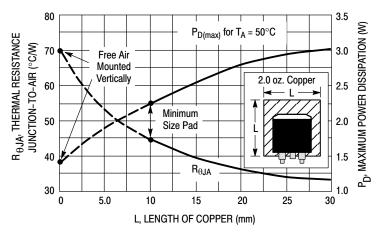


Figure 26. 3-Pin and 5-Pin D²PAK
Thermal Resistance and Maximum Power
Dissipation versus P.C.B. Copper Length

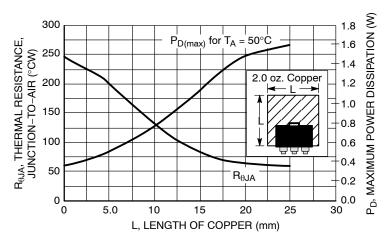


Figure 27. SOT-223 Thermal Resistance and Maximum Power Dissipation vs. P.C.B. Copper Length

DEFINITIONS

Dropout Voltage – The input/output voltage differential at which the regulator output no longer maintains regulation against further reductions in input voltage. Measured when the output decreases 100 mV from nominal value at 14 V input, dropout voltage is affected by junction temperature and load current.

Line Regulation – The change in output voltage for a change in the input voltage. The measurement is made under conditions of low dissipation or by using pulse techniques such that the average chip temperature is not significantly affected.

Load Regulation – The change in output voltage for a change in load current at constant chip temperature.

Maximum Power Dissipation – The maximum total device dissipation for which the regulator will operate within specifications.

Bias Current – That part of the input current that is not delivered to the load.

Output Noise Voltage – The rms AC voltage at the output, with constant load and no input ripple, measured over a specified frequency range.

Long-Term Stability – Output voltage stability under accelerated life test conditions with the maximum rated voltage listed in the devices electrical characteristics and maximum power dissipation.

ORDERING INFORMATION

	Ou	tput				
Device	Voltage	Tolerance	Package	Shipping [†]		
LM2931AD-5.0	5.0 V	±3.8%	SOIC-8	98 Units / Rail		
LM2931AD-5.0G	5.0 V	±3.8%	SOIC-8 (Pb-Free)	98 Units / Rail		
LM2931AD-5.0R2	5.0 V	±3.8%	SOIC-8	2500 / Tape & Reel		
LM2931AD-5.0R2G	5.0 V	±3.8%	SOIC-8 (Pb-Free)	2500 / Tape & Reel		
LM2931ADT-5.0	5.0 V	±3.8%	DPAK	75 Units / Rail		
LM2931ADT-5.0G	5.0 V	±3.8%	DPAK (Pb-Free)	75 Units / Rail		
LM2931ADT-5.0RK	5.0 V	±3.8%	DPAK	2500 / VacPk Reel		
LM2931ADT-5.0RKG	5.0 V	±3.8%	DPAK (Pb-Free)	2500 / VacPk Reel		
LM2931AD2T-5.0	5.0 V	±3.8%	D ² PAK	50 Units / Rail		
LM2931AD2T-5.0G	5.0 V	±3.8%	D ² PAK (Pb-Free)	50 Units / Rail		
LM2931AD2T-5.0R4	5.0 V	±3.8%	D ² PAK	800 / VacPk Reel		
LM2931AD2T-5R4G	5.0 V	±3.8%	D ² PAK (Pb-Free)	800 / VacPk Reel		
LM2931AT-5.0	5.0 V	±3.8%	TO-220	50 Units / Rail		
LM2931AT-5.0G	5.0 V	±3.8%	TO-220 (Pb-Free)	50 Units / Rail		
LM2931AZ-5.0	5.0 V	±3.8%	TO-92	2000 / Inner Bag		
LM2931AZ-5.0G	5.0 V	±3.8%	TO-92 (Pb-Free)	2000 / Inner Bag		
LM2931AZ-5.0RA	5.0 V	±3.8%	TO-92	2000 / Tape & Reel		
LM2931AZ-5.0RAG	5.0 V	±3.8%	TO-92 (Pb-Free)	2000 / Tape & Reel		
LM2931AZ-5.0RP	5.0 V	±3.8%	TO-92	2000 / Ammo Pack		
LM2931AZ-5.0RPG	5.0 V	±3.8%	TO-92 (Pb-Free)	2000 / Ammo Pack		
LM2931D-5.0	5.0 V	±5.0%	SOIC-8	98 Units / Rail		
LM2931D-5.0G	5.0 V	±5.0%	SOIC-8 (Pb-Free)	98 Units / Rail		
LM2931D-5.0R2	5.0 V	±5.0%	SOIC-8	2500 / Tape & Reel		
LM2931D-5.0R2G	5.0 V	±5.0%	SOIC-8 (Pb-Free)	2500 / Tape & Reel		
LM2931D2T-5.0	5.0 V	±5.0%	D ² PAK	50 Units / Rail		
LM2931D2T-5.0G	5.0 V	±5.0%	D ² PAK (Pb-Free)	50 Units / Rail		
LM2931D2T-5.0R4	5.0 V	±5.0%	D ² PAK	800 / VacPk Reel		
LM2931D2T-5.0R4G	5.0 V	±5.0%	D ² PAK (Pb-Free)	800 / VacPk Reel		

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

*NCV2931: T_{low} = -40°C, T_{high} = +125°C. Guaranteed by design. NCV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable.

ORDERING INFORMATION

	Out	tput			
Device	Voltage	Tolerance	Package	Shipping [†]	
LM2931DT-5.0	5.0 V	±5.0%	DPAK	75 Units / Rail	
LM2931DT-5.0G	5.0 V	±5.0%	DPAK (Pb-Free)	75 Units / Rail	
LM2931T-5.0	5.0 V	±5.0%	TO-220	50 Units / Rail	
LM2931T-5.0G	5.0 V	± 5.0%	TO-220 (Pb-Free)	50 Units / Rail	
LM2931Z-5.0	5.0 V	±5.0%	TO-92	2000 / Inner Bag	
LM2931Z-5.0G	5.0 V	±5.0%	TO-92 (Pb-Free)	2000 / Inner Bag	
LM2931Z-5.0RA	5.0 V	±5.0%	TO-92	2000 / Tape & Reel	
LM2931Z-5.0RAG	5.0 V	± 5.0%	TO-92 (Pb-Free)	2000 / Tape & Reel	
LM2931Z-5.0RP	5.0 V	±5.0%	TO-92	2000 / Ammo Pack	
LM2931Z-5.0RPG	5.0 V	±5.0%	TO-92 (Pb-Free)	2000 / Ammo Pack	
LM2931CD	Adjustable	±5.0%	SOIC-8	98 Units / Rail	
LM2931CDG	Adjustable	±5.0%	SOIC-8 (Pb-Free)	98 Units / Rail	
LM2931CDR2	Adjustable	±5.0%	SOIC-8	2500 / Tape & Reel	
LM2931CDR2G	Adjustable	±5.0%	SOIC-8 (Pb-Free)	2500 / Tape & Reel	
LM2931CD2T	Adjustable	±5.0%	D ² PAK	50 Units / Rail	
LM2931CD2TG	Adjustable	±5.0%	D ² PAK (Pb-Free)	50 Units / Rail	
LM2931CD2TR4	Adjustable	±5.0%	D ² PAK	800 / VacPk Reel	
LM2931CD2TR4G	Adjustable	±5.0%	D ² PAK (Pb-Free)	800 / VacPk Reel	
LM2931CT	Adjustable	±5.0%	TO-220	50 Units / Rail	
LM2931CTG	Adjustable	±5.0%	TO-220 (Pb-Free)	50 Units / Rail	
LM2931ACD	Adjustable	±2.0%	SOIC-8	98 Units / Rail	
LM2931ACDG	Adjustable	±2.0%	SOIC-8 (Pb-Free)	98 Units / Rail	
LM2931ACDR2	Adjustable	±2.0%	SOIC-8	2500 / Tape & Reel	
LM2931ACDR2G	Adjustable	± 2.0%	SOIC-8 (Pb-Free)	2500 / Tape & Reel	
LM2931ACD2TR4	Adjustable	±2.0%	D ² PAK	800 / VacPk Reel	
LM2931ACD2TR4G	Adjustable	± 2.0%	D ² PAK (Pb-Free)	800 / VacPk Reel	
LM2931ACTV	Adjustable	±2.0%	TO-220	50 Units / Rail	
LM2931ACTVG	Adjustable	±2.0%	TO-220 (Pb-Free)	50 Units / Rail	

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

*NCV2931: T_{low} = -40°C, T_{high} = +125°C. Guaranteed by design. NCV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable.

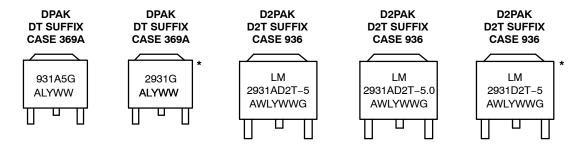
ORDERING INFORMATION

	Out	tput			
Device	Voltage	Tolerance	Package	Shipping [†]	
NCV2931ACDR2*	Adjustable	± 2.5%	SOIC-8	2500 / Tape & Reel	
NCV2931ACDR2G*	Adjustable	±2.5%	SOIC-8 (Pb-Free)	2500 / Tape & Reel	
NCV2931AD-5.0R2*	5.0 V	±3.8%	SOIC-8	2500 / Tape & Reel	
NCV2931AD-5.0R2G*	5.0 V	±3.8%	SOIC-8 (Pb-Free)	2500 / Tape & Reel	
NCV2931AST-5.0T3*	5.0 V	±3.8%	SOT-223	4000 / Tape & Reel	
NCV2931AST-5.0T3G*	5.0 V	±3.8%	SOT-223 (Pb-Free)	4000 / Tape & Reel	
NCV2931AZ-5.0G*	5.0 V	±3.8%	TO-92 (Pb-Free)	2000 / Inner Bag	
NCV2931AZ-5.0RAG*	5.0 V	±3.8%	TO-92 (Pb-Free)	2000 / Tape & Reel	
NCV2931CDR2*	Adjustable	±5.0%	SOIC-8	2500 / Tape & Reel	
NCV2931CDR2G*	Adjustable	±5.0%	SOIC-8 (Pb-Free)	2500 / Tape & Reel	
NCV2931D-5.0R2*	5.0 V	±5.0%	SOIC-8	2500 / Tape & Reel	
NCV2931D-5.0R2G*	5.0 V	±5.0%	SOIC-8 (Pb-Free)	2500 / Tape & Reel	
NCV2931ADT-5.0RK*	5.0 V	±3.8%	DPAK	2500 / Tape & Reel	
NCV2931ADT5.0RKG*	5.0 V	±3.8%	DPAK (Pb-Free)	2500 / Tape & Reel	
NCV2931DT-5.0RK*	5.0 V	±5.0%	DPAK	2500 / Tape & Reel	
NCV2931DT-5.0RKG*	5.0 V	±5.0%	DPAK (Pb-Free)	2500 / Tape & Reel	
NCV2931ACD2TR4G*	Adjustable	± 2.5%	D ² PAK (Pb-Free)	800 / VacPk Reel	
NCV2931D2T-5.0R4*	5.0 V	±5.0%	D ² PAK	800 / VacPk Reel	
NCV2931D2T5.0R4G*	5.0 V	±5.0%	D ² PAK (Pb-Free)	800 / VacPk Reel	

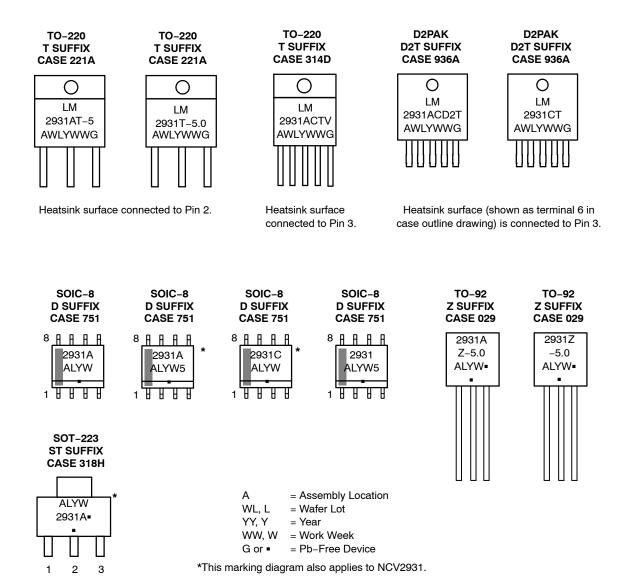
[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Spe-

cifications Brochure, BRD8011/D.
*NCV2931: T_{low} = -40°C, T_{high} = +125°C. Guaranteed by design. NCV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable.

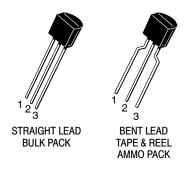
MARKING DIAGRAMS



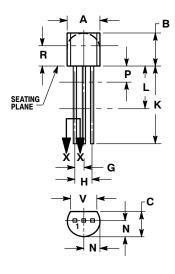
Heatsink surface (shown as terminal 4 in case outline drawing) is connected to Pin 2.



PACKAGE DIMENSIONS



TO-92 (TO-226) CASE 29-11 **ISSUE AM**

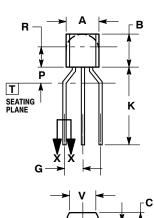


STRAIGHT LEAD **BULK PACK**



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
 4. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
С	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500		12.70	
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
Р		0.100		2.54
R	0.115		2.93	
V	0.135		3.43	



BENT LEAD TAPE & REEL AMMO PACK



- NOTES:

 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.

 2. CONTROLLING DIMENSION: MILLIMETERS.

 3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.

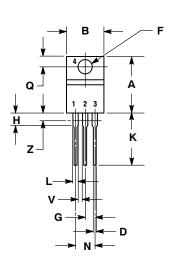
 4. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

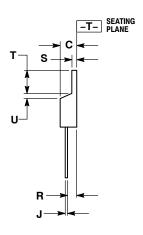
	MILLIMETERS			
DIM	MIN	MAX		
Α	4.45	5.20		
В	4.32	5.33		
С	3.18	4.19		
D	0.40	0.54		
G	2.40	2.80		
J	0.39	0.50		
K	12.70			
N	2.04	2.66		
P	1.50	4.00		
R	2.93			
V	3.43			

PACKAGE DIMENSIONS

TO-220, SINGLE GAUGE

CASE 221AB-01 **ISSUE A**



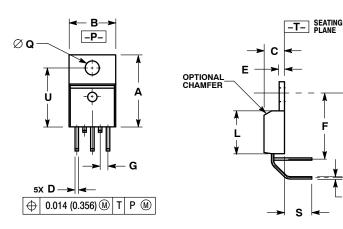


- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCHES.
 3. DIMENSION Z DEFINES A Z DONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.
 4. PRODUCT SHIPPED PRIOR TO 2008 HAD DIMENSIONS S = 0.045 0.055 INCHES (1.143 1.397 MM)

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.570	0.620	14.48	15.75
В	0.380	0.405	9.66	10.28
С	0.160	0.190	4.07	4.82
D	0.025	0.035	0.64	0.88
F	0.142	0.147	3.61	3.73
G	0.095	0.105	2.42	2.66
Н	0.110	0.155	2.80	3.93
J	0.018	0.025	0.46	0.64
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.020	0.024	0.508	0.61
Т	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
٧	0.045		1.15	
Z		0.080		2.04

PACKAGE DIMENSIONS

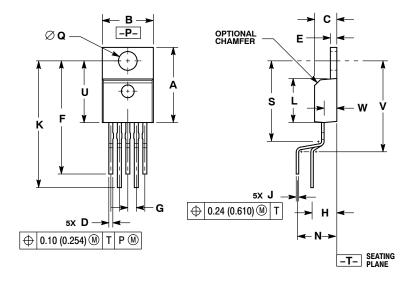
TO-220 **TH SUFFIX** CASE 314A-03 ISSUE E



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
- 2. CONTROLLING DIMENSION. NOTA:
 3. DIMENSION D DOES NOT INCLUDE
 INTERCONNECT BAR (DAMBAR) PROTRUSION.
 DIMENSION D INCLUDING PROTRUSION SHALL
 NOT EXCEED 0.043 (1.092) MAXIMUM.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.572	0.613	14.529	15.570
В	0.390	0.415	9.906	10.541
С	0.170	0.180	4.318	4.572
D	0.025	0.038	0.635	0.965
E	0.048	0.055	1.219	1.397
F	0.570	0.585	14.478	14.859
G	0.067	BSC	1.702 BSC	
J	0.015	0.025	0.381	0.635
K	0.730	0.745	18.542	18.923
L	0.320	0.365	8.128	9.271
Q	0.140	0.153	3.556	3.886
S	0.210	0.260	5.334	6.604
U	0.468	0.505	11.888	12.827

TO-220 **TV SUFFIX** CASE 314B-05 **ISSUE L**



NOTES:

- IOTES:

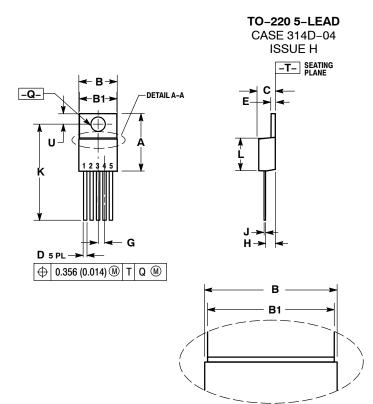
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

 2. CONTROLLING DIMENSION: INCH.

 3. DIMENSION DOES NOT INCLUDE INTERCONDECT BAR (DAMBAR) PROTRUSION. DIMENSION D INCLUDING PROTRUSION SHALL NOT EXCEED 0.043 (1.092) MAXIMUM.

	INCHES		MILLIN	IETERS	
DIM	MIN	MAX	MIN	MAX	
Α	0.572	0.613	14.529	15.570	
В	0.390	0.415	9.906	10.541	
С	0.170	0.180	4.318	4.572	
D	0.025	0.038	0.635	0.965	
Е	0.048	0.055	1.219	1.397	
F	0.850	0.935	21.590	23.749	
G	0.067	BSC	1.702 BSC		
Н	0.166	BSC	4.216 BSC		
J	0.015	0.025	0.381	0.635	
K	0.900	1.100	22.860	27.940	
L	0.320	0.365	8.128	9.271	
N	0.320	BSC	8.128	BSC	
Q	0.140	0.153	3.556	3.886	
S		0.620		15.748	
U	0.468	0.505	11.888	12.827	
٧		0.735		18.669	
W	0.090	0.110	2.286	2.794	

PACKAGE DIMENSIONS



NOTES:

- OLGS.

 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

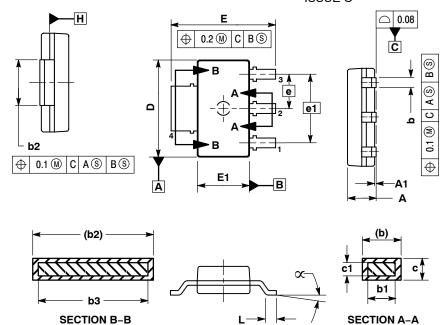
 2. CONTROLLING DIMENSION: INCH.

 3. DIMENSION D DOES NOT INCLUDE
- INTERCONNECT BAR (DAMBAR) PROTRUSION.
 DIMENSION D INCLUDING PROTRUSION SHALL
 NOT EXCEED 10.92 (0.043) MAXIMUM.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.572	0.613	14.529	15.570
В	0.390	0.415	9.906	10.541
B1	0.375	0.415	9.525	10.541
C	0.170	0.180	4.318	4.572
D	0.025	0.038	0.635	0.965
E	0.048	0.055	1.219	1.397
G	0.067	BSC	1.702 BSC	
Н	0.087	0.112	2.210	2.845
J	0.015	0.025	0.381	0.635
K	0.977	1.045	24.810	26.543
L	0.320	0.365	8.128	9.271
Q	0.140	0.153	3.556	3.886
U	0.105	0.117	2.667	2.972

SOT-223 ST SUFFIX CASE 318H-01 **ISSUE O**

DETAIL A-A

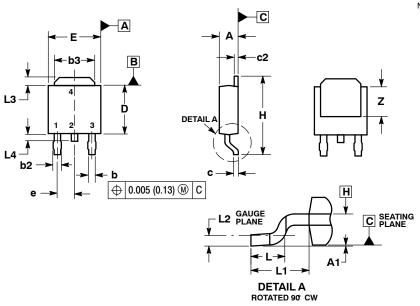


- NOTES:
 1. DIMENSIONS ARE IN MILLIMETERS.
- DIMENSIONS ARE IN MILLIMETERS.
 INTERPRET DIMENSIONS AND TOLERANCES
 PER ASME Y14.5M, 1994.
 DIMENSION E1 DOES NOT INCLUDE INTERLEAD
 FLASH OR PROTRUSION. INTERLEAD FLASH OR
 PROTRUSION SHALL NOT EXCEED 0.23 PER
- 4. DIMENSIONS b AND b2 DO NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 TOTAL IN EXCESS OF THE 6 AND 62 DIMENSIONS AT MAXIMUM MATERIAL CONDITION.
 5. TERMINAL NUMBERS ARE SHOWN FOR REFERENCE ONLY.
- 6. DIMENSIONS D AND E1 ARE TO BE DETERMINED AT DATUM PLANE H.

	MILLIMETERS		
DIM	MIN	MAX	
Α		1.80	
A1	0.02	0.11	
b	0.60	0.88	
b1	0.60	0.80	
b2	2.90	3.10	
b3	2.90	3.05	
С	0.24	0.35	
c1	0.24	0.30	
D	6.30	6.70	
E	6.70	7.30	
E1	3.30	3.70	
е	2.30		
e1	4.60		
L	0.25		
\propto	0 °	10°	

PACKAGE DIMENSIONS

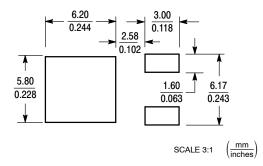
DPAK DT SUFFIX CASE 369C-01 ISSUE D



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 2. CONTROLLING DIMENSION: INCHES.
 3. THERMAL PAD CONTOUR OPTIONAL WITHIN DIMENSIONS b3, L3 and Z.
 4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.006 INCHES PER SIDE.
 5. DIMENSIONS D AND E ARE DETERMINED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY.
 6. DATUMS A AND B ARE DETERMINED AT DATUM PLANE H.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.086	0.094	2.18	2.38
A1	0.000	0.005	0.00	0.13
b	0.025	0.035	0.63	0.89
b2	0.030	0.045	0.76	1.14
b3	0.180	0.215	4.57	5.46
С	0.018	0.024	0.46	0.61
c2	0.018	0.024	0.46	0.61
D	0.235	0.245	5.97	6.22
Е	0.250	0.265	6.35	6.73
е	0.090 BSC		2.29 BSC	
Н	0.370	0.410	9.40	10.41
L	0.055	0.070	1.40	1.78
L1	0.108 REF		2.74 REF	
L2	0.020 BSC		0.51 BSC	
L3	0.035	0.050	0.89	1.27
L4		0.040		1.01
Z	0.155		3.93	

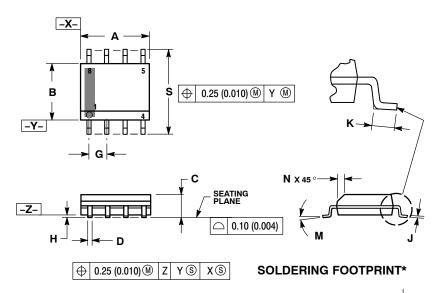
SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

PACKAGE DIMENSIONS

SOIC-8 NB CASE 751-07 **ISSUE AK**



NOTES:

- NOTES:

 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

 2. CONTROLLING DIMENSION: MILLIMETER.

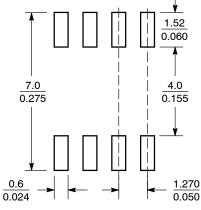
 3. DIMENSION A AND B DO NOT INCLUDE MOLD PROTRUSION.

 4. MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE PER SIDE.
- PER SIDE.

 5. DIMENSION D DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 (0.005) TOTAL IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION.

 6. 751-01 THRU 751-06 ARE OBSOLETE. NEW STANDARD IS 751.07
- STANDARD IS 751-07.

	MILLIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
Α	4.80	5.00	0.189	0.197
В	3.80	4.00	0.150	0.157
С	1.35	1.75	0.053	0.069
D	0.33	0.51	0.013	0.020
G	1.27 BSC		0.050 BSC	
Н	0.10	0.25	0.004	0.010
J	0.19	0.25	0.007	0.010
K	0.40	1.27	0.016	0.050
М	0 °	8 °	0 °	8 °
N	0.25	0.50	0.010	0.020
S	5.80	6.20	0.228	0.244

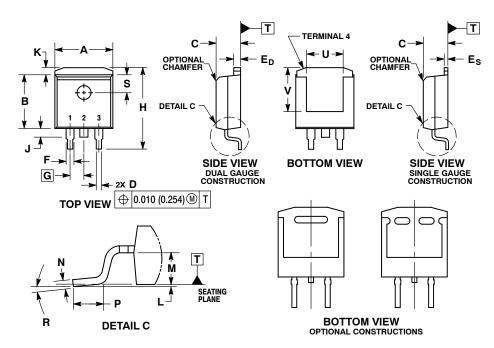


*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

SCALE 6:1

PACKAGE DIMENSIONS

$\mathsf{D}^2\mathsf{PAK}$ **D2T SUFFIX** CASE 936-03 ISSUE D

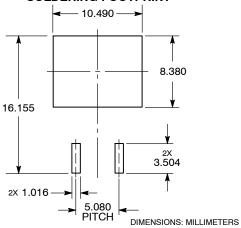


- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCHES.
 3. TAB CONTOUR OPTIONAL WITHIN DIMENSIONS

- AND K.
 DIMENSIONS U AND V ESTABLISH A MINIMUM
 MOUNTING SURFACE FOR TERMINAL 4.
 DIMENSIONS A AND B DO NOT INCLUDE MOLD
 FLASH OR GATE PROTRUSIONS. MOLD FLASH
 AND GATE PROTRUSIONS NOT TO EXCEED 0.025 (0.635) MAXIMUM.
 6. SINGLE GAUGE DESIGN WILL BE SHIPPED AF-
- TER FPCN EXPIRATION IN OCTOBER 2011.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.386	0.403	9.804	10.236
В	0.356	0.368	9.042	9.347
С	0.170	0.180	4.318	4.572
D	0.026	0.036	0.660	0.914
ED	0.045	0.055	1.143	1.397
Es	0.018	0.026	0.457	0.660
F	0.051 REF		1.295 REF	
G	0.100 BSC		2.540 BSC	
Н	0.539	0.579	13.691	14.707
J	0.125 MAX		3.175 MAX	
K	0.050 REF		1.270 REF	
L	0.000	0.010	0.000	0.254
M	0.088	0.102	2.235	2.591
N	0.018	0.026	0.457	0.660
P	0.058	0.078	1.473	1.981
R	5° REF		5° REF	
S	0.116 REF		2.946 REF	
U	0.200 MIN		5.080 MIN	
V	0.250 MIN		6.350 MIN	

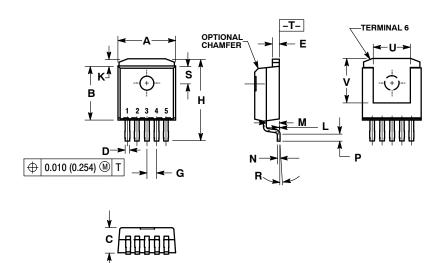
SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

PACKAGE DIMENSIONS

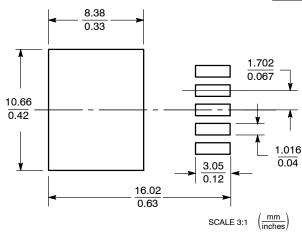
D²PAK **D2T SUFFIX** CASE 936A-02 **ISSUE C**



NOTES

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. CONTROLLING DIMENSION: INCH.
- TAB CONTOUR OPTIONAL WITHIN DIMENSIONS A AND K.
- DIMENSIONS U AND V ESTABLISH A MINIMUM
- MOUNTING SURFACE FOR TERMINAL 6.
 DIMENSIONS A AND B DO NOT INCLUDE MOLD
 FLASH OR GATE PROTRUSIONS. MOLD FLASH AND GATE PROTRUSIONS NOT TO EXCEED 0.025 (0.635) MAXIMUM.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.386	0.403	9.804	10.236
В	0.356	0.368	9.042	9.347
С	0.170	0.180	4.318	4.572
D	0.026	0.036	0.660	0.914
E	0.045	0.055	1.143	1.397
G	0.067 BSC		1.702 BSC	
Н	0.539	0.579	13.691	14.707
K	0.050 REF		1.270 REF	
L	0.000	0.010	0.000	0.254
М	0.088	0.102	2.235	2.591
N	0.018	0.026	0.457	0.660
P	0.058	0.078	1.473	1.981
R	5°REF		5° REF	
S	0.116 REF		2.946 REF	
U	0.200 MIN		5.080 MIN	
٧	0.250 MIN		6.350 MIN	



SOLDERING FOOTPRINT*

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and (III) are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without particular purpose, for does Scribe as scribe any attaining arising out of the application of use of any product of circuit, and specifications can all an inability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all Claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA

Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free

Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910 Japan Customer Focus Center

Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative