

LM380 2.5W Audio Power Amplifier

Check for Samples: LM380

FEATURES

- Wide Supply Voltage Range: 10V-22V
- Low Quiescent Power Drain: 0.13W (V_S= 18V)
- Voltage Gain Fixed at 50
- High Peak Current Capability: 1.3A
- Input Referenced to GND
- High Input Impedance: 150kΩ
- Low Distortion
- Quiescent Output Voltage is at One-Half of the Supply Voltage
- Standard Dual-In-Line Package

DESCRIPTION

The LM380 is a power audio amplifier for consumer applications. In order to hold system cost to a minimum, gain is internally fixed at 34 dB. A unique input stage allows ground referenced input signals. The output automatically self-centers to one-half the supply voltage.

The output is short circuit proof with internal thermal limiting. The package outline is standard dual-in-line. The LM380N uses a copper lead frame. The center three pins on either side comprise a heat sink. This makes the device easy to use in standard PC layouts.

Uses include simple phonograph amplifiers, intercoms, line drivers, teaching machine outputs, alarms, ultrasonic drivers, TV sound systems, AM-FM radio, small servo drivers, power converters, etc.

A selected part for more power on higher supply voltages is available as the LM384. For more information see SNAA086.

Connection Diagrams

(Dual-In-Line Packages, Top View)

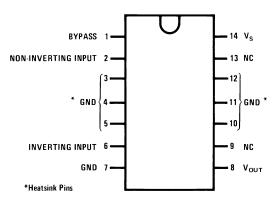


Figure 1. 14-Pin PDIP See NFF0014A Package

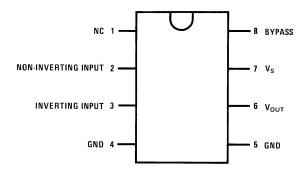


Figure 2. 8-Pin PDIP See P Package



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Block and Schematic Diagrams

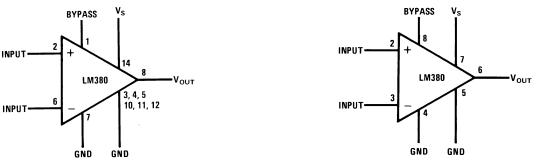


Figure 3. 14-Pin PDIP

Figure 4. 8-Pin PDIP

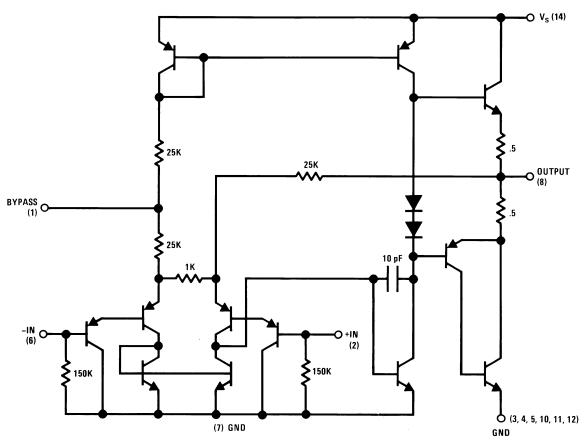


Figure 5.





These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

Absolute Maximum Ratings (1)(2)

Absolute maximum ratings			
Supply Voltage	22V		
Peak Current		1.3A	
Package Dissipation	14-Pin PDIP ⁽³⁾	8.3W	
	8-Pin PDIP ⁽³⁾	1.67W	
Input Voltage	±0.5V		
Storage Temperature	−65°C to +150°C		
Operating Temperature	0°C to +70°C		
Junction Temperature	+150°C		
Lead Temperature (Soldering, 10 sec.)	+260°C		
ESD rating to be determined			
Thermal Resistance	θ _{JC} (14-Pin PDIP)	30°C/W	
	θ _{JC} (8-Pin PDIP)	37°C/W	
	θ _{JA} (14-Pin PDIP)	79°C/W	
	θ _{JA} (8-Pin PDIP)	107°C/W	

[&]quot;Absolute Maximum Ratings" indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not ensure specific performance limits.

Electrical Characteristics(1)

Symbol	Parameter	Conditions	Min	Тур	Max	Units
P _{OUT(RMS)}	Output Power	$R_L = 8\Omega$, THD = $3\%^{(2)(3)}$	2.5			W
A _V	Gain		40	50	60	V/V
V _{OUT}	Output Voltage Swing	$R_L = 8\Omega$		14		V_{p-p}
Z _{IN}	Input Resistance			150k		Ω
THD	Total Harmonic Distortion	See ⁽³⁾⁽⁴⁾		0.2		%
PSRR	Power Supply Rejection Ratio	See ⁽⁵⁾		38		dB
Vs	Supply Voltage		10		22	V
BW	Bandwidth	$P_{OUT} = 2W, R_L = 8\Omega$		100k		Hz
IQ	Quiescent Supply Current			7	25	mA
V_{OUTQ}	Quiescent Output Voltage		8	9.0	10	V
I _{BIAS}	Bias Current	Inputs Floating		100		nA
I _{SC}	Short Circuit Current			1.3		Α

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If Military/Aerospace specified devices are required, please contact the Texas Instruments Sales Office/Distributors for availability and specifications.

The package is to be derated at 15°C/W junction to heat sink pins for 14-pin pkg; 75°C/W for 8-pin.

 V_S = 18V and T_A = 25°C unless otherwise specified. With device Pins 3, 4, 5, 10, 11, 12 soldered into a 1/16" epoxy glass board with 2 ounce copper foil with a minimum surface of 6 square (2)inches.

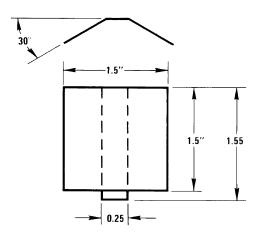
 $C_{BYPASS} = 0.47 \mu fd$ on Pin 1.

The maximum junction temperature of the LM380 is 150°C.

Rejection ratio referred to the output with $C_{BYPASS} = 5 \mu F$.



Heat Sink Dimensions

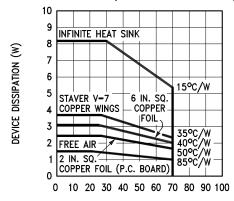


Staver Heat Sink #V-7 Staver Company 41 Saxon Ave. P.O. Drawer H Bayshore, NY 11706 Tel: (516) 666-8000 Copper Wings 2 Required Soldered to Pins 3, 4, 5, 10, 11, 12 Thickness 0.04 Inches



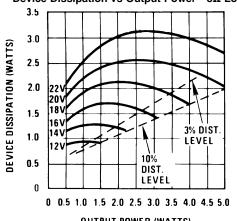
Typical Performance Characteristics

Maximum Device Dissipation vs Ambient Temperature

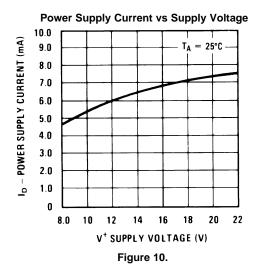


TA- AMBIENT TEMPERATURE (°C) Note: 2 oz. copper foil, single-sided PC board. Figure 6.

Device Dissipation vs Output Power— 8Ω Load



OUTPUT POWER (WATTS) Figure 8.



Device Dissipation vs Output Power—4Ω Load

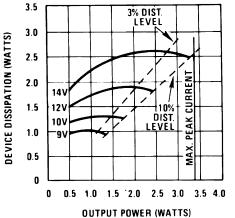
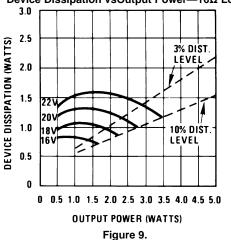


Figure 7.

Device Dissipation vsOutput Power—16Ω Load



Total Harmonic Distortion vs Frequency

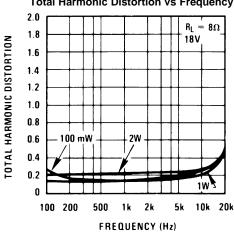
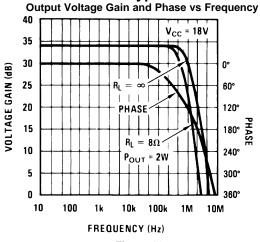


Figure 11.

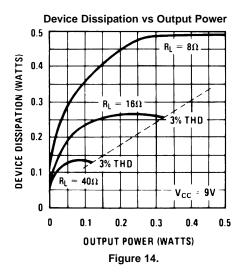
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Typical Performance Characteristics (continued)







Total Harmonic Distortion vs Output Power - TOTAL HARMONIC DISTORTION (%) f = 1 kHz 9.0 V_{CC} = 22V 8.0 $R_L = 8\Omega$ CBYPASS = 5 µF HEATSINK = TWO 7.0 6.0 COPPER WINGS SEE FIG. PAGE 4 5.0 4.0 3.0 2.0 1.0

Po – OUTPUT POWER (WATTS) Figure 13.

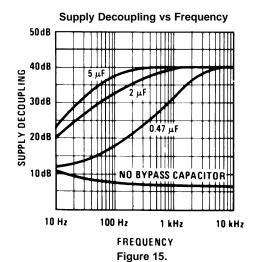
0.5 1.0

2.0

5.0 10

0.2

0.1



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Typical Applications

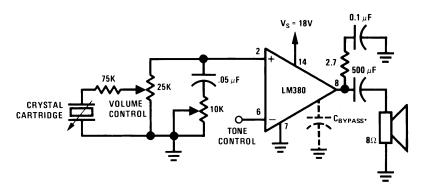


Figure 16. Phono Amplifier

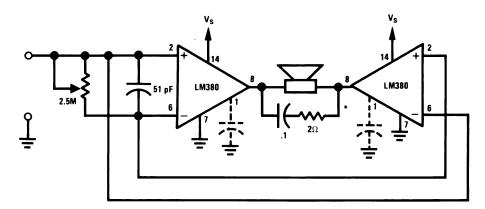


Figure 17. Bridge Amplifier

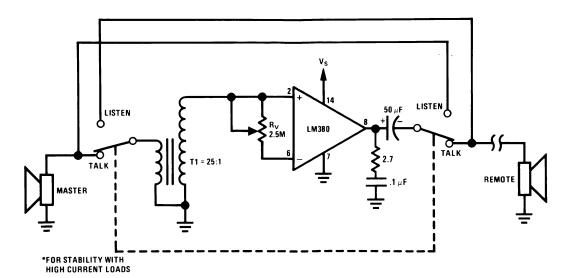


Figure 18. Intercom



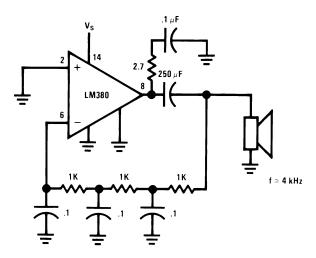


Figure 19. Phase Shift Oscillator



REVISION HISTORY

Cł	Changes from Revision B (April 2013) to Revision C					
•	Changed layout of National Data Sheet to TI format	8				



PACKAGE OPTION ADDENDUM

27-Mar-2014

PACKAGING INFORMATION

Orderable Device	Status	Package Type	Package	Pins	Package	Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Device Marking	Samples
	(1)		Drawing		Qty	(2)	(6)	(3)		(4/5)	
LM380N	LIFEBUY	PDIP	NFF	14	25	TBD	Call TI	Call TI	0 to 70	LM380N	
LM380N-8/NOPB	ACTIVE	PDIP	Р	8	40	Green (RoHS & no Sb/Br)	CU SN	Level-1-NA-UNLIM	0 to 70	LM 380N-8	Samples
LM380N/NOPB	ACTIVE	PDIP	NFF	14	25	Pb-Free (RoHS Exempt)	CU SN	Level-1-NA-UNLIM	0 to 70	LM380N	Samples

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

- (3) MSL, Peak Temp. The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.
- (4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.
- (5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.
- (6) Lead/Ball Finish Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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P (R-PDIP-T8)

PLASTIC DUAL-IN-LINE PACKAGE



NOTES:

- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. Falls within JEDEC MS-001 variation BA.





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