

# MMBT5087L

## Low Noise Transistor

### PNP Silicon

#### Features

- NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

#### MAXIMUM RATINGS

| Rating                         | Symbol    | Value | Unit |
|--------------------------------|-----------|-------|------|
| Collector – Emitter Voltage    | $V_{CEO}$ | -50   | Vdc  |
| Collector – Base Voltage       | $V_{CBO}$ | -50   | Vdc  |
| Emitter – Base Voltage         | $V_{EBO}$ | -3.0  | Vdc  |
| Collector Current – Continuous | $I_C$     | -50   | mAdc |

#### THERMAL CHARACTERISTICS

| Characteristic                                                                                                      | Symbol          | Max         | Unit                       |
|---------------------------------------------------------------------------------------------------------------------|-----------------|-------------|----------------------------|
| Total Device Dissipation FR-5 Board,<br>(Note 1) $T_A = 25^\circ\text{C}$<br>Derate above $25^\circ\text{C}$        | $P_D$           | 225<br>1.8  | mW<br>mW/ $^\circ\text{C}$ |
| Thermal Resistance, Junction-to-Ambient                                                                             | $R_{\theta JA}$ | 556         | $^\circ\text{C/W}$         |
| Total Device Dissipation Alumina<br>Substrate, (Note 2) $T_A = 25^\circ\text{C}$<br>Derate above $25^\circ\text{C}$ | $P_D$           | 300<br>2.4  | mW<br>mW/ $^\circ\text{C}$ |
| Thermal Resistance, Junction-to-Ambient                                                                             | $R_{\theta JA}$ | 417         | $^\circ\text{C/W}$         |
| Junction and Storage Temperature                                                                                    | $T_J, T_{stg}$  | -55 to +150 | $^\circ\text{C}$           |

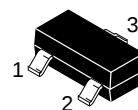
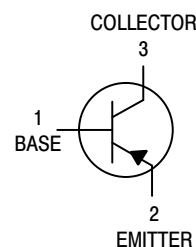
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. FR-5 = 1.0 x 0.75 x 0.062 in.
2. Alumina = 0.4 x 0.3 x 0.024 in. 99.5% alumina.



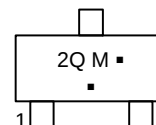
ON Semiconductor®

[www.onsemi.com](http://www.onsemi.com)



SOT-23 (TO-236)  
CASE 318  
STYLE 6

#### MARKING DIAGRAM



2Q = Device Code  
M = Date Code\*  
■ = Pb-Free Package

(Note: Microdot may be in either location)

\*Date Code orientation and/or overbar may vary depending upon manufacturing location.

#### ORDERING INFORMATION

| Device                           | Package             | Shipping <sup>†</sup>   |
|----------------------------------|---------------------|-------------------------|
| MMBT5087LT1G,<br>NSVMMBT5087LT1G | SOT-23<br>(Pb-Free) | 3,000 / Tape &<br>Reel  |
| MMBT5087LT3G,<br>NSVMMBT5087LT3G | SOT-23<br>(Pb-Free) | 10,000 / Tape &<br>Reel |

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# MMBT5087L

## ELECTRICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

| Characteristic                                                                                                     | Symbol        | Min    | Max        | Unit |
|--------------------------------------------------------------------------------------------------------------------|---------------|--------|------------|------|
| <b>OFF CHARACTERISTICS</b>                                                                                         |               |        |            |      |
| Collector-Emitter Breakdown Voltage<br>( $I_C = -1.0\text{ mA}$ , $I_B = 0$ )                                      | $V_{(BR)CEO}$ | -50    | -          | Vdc  |
| Collector-Base Breakdown Voltage<br>( $I_C = -100\text{ }\mu\text{A}$ , $I_E = 0$ )                                | $V_{(BR)CBO}$ | -50    | -          | Vdc  |
| Collector Cutoff Current<br>( $V_{CB} = -10\text{ Vdc}$ , $I_E = 0$ )<br>( $V_{CB} = -35\text{ Vdc}$ , $I_E = 0$ ) | $I_{CBO}$     | -<br>- | -10<br>-50 | nAdc |

## ON CHARACTERISTICS

|                                                                                                                                                                                                          |               |                   |               |     |
|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------|-------------------|---------------|-----|
| DC Current Gain<br>( $I_C = -100\text{ }\mu\text{A}$ , $V_{CE} = -5.0\text{ Vdc}$ )<br>( $I_C = -1.0\text{ mA}$ , $V_{CE} = -5.0\text{ Vdc}$ )<br>( $I_C = -10\text{ mA}$ , $V_{CE} = -5.0\text{ Vdc}$ ) | $h_{FE}$      | 250<br>250<br>250 | 800<br>-<br>- | -   |
| Collector-Emitter Saturation Voltage<br>( $I_C = -10\text{ mA}$ , $I_B = -1.0\text{ mA}$ )                                                                                                               | $V_{CE(sat)}$ | -                 | -0.3          | Vdc |
| Base-Emitter Saturation Voltage<br>( $I_C = -10\text{ mA}$ , $I_B = -1.0\text{ mA}$ )                                                                                                                    | $V_{BE(sat)}$ | -                 | 0.85          | Vdc |

## SMALL-SIGNAL CHARACTERISTICS

|                                                                                                                                                                                                                                                   |           |        |            |     |
|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------|--------|------------|-----|
| Current-Gain — Bandwidth Product<br>( $I_C = -500\text{ }\mu\text{A}$ , $V_{CE} = -5.0\text{ Vdc}$ , $f = 20\text{ MHz}$ )                                                                                                                        | $f_T$     | 40     | -          | MHz |
| Output Capacitance<br>( $V_{CB} = -5.0\text{ Vdc}$ , $I_E = 0$ , $f = 1.0\text{ MHz}$ )                                                                                                                                                           | $C_{obo}$ | -      | 4.0        | pF  |
| Small-Signal Current Gain<br>( $I_C = -1.0\text{ mA}$ , $V_{CE} = -5.0\text{ Vdc}$ , $f = 1.0\text{ kHz}$ )                                                                                                                                       | $h_{fe}$  | 250    | 900        | -   |
| Noise Figure<br>( $I_C = -20\text{ mA}$ , $V_{CE} = -5.0\text{ Vdc}$ , $R_S = 10\text{ k}\Omega$ , $f = 1.0\text{ kHz}$ )<br>( $I_C = -100\text{ }\mu\text{A}$ , $V_{CE} = -5.0\text{ Vdc}$ , $R_S = 3.0\text{ k}\Omega$ , $f = 1.0\text{ kHz}$ ) | NF        | -<br>- | 2.0<br>2.0 | dB  |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

## TYPICAL NOISE CHARACTERISTICS

( $V_{CE} = -5.0\text{ Vdc}$ ,  $T_A = 25^\circ\text{C}$ )

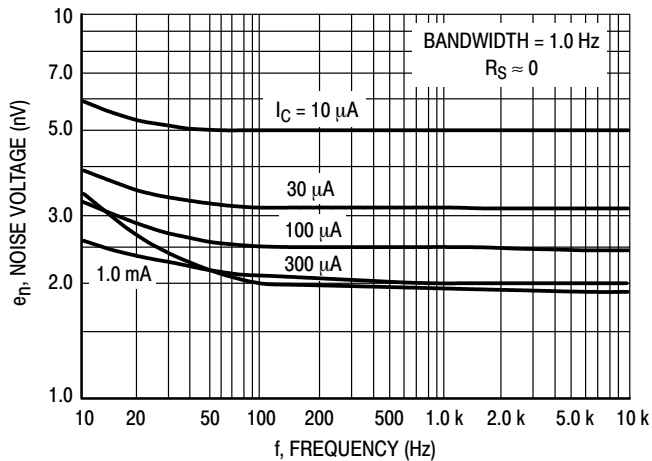


Figure 1. Noise Voltage

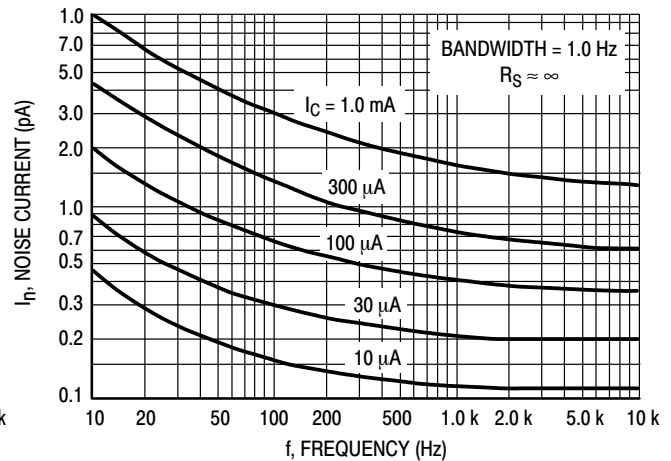


Figure 2. Noise Current

## NOISE FIGURE CONTOURS

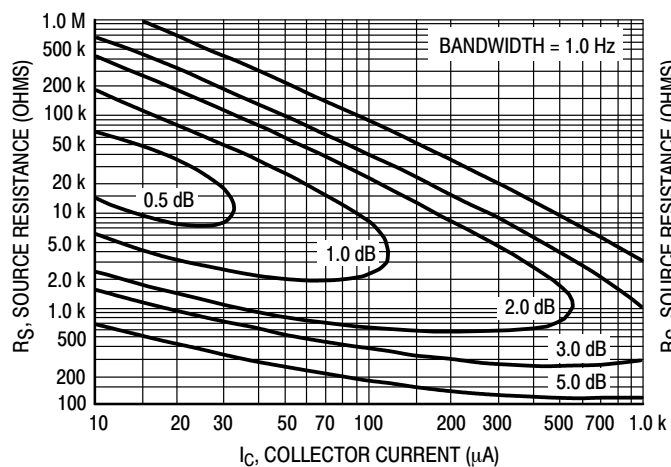
(V<sub>CE</sub> = -5.0 Vdc, T<sub>A</sub> = 25°C)

Figure 3. Narrow Band, 100 Hz

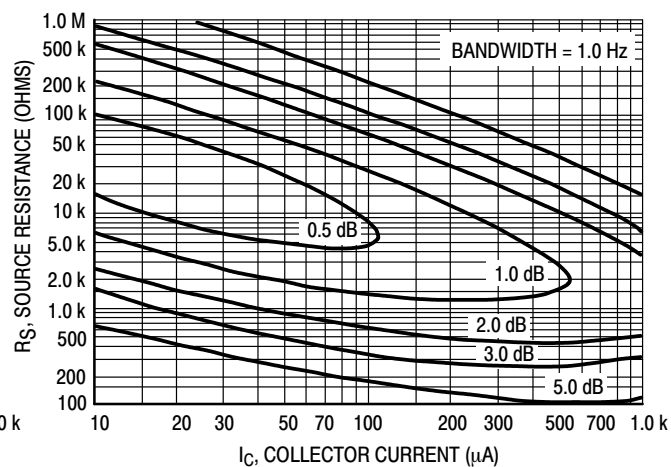


Figure 4. Narrow Band, 1.0 kHz

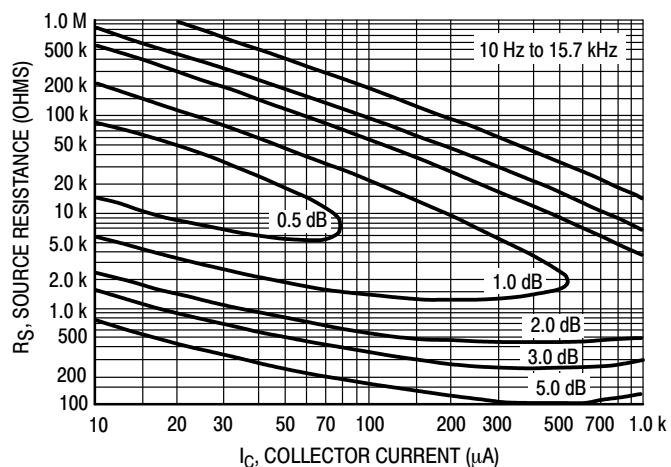


Figure 5. Wideband

Noise Figure is Defined as:

$$NF = 20 \log_{10} \left[ \frac{e_n^2 + 4KTR_S + I_n^2 R_S^2}{4KTR_S} \right]^{1/2}$$

 $e_n$  = Noise Voltage of the Transistor referred to the input. (Figure 3) $I_n$  = Noise Current of the Transistor referred to the input. (Figure 4) $K$  = Boltzman's Constant ( $1.38 \times 10^{-23} \text{ J/}^\circ\text{K}$ ) $T$  = Temperature of the Source Resistance ( $^\circ\text{K}$ ) $R_S$  = Source Resistance (Ohms)

## TYPICAL STATIC CHARACTERISTICS

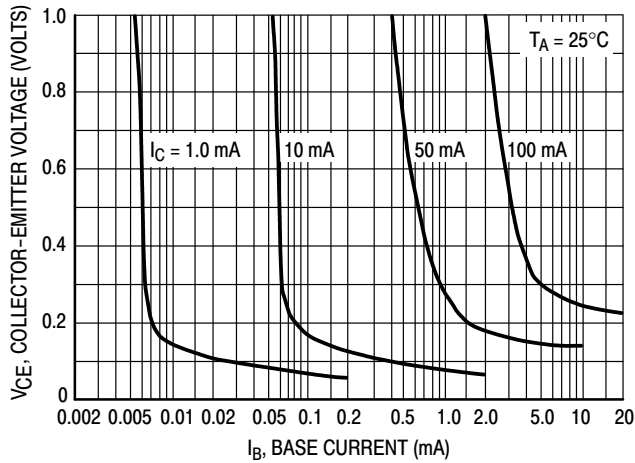


Figure 6. Collector Saturation Region

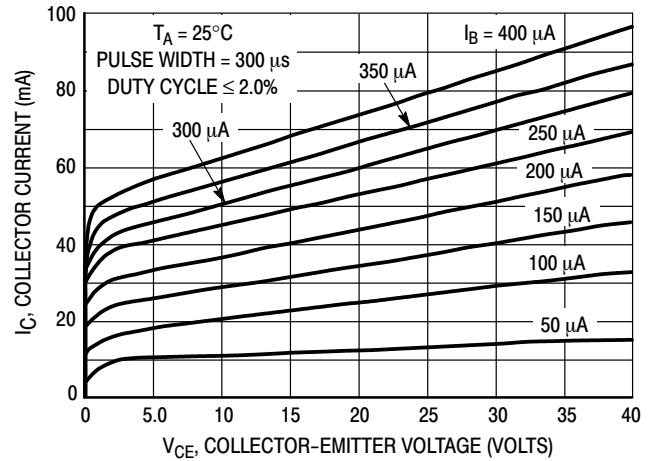


Figure 7. Collector Characteristics

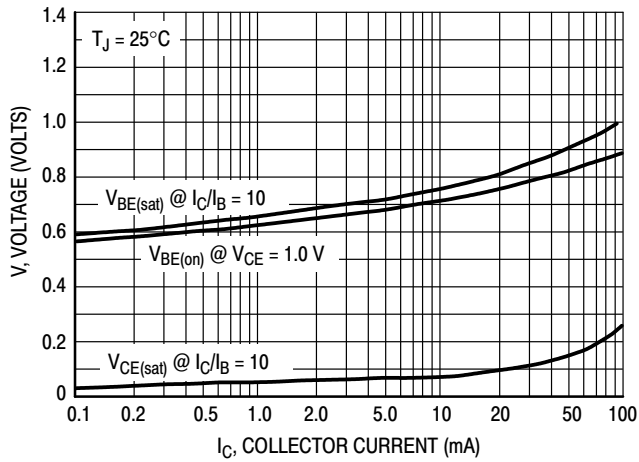


Figure 8. "On" Voltages

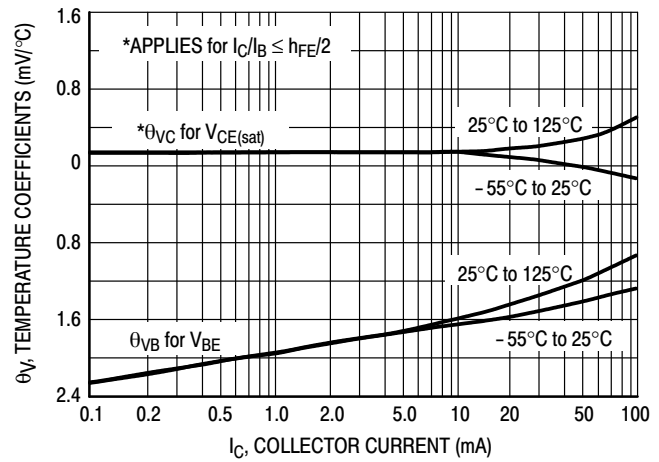


Figure 9. Temperature Coefficients

## TYPICAL DYNAMIC CHARACTERISTICS

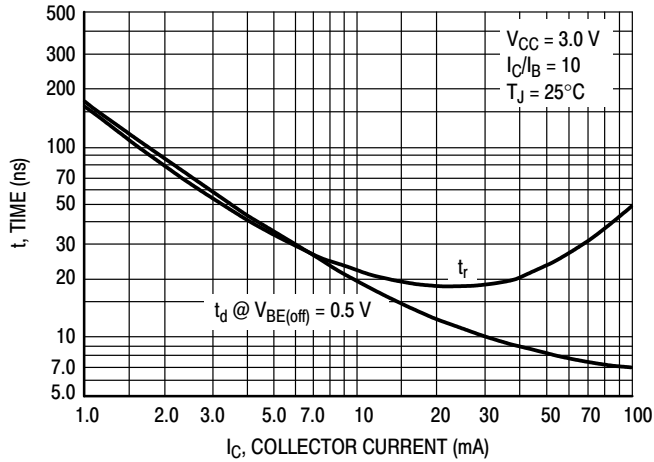


Figure 10. Turn-On Time

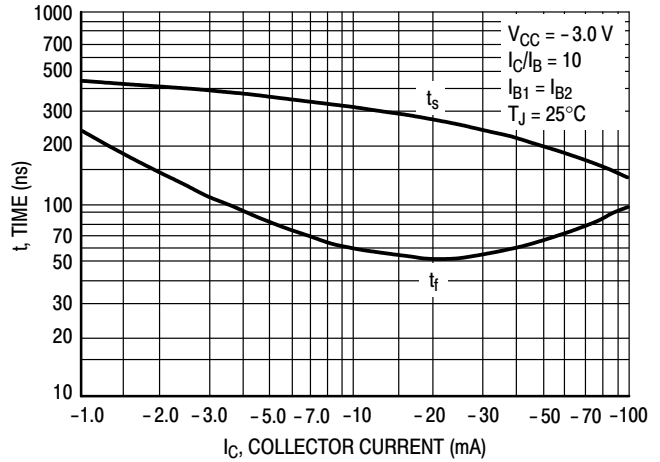


Figure 11. Turn-Off Time

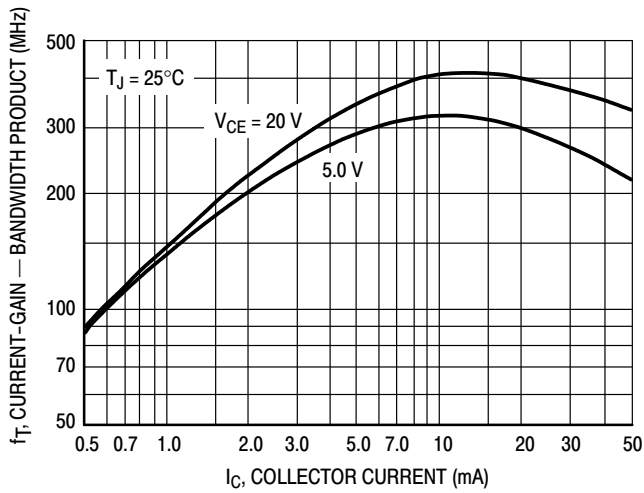


Figure 12. Current-Gain — Bandwidth Product

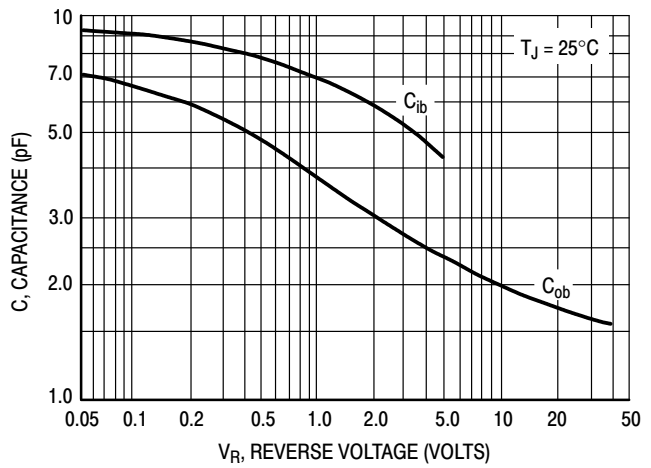


Figure 13. Capacitance

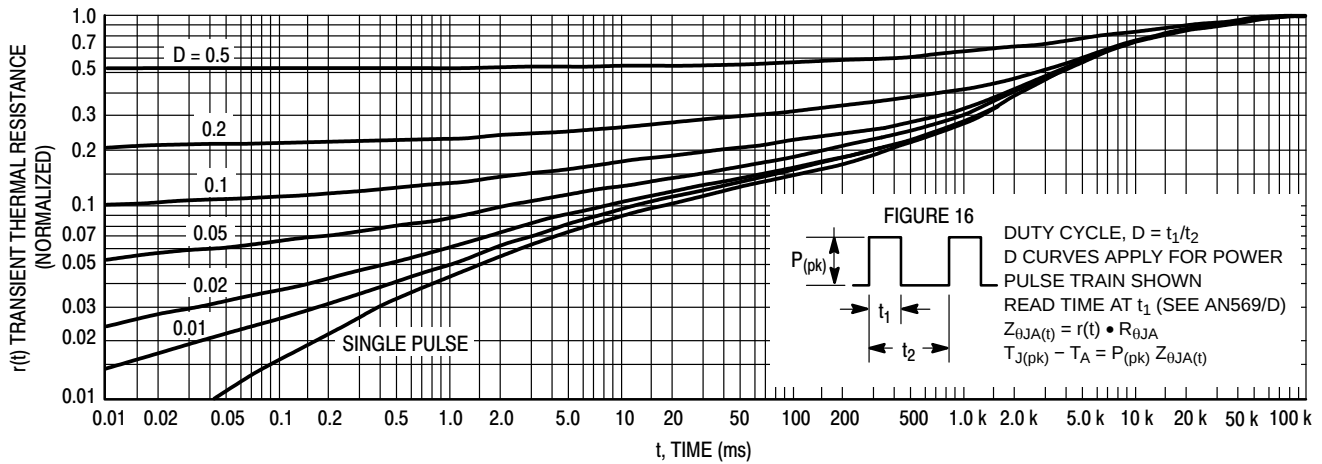


Figure 14. Thermal Response

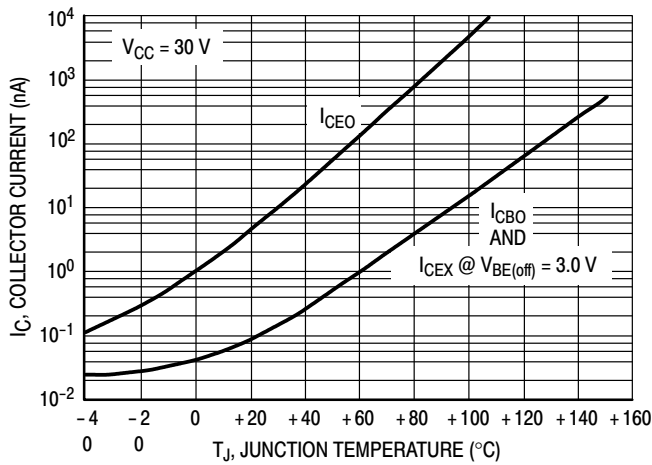


Figure 15. Typical Collector Leakage Current

#### DESIGN NOTE: USE OF THERMAL RESPONSE DATA

A train of periodical power pulses can be represented by the model as shown in Figure 16. Using the model and the device thermal response the normalized effective transient thermal resistance of Figure 14 was calculated for various duty cycles.

To find  $Z_{\theta JA(t)}$ , multiply the value obtained from Figure 14 by the steady state value  $R_{\theta JA}$ .

Example:

Dissipating 2.0 watts peak under the following conditions:

$t_1 = 1.0$  ms,  $t_2 = 5.0$  ms ( $D = 0.2$ )

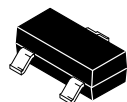
Using Figure 14 at a pulse width of 1.0 ms and  $D = 0.2$ , the reading of  $r(t)$  is 0.22.

The peak rise in junction temperature is therefore

$$\Delta T = r(t) \times P_{(pk)} \times R_{\theta JA} = 0.22 \times 2.0 \times 200 = 88^\circ\text{C}.$$

For more information, see ON Semiconductor Application Note AN569/D, available from the Literature Distribution Center or on our website at [www.onsemi.com](http://www.onsemi.com).

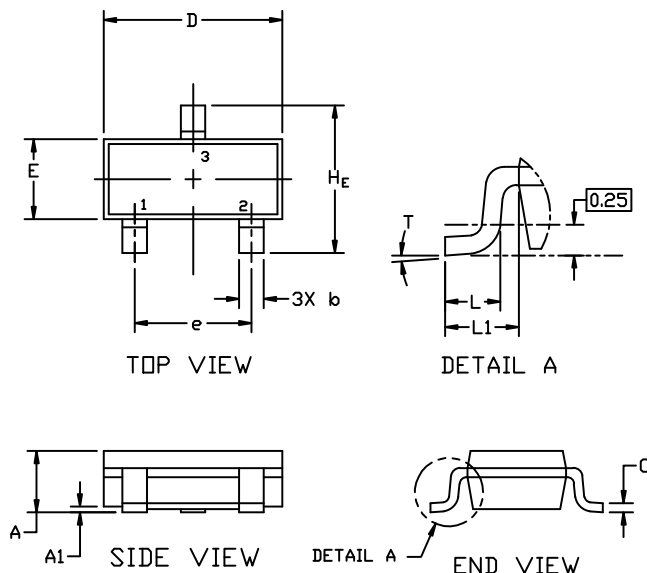
# MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS



SCALE 4:1

**SOT-23 (TO-236)**  
**CASE 318**  
**ISSUE AT**

DATE 01 MAR 2023

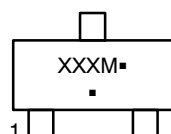


## NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M,1994.
2. CONTROLLING DIMENSION: MILLIMETERS
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE BASE MATERIAL.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

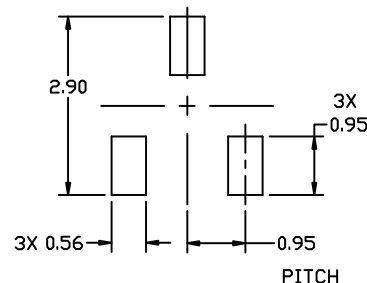
| DIM            | MILLIMETERS |      |      | INCHES |       |       |
|----------------|-------------|------|------|--------|-------|-------|
|                | MIN.        | NOM. | MAX. | MIN.   | NOM.  | MAX.  |
| A              | 0.89        | 1.00 | 1.11 | 0.035  | 0.039 | 0.044 |
| A1             | 0.01        | 0.06 | 0.10 | 0.000  | 0.002 | 0.004 |
| b              | 0.37        | 0.44 | 0.50 | 0.015  | 0.017 | 0.020 |
| c              | 0.08        | 0.14 | 0.20 | 0.003  | 0.006 | 0.008 |
| D              | 2.80        | 2.90 | 3.04 | 0.110  | 0.114 | 0.120 |
| E              | 1.20        | 1.30 | 1.40 | 0.047  | 0.051 | 0.055 |
| e              | 1.78        | 1.90 | 2.04 | 0.070  | 0.075 | 0.080 |
| L              | 0.30        | 0.43 | 0.55 | 0.012  | 0.017 | 0.022 |
| L1             | 0.35        | 0.54 | 0.69 | 0.014  | 0.021 | 0.027 |
| H <sub>E</sub> | 2.10        | 2.40 | 2.64 | 0.083  | 0.094 | 0.104 |
| T              | 0°          | ---  | 10°  | 0°     | ---   | 10°   |

## GENERIC MARKING DIAGRAM\*



XXX = Specific Device Code  
M = Date Code  
▪ = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.



## RECOMMENDED MOUNTING FOOTPRINT

\* For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

## STYLES ON PAGE 2

|                         |                        |                                                                                                                                                                                  |
|-------------------------|------------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>DOCUMENT NUMBER:</b> | <b>98ASB42226B</b>     | Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red. |
| <b>DESCRIPTION:</b>     | <b>SOT-23 (TO-236)</b> | <b>PAGE 1 OF 2</b>                                                                                                                                                               |

onsemi and onsemi are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.

# MECHANICAL CASE OUTLINE

## PACKAGE DIMENSIONS



### SOT-23 (TO-236) CASE 318 ISSUE AT

DATE 01 MAR 2023

|                                                         |                                                       |                                                             |                                                             |                                                             |                                                             |
|---------------------------------------------------------|-------------------------------------------------------|-------------------------------------------------------------|-------------------------------------------------------------|-------------------------------------------------------------|-------------------------------------------------------------|
| STYLE 1 THRU 5:<br>CANCELLED                            | STYLE 6:<br>PIN 1. BASE<br>2. EMITTER<br>3. COLLECTOR | STYLE 7:<br>PIN 1. EMITTER<br>2. BASE<br>3. COLLECTOR       | STYLE 8:<br>PIN 1. ANODE<br>2. NO CONNECTION<br>3. CATHODE  |                                                             |                                                             |
| STYLE 9:<br>PIN 1. ANODE<br>2. ANODE<br>3. CATHODE      | STYLE 10:<br>PIN 1. DRAIN<br>2. SOURCE<br>3. GATE     | STYLE 11:<br>PIN 1. ANODE<br>2. CATHODE<br>3. CATHODE-ANODE | STYLE 12:<br>PIN 1. CATHODE<br>2. CATHODE<br>3. ANODE       | STYLE 13:<br>PIN 1. SOURCE<br>2. DRAIN<br>3. GATE           | STYLE 14:<br>PIN 1. CATHODE<br>2. GATE<br>3. ANODE          |
| STYLE 15:<br>PIN 1. GATE<br>2. CATHODE<br>3. ANODE      | STYLE 16:<br>PIN 1. ANODE<br>2. CATHODE<br>3. CATHODE | STYLE 17:<br>PIN 1. NO CONNECTION<br>2. ANODE<br>3. CATHODE | STYLE 18:<br>PIN 1. NO CONNECTION<br>2. CATHODE<br>3. ANODE | STYLE 19:<br>PIN 1. CATHODE<br>2. ANODE<br>3. CATHODE-ANODE | STYLE 20:<br>PIN 1. CATHODE<br>2. ANODE<br>3. GATE          |
| STYLE 21:<br>PIN 1. GATE<br>2. SOURCE<br>3. DRAIN       | STYLE 22:<br>PIN 1. RETURN<br>2. OUTPUT<br>3. INPUT   | STYLE 23:<br>PIN 1. ANODE<br>2. ANODE<br>3. CATHODE         | STYLE 24:<br>PIN 1. GATE<br>2. DRAIN<br>3. SOURCE           | STYLE 25:<br>PIN 1. ANODE<br>2. CATHODE<br>3. GATE          | STYLE 26:<br>PIN 1. CATHODE<br>2. ANODE<br>3. NO CONNECTION |
| STYLE 27:<br>PIN 1. CATHODE<br>2. CATHODE<br>3. CATHODE | STYLE 28:<br>PIN 1. ANODE<br>2. ANODE<br>3. ANODE     |                                                             |                                                             |                                                             |                                                             |

|                  |                 |                                                                                                                                                                                     |
|------------------|-----------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| DOCUMENT NUMBER: | 98ASB42226B     | Electronic versions are uncontrolled except when accessed directly from the Document Repository.<br>Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red. |
| DESCRIPTION:     | SOT-23 (TO-236) | PAGE 2 OF 2                                                                                                                                                                         |

onsemi and onsemi are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.

**onsemi**, **Onsemi**, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "**onsemi**" or its affiliates and/or subsidiaries in the United States and/or other countries. **onsemi** owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of **onsemi**'s product/patent coverage may be accessed at [www.onsemi.com/site/pdf/Patent-Marking.pdf](http://www.onsemi.com/site/pdf/Patent-Marking.pdf). **onsemi** reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and **onsemi** makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

## ADDITIONAL INFORMATION

### TECHNICAL PUBLICATIONS:

Technical Library: [www.onsemi.com/design/resources/technical-documentation](http://www.onsemi.com/design/resources/technical-documentation)  
onsemi Website: [www.onsemi.com](http://www.onsemi.com)

### ONLINE SUPPORT: [www.onsemi.com/support](http://www.onsemi.com/support)

For additional information, please contact your local Sales Representative at  
[www.onsemi.com/support/sales](http://www.onsemi.com/support/sales)