



NTE1900 Integrated Circuit 3-Terminal Adjustable Positive Voltage Regulator

Description:

The NTE1900 is an adjustable 3-terminal positive voltage regulator in a TO92 type package capable of supplying in excess of 100mA over a 1.2V to 37V output range. This device is exceptionally easy to use and both line and load regulation are better than standard fixed regulators.

In addition to higher performance than fixed regulators, the NTE1900 offers full overload protection available only in IC's. Included on the chip are current limit, thermal overload protection, and safe area protection. All overload protection circuitry remains fully functional even if the adjustment terminal is disconnected.

Normally, no capacitors are needed unless the device is situated far from the input filter capacitors in which case an input bypass is needed. An optional output capacitor can be added to improve transient response. The adjustment terminal can be bypassed to achieve very high ripple rejection ratios which are difficult to achieve with standard 3-terminal regulators.

Besides replacing fixed regulators, the NTE1900 is useful in a wide variety of other applications. Since the regulator is "floating" and sees only the input-to-output differential voltage, supplies of several hundred volts can be regulated as long as the maximum input to output differential is not exceeded.

Also, it makes an especially simple adjustable switching regulator, a programmable output regulator, or by connecting a fixed resistor between the adjustment and output, the NTE1900 can be used as a precision current regulator. Supplies with electronic shutdown can be achieved by clamping the adjustment terminal to GND which programs the output to 1.2V where most loads draw little current.

Features:

- Adjustable Output Down to 1.2V
- Guaranteed 100mA Output Current
- Line Regulation Typically 0.01%/V
- Load Regulation Typically 0.1%
- Current Limit Constant with Temperature
- Eliminates the Need to Stock Many Voltages
- 80dB Ripple Rejection

Absolute Maximum Ratings:

Power Dissipation, P_D	Internally Limited
Input-Output Voltage Differential, $V_I - V_O$	40V
Operating Junction Temperature Range, T_J	-40° to +125°C
Storage Temperature Range, T_{stg}	-65° to +150°C
Lead Temperature (During Soldering, 10sec), T_L	+300°C

Electrical Characteristics: ($-25^{\circ} \leq T_J \leq +125^{\circ}C$, $V_{IN}-V_{OUT} = 5V$, $I_O = 40mA$, $I_{MAX} = 100mA$, Note 1 unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Line Regulation	Reg_{line}	$T_A = +25^{\circ}C$, $3V \leq (V_{IN}-V_{OUT}) \leq 40V$, Note 2	–	0.01	0.04	%/V
		$3V \leq (V_{IN}-V_{OUT}) \leq 40V$	–	0.02	0.07	%/V
Load Regulation	Reg_{load}	$T_A = +25^{\circ}C$, $5mA \leq I_O \leq I_{MAX}$, Note 2	–	0.1	0.5	%
		$5mA \leq I_O \leq 100mA$, Note 2	–	0.3	1.5	%
Thermal Regulation		$T_A = +25^{\circ}C$, 10ms Pulse	–	0.04	0.2	%/W
Adjustment Pin Current	I_{Adj}		–	50	100	μA
Adjustment Pin Current Change	ΔI_{Adj}	$5mA \leq I_L \leq 100mA$, $3V \leq (V_{IN}-V_{OUT}) \leq 40V$, $P \leq 625mW$	–	0.2	5.0	μA
Reference Voltage	V_{ref}	$3V \leq (V_{IN}-V_{OUT}) \leq 40V$, $5mA \leq I_O \leq 100mA$, $P \leq 625mW$, Note 3	1.20	1.25	1.30	V
Temperature Stability	T_S	$-25^{\circ} \leq T_J \leq +125^{\circ}C$	–	0.65	–	%
Minimum Load Current	I_{Lmin}	$(V_{IN}-V_{OUT}) \leq 40V$	–	3.5	5	mA
		$3V \leq (V_{IN}-V_{OUT}) \leq 15V$	–	1.5	2.5	mA
Maximum Output Current Limit	I_{max}	$3V \leq (V_{IN}-V_{OUT}) \leq 13V$	100	200	300	mA
		$V_{IN}-V_{OUT} = 40V$	25	50	150	mA
RMS Output Noise, % of V_{OUT}	N	$T_A = +25^{\circ}C$, $10Hz \leq f \leq 10kHz$	–	0.003	–	%
Ripple Rejection Ratio	RR	$V_{OUT} = 10V$, $f = 120Hz$, $C_{ADJ} = 0$	–	65	–	dB
		$C_{ADJ} = 10\mu F$	66	80	–	dB
Long Term Stability	S	$T_J = +125^{\circ}C$, 1000 Hours	–	0.3	1.0	%

Note 1. Although power dissipation is internally limited, these specifications are applicable for power dissipations of 625mW.

Note 2. Regulation is measured at constant junction temperature, using pulse testing with a low duty cycle. Changes in output voltage due to heating effects are covered under the specification for thermal regulation.

Note 3. Thermal resistance is $180^{\circ}C/W$ junction to ambient with 0.4" leads from a PC board and $160^{\circ}C/W$ junction to ambient with 0.125" lead length to PC board.

