



NTE2402 (NPN) & NTE2403 (PNP) Silicon Complementary Transistors Low Noise, UHF/VHF Amplifier

Description:

The NTE2402 (NPN) and NTE2403 (PNP) are silicon complementary transistors in an SOT-23 type surface mount package designed for use in UHF and microwave amplifiers in thick and thin-film circuits, such as in aerial amplifiers, radar systems, oscilloscopes, spectrum analysers, etc. These transistors feature low intermodulation distortion and high power gain. Due to very high transition frequency, these devices also have excellent wideband properties and low noise up to high frequencies.

Absolute Maximum Ratings:

Collector-Base Voltage, V_{CBO}	20V
Collector-Emitter Voltage, V_{CEO}	15V
Emitter-Base Voltage, V_{EBO}	2V
DC Collector Current, I_C	25mA
Total Power Dissipation ($T_A \leq +60^\circ\text{C}$, Note 1), P_{tot}	200mW
Operating Junction Temperature, T_J	$+150^\circ\text{C}$
Storage Temperature Range, T_{stg}	-65° to $+150^\circ\text{C}$
Thermal Resistance, Junction-to-Ambient (Note 1), R_{thJA}	430K/W

Note 1. Mounted on a ceramic substrate of .314 (8mm) x .393 (10mm) x .027 (0.7mm).

Electrical Characteristics: ($T_J = +25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Collector Cutoff Current	I_{CBO}	$V_{CB} = 10\text{V}$, $I_E = 0$	—	—	50	nA
DC Current Gain	h_{FE}	$V_{CE} = 10\text{V}$, $I_C = 14\text{mA}$	25	50	—	
Transition Frequency	f_T	$V_{CE} = 10\text{V}$, $I_C = 14\text{mA}$, $f = 500\text{MHz}$	—	5	—	GHz
Collector Capacitance	C_C	$V_{CB} = 10\text{V}$, $I_E = I_C = 0$, $f = 1\text{MHz}$	—	0.75	—	pF
Emitter Capacitance	C_e	$V_{EB} = 0.5\text{V}$, $I_C = I_E = 0$, $f = 1\text{MHz}$	—	0.8	—	pF
Feedback Capacitance	C_{re}	$V_{CE} = 10\text{V}$, $I_C = 2\text{mA}$, $f = 1\text{MHz}$, $T_A = +25^\circ\text{C}$	—	0.4	—	pF

Electrical Characteristics (Cont'd): ($T_J = +25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Noise Figure (At Optimum Source Impedance)	F	$V_{CE} = 10\text{V}$, $I_C = 2\text{mA}$, $f = 500\text{MHz}$, $T_A = +25^{\circ}\text{C}$	–	2.4	–	dB
Max. Unilateral Power Gain (S_{re} Assumed to be Zero)	G_{UM}	$V_{CE} = 10\text{V}$, $I_C = 2\text{mA}$, $f = 500\text{MHz}$, $T_A = +25^{\circ}\text{C}$, Note 2	–	18	–	dB
Output Voltage (At $d_{im} = -60\text{dB}$)	V_O	$V_{CE} = 10\text{V}$, $I_C = 14\text{mA}$, $R_L = 75\Omega$, $T_A = +25^{\circ}\text{C}$, $f_{(p+q-r)} = 493,25\text{MHz}$	–	150	–	mV

Note 2. $G_{UM} = 10 \log |S_{fe}|^2 / [1 - |S_{ie}|^2] [1 - |S_{oe}|^2]$.

