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# RHRG1540CC, RHRG1560CC

# 30 A, 400 V - 600 V Hyperfast Dual Diode

## **Description**

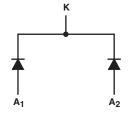
The RHRG1540CC, RHRG1560CC is a hyperfast dual diode with soft recovery characteristics. It has the half recovery time of ultrafast diodes and is silicon nitride passivated ionimplanted epitaxial planar construction. These devices are intended to be used as freewheeling/clamping diodes and diodes in a variety of switching power supplies and other power switching applications. Their low stored charge and hyperfast soft recovery minimize ringing and electrical noise in many power switching circuits reducing power loss in the switching transistors.

### **Ordering Information**

PART NUMBER	PACKAGE	BRAND			
RHRG1540CC	TO-247-3L	RHRG1540C			
RHRG1560CC	TO-247-3L	RHRG1560C			

NOTE: When ordering, use the entire part number.

# Symbol



#### Features

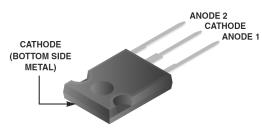
- Hyperfast Recovery  $t_{rr}$  = 40 ns (@  $I_F$  = 15 A)
- Max Forward Voltage,  $V_F = 2.1 \text{ V } (@ T_C = 25^{\circ}\text{C})$
- 400 V, 600 V Reverse Voltage and High Reliability
- · Avalanche Energy Rated
- RoHS Compliant

### **Applications**

- · Switching Power Supplies
- Power Switching Circuits
- General Purpose

### **Packaging**

#### JEDEC STYLE TO-247



#### **Absolute Maximum Rating** (Per Leg) T<sub>C</sub> = 25°C, Unless Otherwise Specified

RHRG1540CC	RHRG1560CC	UNITS	
400	600	V	
400	600	V	
400	600	V	
15	15	Α	
30	30	Α	
200	200	Α	
100	100	W	
. 20	20	mJ	
-65 to 175	-65 to 175	°C	
ו ו	400 400 400 15 30 1 200 100 20	400 600 400 600 400 600 15 15 30 30 200 200 100 100 20 20	400 600 V 400 600 V 15 15 A 30 30 A 200 200 A 100 100 W 20 20 mJ

**Electrical Specifications** (Per Leg)  $T_C = 25^{\circ}C$ , Unless Otherwise Specified

SYMBOL	TEST CONDITION	ı	RHRG1540CC		RHRG1560CC			
		MIN	TYP	MAX	MIN	TYP	MAX	UNIT
V <sub>F</sub>	I <sub>F</sub> = 15 A	-	-	2.1	-	-	2.1	V
	I <sub>F</sub> = 15 A, T <sub>C</sub> = 150 <sup>o</sup> C	-	-	1.7	-	-	1.7	V
I <sub>R</sub>	V <sub>R</sub> = 400 V	-	-	100	-	-	-	μΑ
	V <sub>R</sub> = 600 V	-	-	-	-	-	100	μΑ
	$V_R = 400 \text{ V}, T_C = 150^{\circ}\text{C}$	-	-	500	-	-	-	μΑ
	$V_R = 600 \text{ V}, T_C = 150^{\circ}\text{C}$	-	-	-	-	-	500	μΑ
t <sub>rr</sub>	$I_F = 1 \text{ A, } dI_F/dt = 100 \text{ A/}\mu\text{s}$	-	-	35	-	-	35	ns
	$I_F = 15 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}$	-	-	40	-	-	40	ns
ta	$I_F = 15 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}$	-	20	-	-	20	-	ns
t <sub>b</sub>	$I_F = 15 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}$	-	15	-	-	15	-	ns
Q <sub>rr</sub>	$I_F = 15 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}$	-	40	-	-	40	-	nC
СЈ	V <sub>R</sub> = 10 V, I <sub>F</sub> = 0 A	-	60	-	-	60	-	pF
$R_{ heta JC}$		-	-	1.5	-	-	1.5	°C/W

#### **DEFINITIONS**

 $V_F$  = Instantaneous forward voltage (pw = 300  $\mu$ s, D = 2%).

I<sub>R</sub> = Instantaneous reverse current.

 $T_{rr}$  = Reverse recovery time (See Figure 9), summation of  $t_a + t_b$ .

 $t_a$  = Time to reach peak reverse current (See Figure 9).

 $t_b$  = Time from peak  $I_{RM}$  to projected zero crossing of  $I_{RM}$  based on a straight line from peak  $I_{RM}$  through 25% of  $I_{RM}$  (See Figure 9).

 $Q_{rr}$  = Reverse Recovery Charge.

C<sub>J</sub> = Junction Capacitance.

 $R_{\theta JC}$  = Thermal resistance junction to case.

pw = pulse width.

D = duty cycle.

# **Typical Performance Curves**

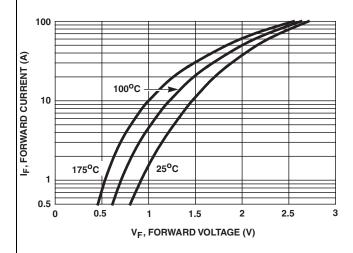


FIGURE 1. FORWARD CURRENT vs FORWARD VOLTAGE

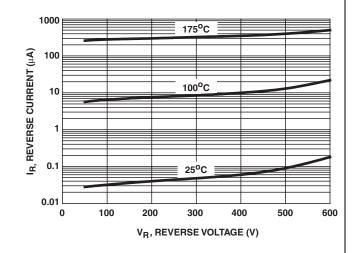


FIGURE 2. REVERSE CURRENT vs REVERSE VOLTAGE

# Typical Performance Curves (Continued)

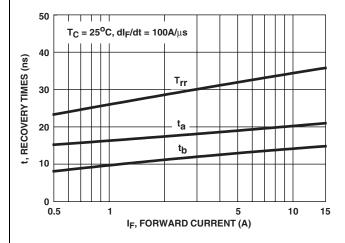


FIGURE 3.  $T_{rr}$ ,  $t_a$  and  $t_b$  curves vs forward current

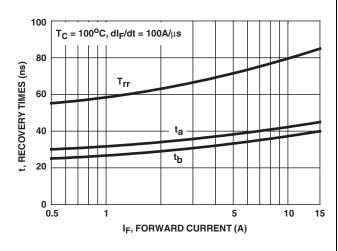


FIGURE 4. T<sub>rr</sub>, t<sub>a</sub> AND t<sub>b</sub> CURVES vs FORWARD CURRENT

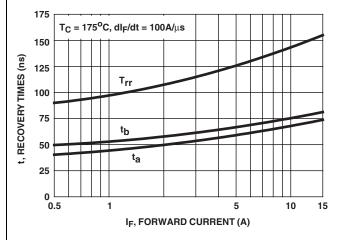


FIGURE 5.  $T_{rr}$ ,  $t_a$  AND  $t_b$  CURVES vs FORWARD CURRENT

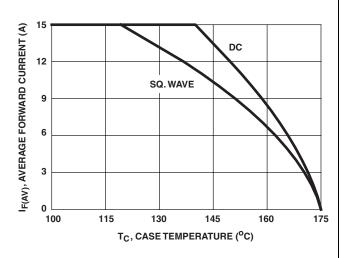


FIGURE 6. CURRENT DERATING CURVE

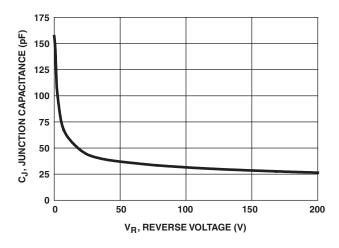


FIGURE 7. JUNCTION CAPACITANCE vs REVERSE VOLTAGE

#### Test Circuits and Waveforms

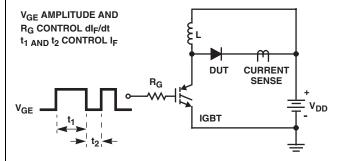


FIGURE 8. T<sub>rr</sub> TEST CIRCUIT

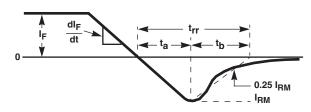


FIGURE 9. T<sub>rr</sub> WAVEFORMS AND DEFINITIONS

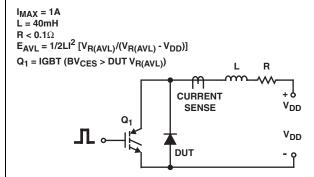


FIGURE 10. AVALANCHE ENERGY TEST CIRCUIT

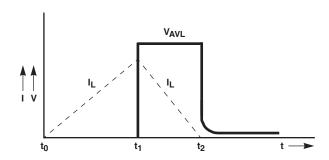


FIGURE 11. AVALANCHE CURRENT AND VOLTAGE WAVEFORMS

# **Package Dimensions** В 15.87 E A 12.81/E Ø3.65 € Ø 6.85 6.61 Ø 3.65 3.51 ⊕ 0.254 M B AM 5.58 5.34 1.35 13.08 MIN 20.82 20.32 E 3 3.93 E 16.25 E ( 1.60

NOTES: UNLESS OTHERWISE SPECIFIED.

2.66 2.42

5.56

11.12

- A. PACKAGE REFERENCE: JEDEC TO-247, ISSUE E, VARIATION AB, DATED JUNE, 2004.
- B. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.

3

0.254 M B AM

- C. ALL DIMENSIONS ARE IN MILLIMETERS.
- D. DRAWING CONFORMS TO ASME Y14.5 1994

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