

RJP63F3DPP-M0

Silicon N Channel IGBT
High Speed Power Switching

R07DS0321EJ0200

Rev.2.00

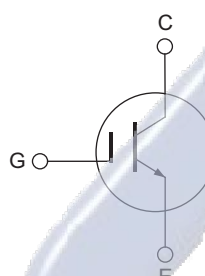
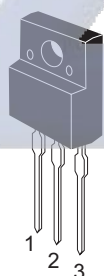
May 26, 2011

Features

- Trench gate and thin wafer technology (G6H series)
- Low collector to emitter saturation voltage $V_{CE(sat)} = 1.7\text{ V typ}$
- High speed switching $t_f = 100\text{ ns typ}$
- Low leak current $I_{CES} = 1\text{ }\mu\text{A max}$
- Isolated package TO-220FL

Outline

RENESAS Package code: PRSS0003AF-A)
(Package name: TO-220FL)



1. Gate
2. Collector
3. Emitter

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Ratings	Unit
Collector to emitter voltage	V_{CES}	630	V
Gate to emitter voltage	V_{GES}	±30	V
Collector current	I_C	40	A
Collector peak current	$i_{c(peak)}$ ^{Note1}	200	A
Collector dissipation	P_C ^{Note2}	30	W
Junction to case thermal impedance	θ_{j-c}	4.17	°C/W
Junction temperature	T_j	150	°C
Storage temperature	T_{stg}	-55 to +150	°C

Notes: 1. $PW \leq 10\text{ }\mu\text{s}$, duty cycle $\leq 1\%$

2. $T_c = 25^\circ\text{C}$

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Electrical Characteristics

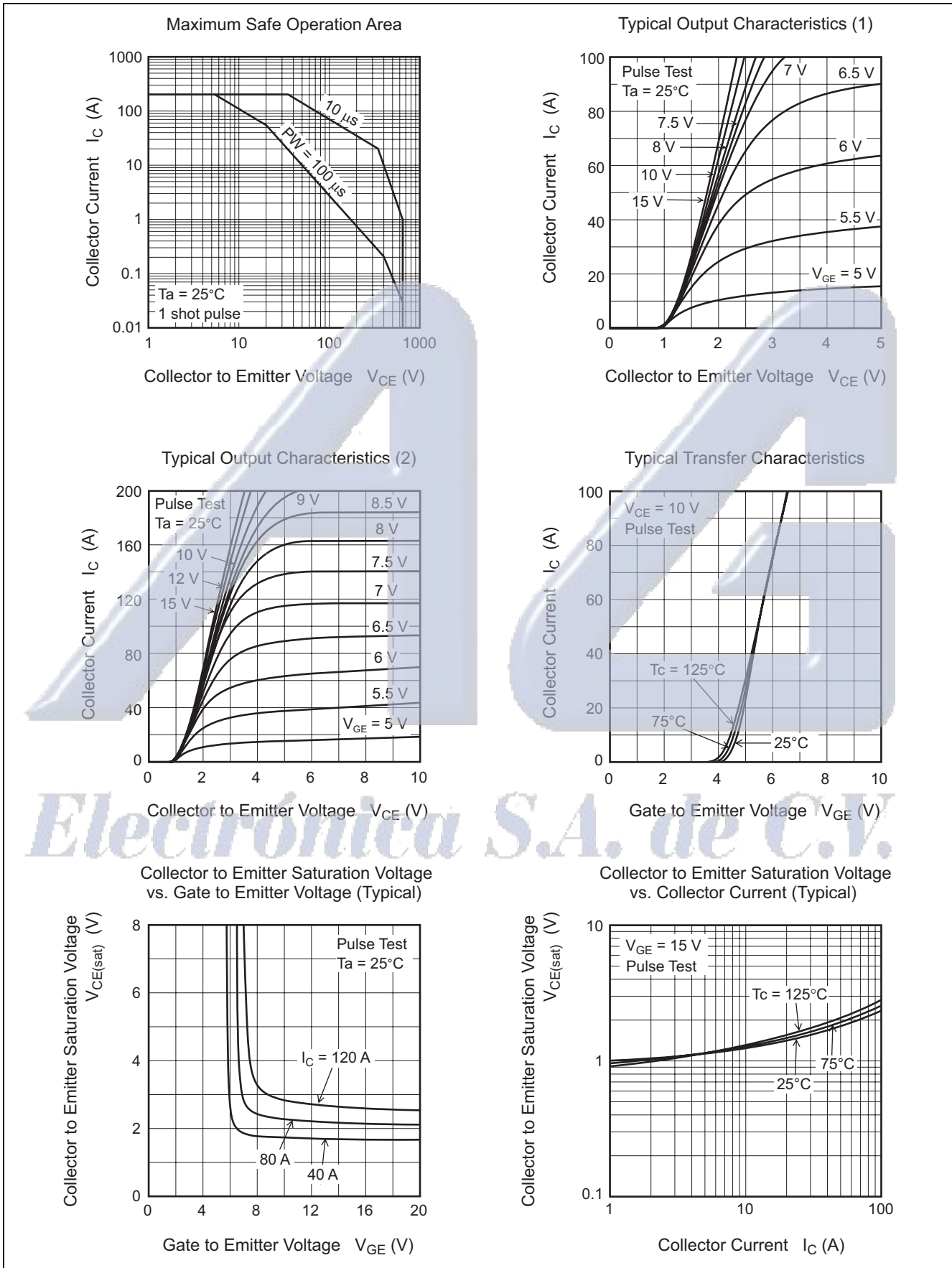
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Zero gate voltage collector current	I_{CES}	—	—	1	μA	$V_{CE} = 630 \text{ V}, V_{GE} = 0$
Gate to emitter leak current	I_{GES}	—	—	± 100	nA	$V_{GE} = \pm 30 \text{ V}, V_{CE} = 0$
Gate to emitter cutoff voltage	$V_{GE(off)}$	2.5	—	5	V	$V_{CE} = 10 \text{ V}, I_C = 1 \text{ mA}$
Collector to emitter saturation voltage	$V_{CE(sat)}$	—	1.7	2.2	V	$I_C = 40 \text{ A}, V_{GE} = 15 \text{ V}^{\text{Note3}}$
Input capacitance	C_{ies}	—	1250	—	pF	$V_{CE} = 25 \text{ V}$
Output capacitance	C_{oes}	—	48	—	pF	$V_{GE} = 0$
Reveres transfer capacitance	C_{res}	—	22	—	pF	$f = 1 \text{ MHz}$
Total gate charge	Q_g	—	36	—	nC	$V_{GE} = 15 \text{ V}$
Gate to emitter charge	Q_{ge}	—	7	—	nC	$V_{CE} = 300 \text{ V}$
Gate to collector charge	Q_{gc}	—	10	—	nC	$I_C = 40 \text{ A}$
Switching time	$t_{d(on)}$	—	0.02	—	μs	$I_C = 40 \text{ A}$
	t_r	—	0.07	—	μs	$R_L = 7.5 \Omega$
	$t_{d(off)}$	—	0.05	—	μs	$V_{GE} = 15 \text{ V}$
	t_f	—	0.1	—	μs	$R_g = 5 \Omega$

Notes: 3. Pulse test.

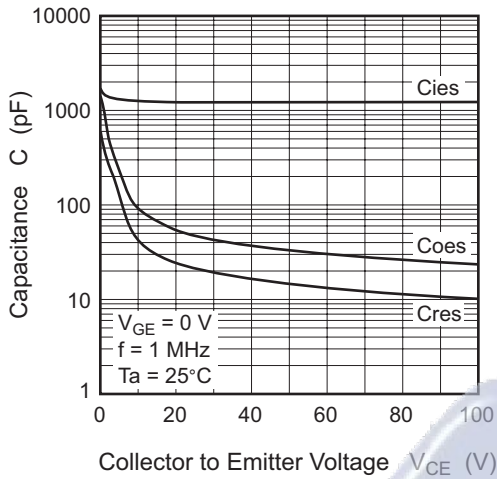
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Main Characteristics

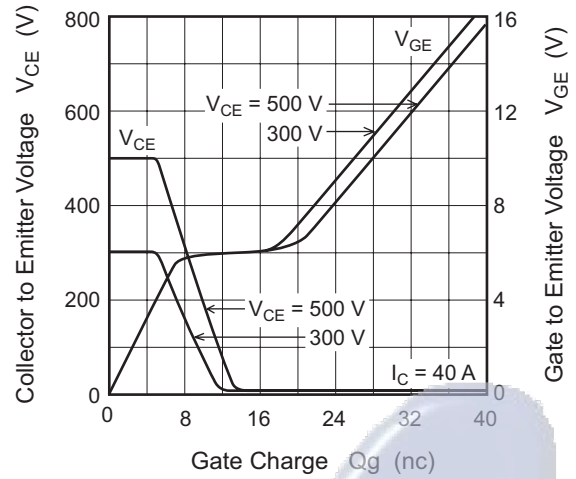


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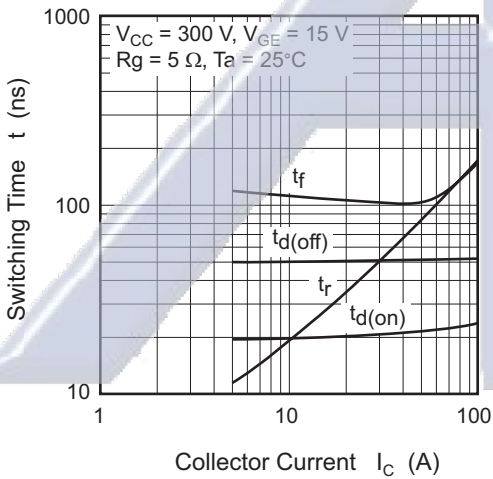
Typical Capacitance vs. Collector to Emitter Voltage (Typical)



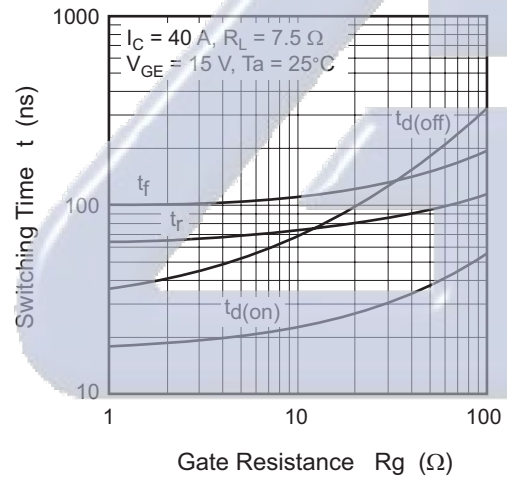
Dynamic Input Characteristics (Typical)



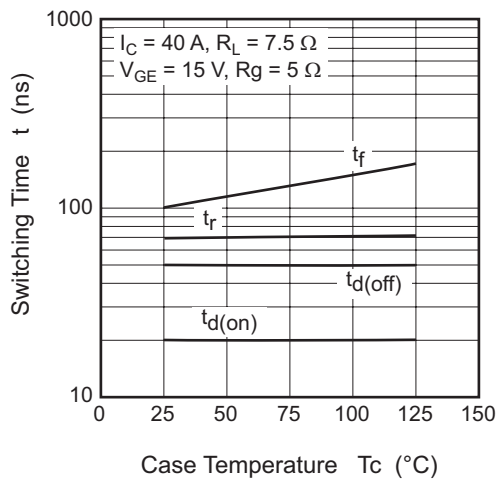
Switching Characteristics (Typical) (1)



Switching Characteristics (Typical) (2)

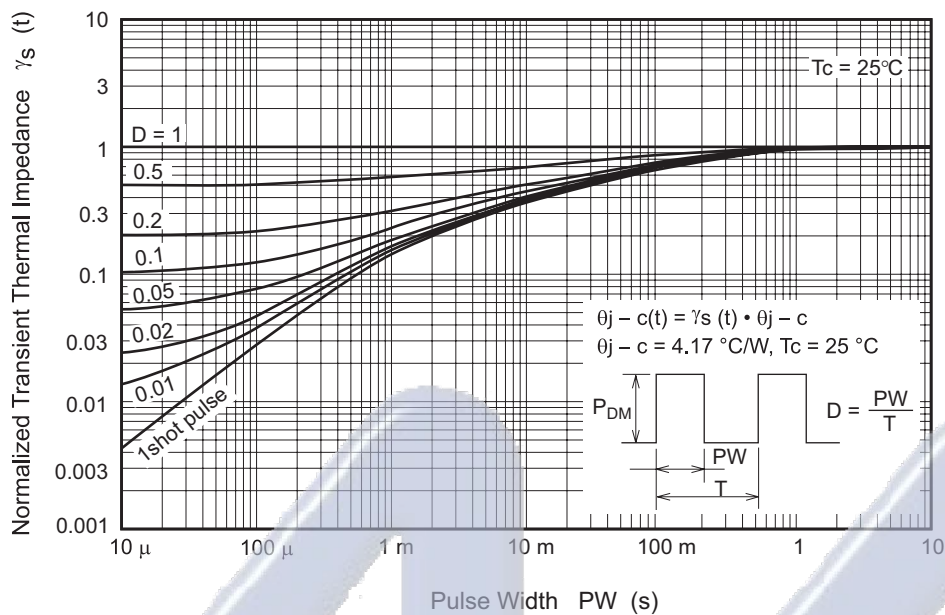


Switching Characteristics (Typical) (3)

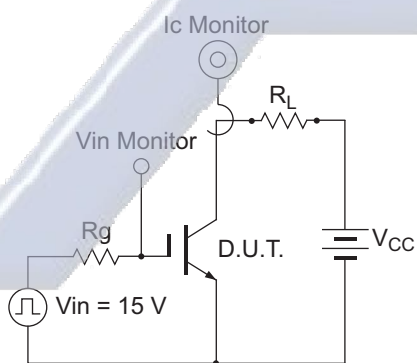


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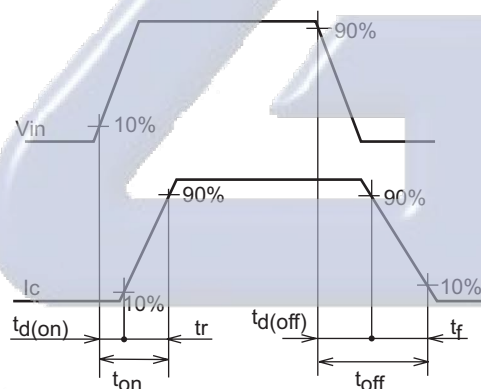
Normalized Transient Thermal Impedance vs. Pulse Width



Switching Time Test Circuit



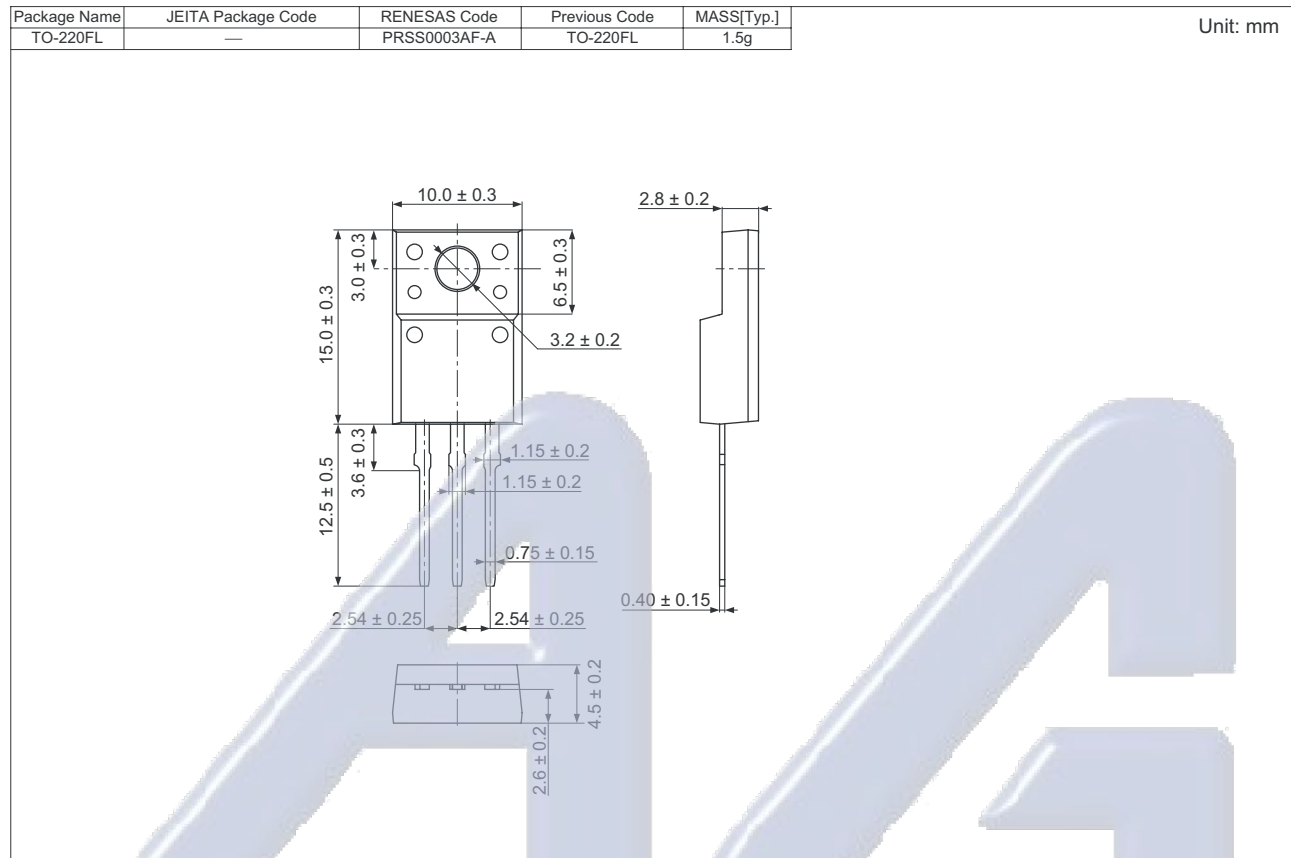
Waveform



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Package Dimension



Ordering Information

Orderable Part No.	Quantity	Shipping Container
RJP63F3DPP-M0-T2	600 pcs	Box (Tube)

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