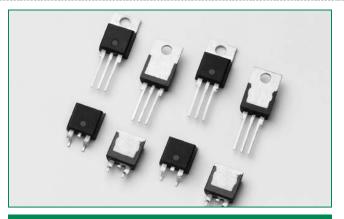


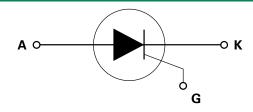
25 Amps High Junction Temperature SCRs

SJxx25xx Series





Schematic Symbol



Agency Approval

Agency	Agency File Number
71	L Packages: E71639

Description

This SJxx25xx high temperature SCR series is ideal for uni-directional switch applications such as phase control in heating, motor speed controls, converters/rectifiers and inrush current controllers. These SCRs have a low gate current trigger level of 6mA, 10mA or 35mA maximum at approximately 1.5V

Features & Benefits

- Halogen free and RoHS compliant
- 150°C maximum junction temperature
- Surge capability up to 350 A at 60 Hz half cycle

Applications

Typical applications include AC Generator (ACG) rectifiers, battery voltage regulators, generic converters, inrush current controller in various AC to DC applications and soft starter for low power AC motor. Additional applications include controls for power tools, home/brown good and white goods appliances.

Isolated packages offered for ease of heat sinking.

Main Features

Symbol	Value	Unit
I _{T(RMS)}	25	А
V_{DRM}/V_{RRM}	400 or 600	V
I _{GT}	6,10, to 35	mA

Absolute Maximum Ratings

Symbol	Parameter	Test Conditions		Value	Unit
V_{DRM}/V_{RSM}	Peak non-repetitive blocking voltage	Pw=100 μs		V _{DRM} /V _{RRM} +100	V
		SJxx25Lx	T _C = 100°C		
I _{T(RMS)}	RMS on-state current	SJxx25Rx	T _C = 125°C	25	А
		SJxx25Nx	1 _C = 120 0		
		SJxx25Lx	$T_{\rm C} = 100^{\circ} C$		
I _{T(AV)}	Average on-state current	SJxx25Rx	T _ 125°C	16	Α
		SJxx25Nx	$T_c = 125$ °C		
		single half cycle; f = 50Hz;		300	А
I _{TSM}	Peak non-repetitive surge current	T _J (initial) = 25°C			
13101	, ,	single half cycle; $f = 60Hz$; T_J (initial) = 25°C		350	
l²t	I²t Value for fusing	$t_p = 8$.3 ms	510	A ² s
di/dt	Critical rate of rise of on-state current	f = 60Hz ; T _J = 150°C		125	A/µs
I _{GM}	Peak gate current	T _J = 150°C		3	Α
P _{G(AV)}	Average gate power dissipation	T _J = 150°C		0.6	W
T _{stg}	Storage temperature range			-40 to 150	°C
T	Operating junction temperature range			-40 to 150	°C

Note: xx=voltage/10, x=sensitivity

SJxx25xx Series



Electrical Characteristics (T_J = 25°C, unless otherwise specified)

Symbol Test Conditions		Value			Unit	
Зуппрог	rest Conditions			SJxx25x1	SJxx25x2	Offic
1	V 12V P 60.0	MAX.	35	6	10	mA
I _{GT}	$V_D = 12V R_L = 60 \Omega$	MIN.	8	2	5	IIIA
V_{GT}	$V_D = 12V R_L = 60 \Omega$ MAX.			1.5		V
dv/dt	$V_D = 67\% V_{DRM}$; gate open; $T_J = 125$ °C	MIN.	800	70	500	V/µs
dv/dt	$V_D = 67\% V_{DRM}$; gate open; $T_J = 150$ °C	IVIIIN.	400	-	200	ν/μ5
V_{GD}	$V_D = V_{DRM} R_L = 3.3 \text{ k}\Omega T_J = 125^{\circ}\text{C}$	MIN.	MIN. 0.2		V	
I _H	$I_{T} = 400 \text{mA} \text{ (initial)}$	MAX.	75	15	35	mA
t _q	$I_{T} = 2A$; $t_{p} = 50\mu s$; $dv/dt = 5V/\mu s$; $di/dt = 30A/\mu s$	MAX.		40		μs
t _{gt}	$I_{g} = 2 \times I_{gT}$ PW = 15 μ s $I_{T} = 50$ A	TYP.		2		μs

Note: xx=voltage/10, x=package

Static Characteristics

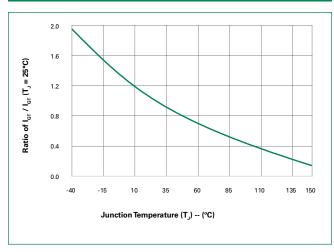
Symbol	Test Conditions			Value	Unit
V _{TM}	Com	Component $I_T = 50A$; $t_p = 380 \mu s$ MAX.			V
	T _J = 25°C		10		
I _{DRM} / I _{RRM}	I_{DRM} / I_{RRM} $V_{DRM} = V_{RRM}$	T _J = 125°C	MAX.	1000	μΑ
		T _J = 150°C		3000	

Thermal Resistances

Symbol	Parameter		Value	Unit
$R_{\theta(JC)}$	Junction to case (AC)	SJxx25Rx SJxx25Nx	1.0	°C/W
' 'θ(JC)	Canadan to case y ter	SJxx25Lx	2.3	3,77

Note: xx=voltage/10, x=sensitivity

Figure 1: Normalized DC Gate Trigger Current vs. Junction Temperature



SJxx25xx Series

Figure 2: Normalized DC Gate Trigger Voltage vs. Junction Temperature

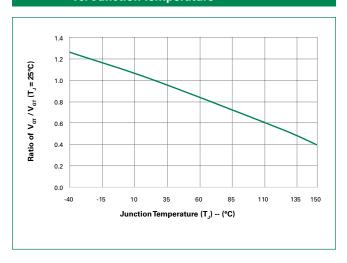




Figure 3: Normalized DC Holding Current vs. Junction Temperature

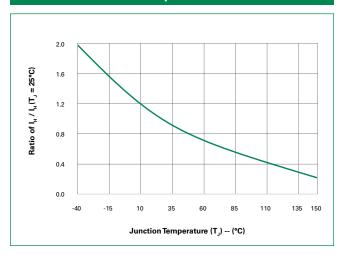


Figure 5: Power Dissipation (Typical) vs. RMS On-State Current

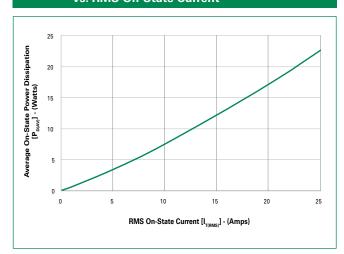
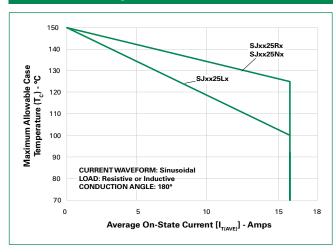


Figure 7: Maximum Allowable Case Temperature vs. Average On-State Current



SJxx25xx Series

Figure 4: On-State Current vs. On-State Voltage (Typical)

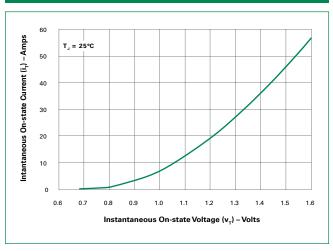


Figure 6: Maximum Allowable Case Temperature vs. RMS On-State Current

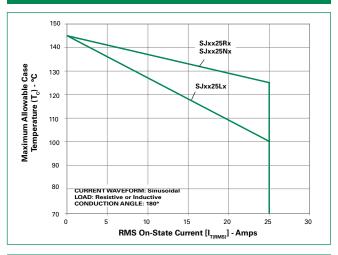


Figure 8: Peak Capacitor Discharge Current

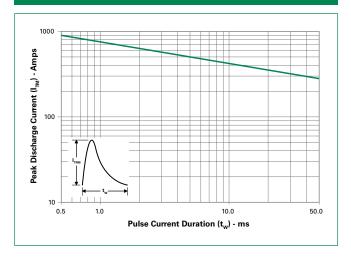




Figure 9: Peak Capacitor Discharge Current Derating

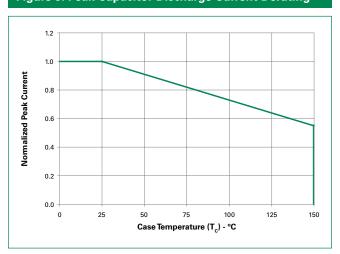
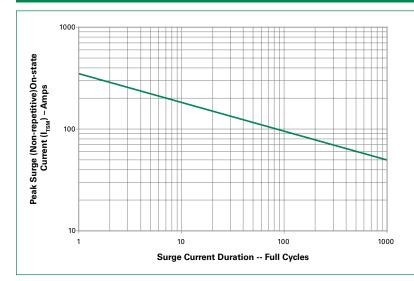


Figure 10: Surge Peak On-State Current vs. Number of Cycles



SUPPLY FREQUENCY: 60 Hz Sinusoidal

LOAD: Resistive

RMS On-State Current: [$I_{T(RMS)}$]: Maximum Rated Value at Specified Case Temperature

Notes

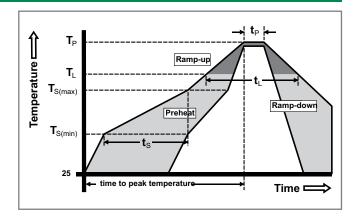
- Gate control may be lost during and immediately following surge current interval.
- Overload may not be repeated until junction temperature has returned to steady-state rated value.



25 Amps High Junction Temperature SCRs

Soldering Parameters

Reflow Co	ndition	Pb – Free assembly	
	-Temperature Min (T _{s(min)})	150°C	
Pre Heat	-Temperature Max (T _{s(max)})	200°C	
	-Time (min to max) (t _s)	60 – 180 secs	
Average ramp up rate (Liquidus Temp) (T _L) to peak		5°C/second max	
T _{S(max)} to T _L	- Ramp-up Rate	5°C/second max	
Reflow	-Temperature (T _L) (Liquidus)	217°C	
nenow	-Time (t _L)	60 – 150 seconds	
PeakTemperature (T _P)		260 ^{+0/-5} °C	
Time within 5°C of actual peak Temperature (t _p)		20 – 40 seconds	
Ramp-down Rate		5°C/second max	
Time 25°C to peak Temperature (T _P)		8 minutes Max.	
Do not exc	ceed	280°C	



Physical Specifications

Terminal Finish	100% Matte Tin-plated
Body Material	UL Recognized compound meeting flammability rating V-0.
Lead Material	Copper Alloy

Design Considerations

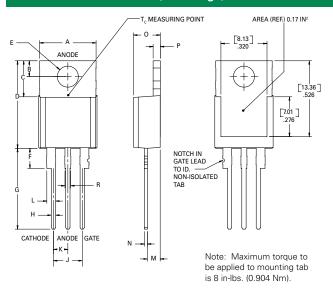
Careful selection of the correct component for the application's operating parameters and environment will go a long way toward extending the operating life of the Thyristor. Good design practice should limit the maximum continuous current through the main terminals to 75% of the component rating. Other ways to ensure long life for a power discrete semiconductor are proper heat sinking and selection of voltage ratings for worst case conditions. Overheating, overvoltage (including dv/dt), and surge currents are the main killers of semiconductors. Correct mounting, soldering, and forming of the leads also help protect against component damage.

Environmental Specifications

Test	Specifications and Conditions
AC Blocking	MIL-STD-750, M-1040, Cond A Applied Peak AC voltage @ 150°C for 1008 hours
Temperature Cycling	MIL-STD-750, M-1051, 100 cycles; -55°C to +150°C; 15-min dwell-time
Temperature/ Humidity	EIA / JEDEC, JESD22-A101 1008 hours; 160V - DC: 85°C; 85% rel humidity
High Temp Storage	MIL-STD-750, M-1031, 1008 hours; 150°C
Low-Temp Storage	1008 hours; -40°C
Resistance to Solder Heat	MIL-STD-750 Method 2031
Solderability	ANSI/J-STD-002, category 3, Test A
Lead Bend	MIL-STD-750, M-2036 Cond E
Moisture Sensitivity Level	Level 1, JEDEC-J-STD-020D

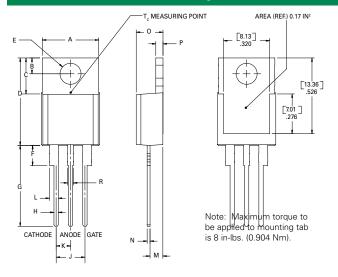


Dimensions — TO-220AB (R-Package) — Non-Isolated Mounting Tab Common with Center Lead



Dimension	Inc	hes	Millin	neters
Dimension	Min	Max	Min	Max
А	0.380	0.420	9.65	10.67
В	0.105	0.115	2.67	2.92
С	0.230	0.250	5.84	6.35
D	0.590	0.620	14.99	15.75
Е	0.142	0.147	3.61	3.73
F	0.110	0.130	2.79	3.30
G	0.540	0.575	13.72	14.61
Н	0.025	0.035	0.64	0.89
J	0.195	0.205	4.95	5.21
K	0.095	0.105	2.41	2.67
L	0.060	0.075	1.52	1.91
М	0.085	0.095	2.16	2.41
N	0.018	0.024	0.46	0.61
0	0.178	0.188	4.52	4.78
Р	0.045	0.060	1.14	1.52
R	0.038	0.048	0.97	1.22

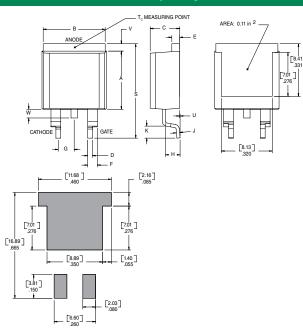
Dimensions — TO-220AB (L-Package) — Isolated Mounting Tab



Dimension	Inc	hes	Millin	neters
Difficusion	Min	Max	Min	Max
А	0.380	0.420	9.65	10.67
В	0.105	0.115	2.67	2.92
С	0.230	0.250	5.84	6.35
D	0.590	0.620	14.99	15.75
Е	0.142	0.147	3.61	3.73
F	0.110	0.130	2.79	3.30
G	0.540	0.575	13.72	14.61
Н	0.025	0.035	0.64	0.89
J	0.195	0.205	4.95	5.21
K	0.095	0.105	2.41	2.67
L	0.060	0.075	1.52	1.91
М	0.085	0.095	2.16	2.41
N	0.018	0.024	0.46	0.61
0	0.178	0.188	4.52	4.78
Р	0.045	0.060	1.14	1.52
R	0.038	0.048	0.97	1.22



Dimensions –TO- 263AB (N-package) — D²-Pak Surface Mount



Dimension	Inches		Millin	neters
Difficusion	Min	Max	Min	Max
А	0.360	0.370	9.14	9.40
В	0.380	0.420	9.65	10.67
С	0.178	0.188	4.52	4.78
D	0.025	0.035	0.64	0.89
Е	0.045	0.060	1.14	1.52
F	0.060	0.075	1.52	1.91
G	0.095	0.105	2.41	2.67
Н	0.092	0.102	2.34	2.59
J	0.018	0.024	0.46	0.61
K	0.090	0.110	2.29	2.79
S	0.590	0.625	14.99	15.88
V	0.035	0.045	0.89	1.14
U	0.002	0.010	0.05	0.25
W	0.040	0.070	1.016	1.78

Product Selector

Part Number	Voltage		Cata Camaisinis	T	Dankana
	400V	600V	Gate Sensitivity	Туре	Package
SJxx25L	X	X	35mA	Standard SCR	TO-220L
SJxx25R	X	X	35mA	Standard SCR	TO-220R
SJxx25N	X	X	35mA	Standard SCR	TO-263
SJxx25L1	X	X	6mA	Standard SCR	TO-220L
SJxx25R1	X	X	6mA	Standard SCR	TO-220R
SJxx25N1	Х	X	6mA	Standard SCR	TO-263
SJxx25L2	X	X	10mA	Standard SCR	TO-220L
SJxx25R2	X	X	10mA	Standard SCR	TO-220R
SJxx25N2	X	X	10mA	Standard SCR	TO-263

Note: xx = Voltage/10

Packing Options

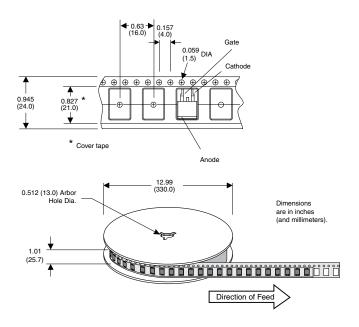
Part Number	Marking	Weight	Packing Mode	Base Quantity
SJxx25LxTP	SJxx25Lx	2.2g	Tube	500 (50 per tube)
SJxx25RxTP	SJxx25Rx	2.2g	Tube	500 (50 per tube)
SJxx25NxTP	SJxx25Nx	1.6g	Tube	500 (50 per tube)
SJxx25NxRP	SJxx25Nx	1.6g	Embossed Carrier	500

Note: xx=voltage/10, x=sensitivity

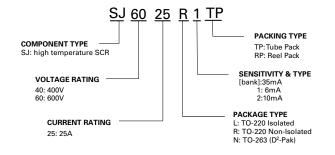


TO-263 Embossed Carrier Reel Pack (RP) Specifications

Meets all EIA-481-2 Standards

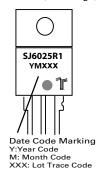


Part Numbering System



Part Marking System

TO-220 AB - (L and R Package) TO-263 AB - (N Package)



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