

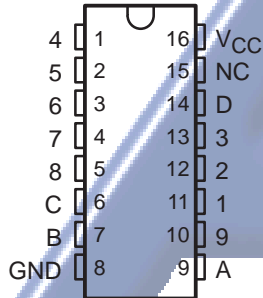
# SN54147, SN54148, SN54LS147, SN54LS148 SN74147, SN74148 (TIM9907), SN74LS147, SN74LS148 10-LINE TO 4-LINE AND 8-LINE TO 3-LINE PRIORITY ENCODERS

SDLS053B – OCTOBER 1976 – REVISED MAY 2004

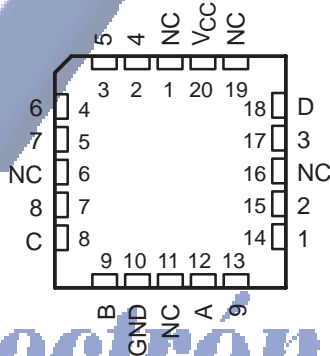
## '147, 'LS147

- Encode 10-Line Decimal to 4-Line BCD
- Applications Include:
  - Keyboard Encoding
  - Range Selection

SN54147, SN54LS147 ... J OR W PACKAGE  
SN74147, SN74LS147 ... D OR N PACKAGE  
(TOP VIEW)



SN54LS147 ... FK PACKAGE  
(TOP VIEW)

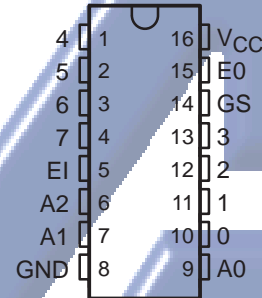


NC.— No internal connection

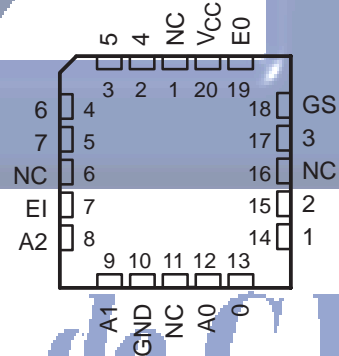
## '148, 'LS148

- Encode 8 Data Lines to 3-Line Binary (Octal)
- Applications Include:
  - n-Bit Encoding
  - Code Converters and Generators

SN54148, SN54LS148 ... J OR W PACKAGE  
SN74148, SN74LS148 ... D, N, OR NS PACKAGE  
(TOP VIEW)



SN54LS148 ... FK PACKAGE  
(TOP VIEW)



*Electrónica S.A. de C.V.*

TYPE	TYPICAL DATA DELAY	TYPICAL POWER DISSIPATION
'147	10 ns	225 mW
'148	10 ns	190 mW
'LS147	15 ns	60 mW
'LS148	15 ns	60 mW

NOTE: The SN54147, SN54LS147, SN54148, SN74147, SN74LS147, and SN74148 are obsolete and are no longer supplied.



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PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

**TEXAS INSTRUMENTS**

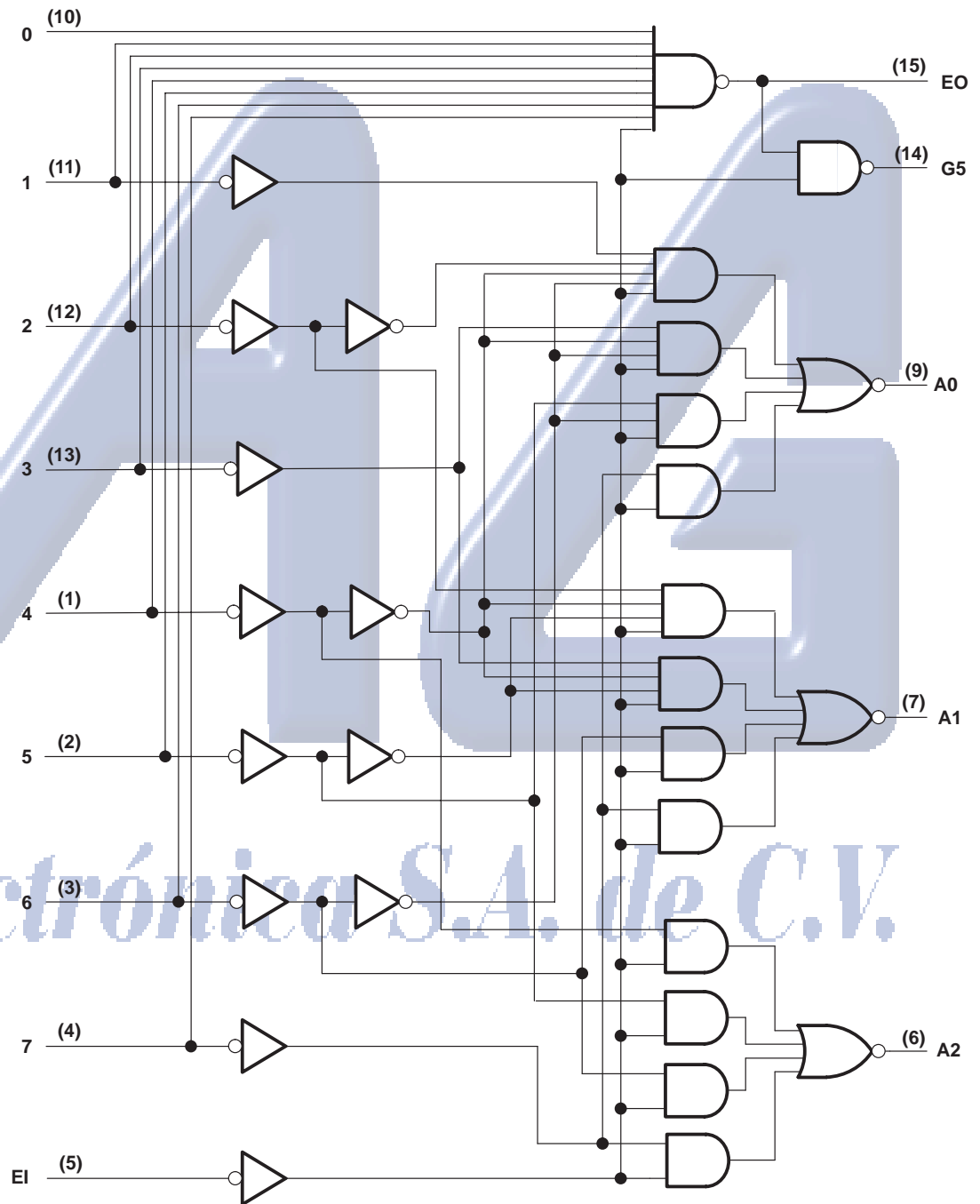
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On products compliant to MIL-PRF-38535, all parameters are tested unless otherwise noted. On all other products, production processing does not necessarily include testing of all parameters.

# SN54147, SN54148, SN54LS147, SN54LS148 SN74147, SN74148 (TIM9907), SN74LS147, SN74LS148 10-LINE TO 4-LINE AND 8-LINE TO 3-LINE PRIORITY ENCODERS

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'148, 'LS148 logic diagram (positive logic)



Pin numbers shown are for D, J, N, NS, and W packages.