



SUPER SSP20N60S / SSF20N60S 600V N-Channel MOSFET

September, 2012

SJ-FET

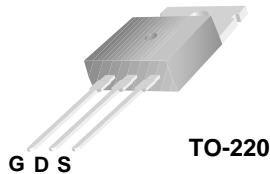
Description

SJ-FET is new generation of high voltage MOSFET family that is utilizing an advanced charge balance mechanism for outstanding low on-resistance and lower gate charge performance.

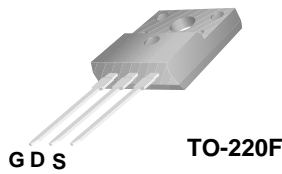
This advanced technology has been tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate and higher avalanche energy. SJ-FET is suitable for various AC/DC power conversion in switching mode operation for higher efficiency.

Features

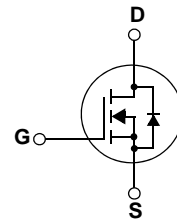
- 650V @ $T_J = 150^\circ\text{C}$
- Typ. $R_{DS(on)} = 0.16 \Omega$
- Ultra Low Gate Charge (typ. $Q_g = 70\text{nC}$)
- 100% avalanche tested



TO-220



TO-220F



Absolute Maximum Ratings

Symbol	Parameter	SSP20N60S	SSF20N60S	Unit
V_{DSS}	Drain-Source Voltage	600		V
I_D	Drain Current - Continuous ($T_C = 25^\circ\text{C}$) - Continuous ($T_C = 100^\circ\text{C}$)	20	20*	A
		12	12*	A
I_{DM}	Drain Current - Pulsed (Note 1)	60	60*	A
V_{GSS}	Gate-Source voltage	± 30		V
E_{AS}	Single Pulsed Avalanche Energy (Note 2)	600		mJ
I_{AR}	Avalanche Current (Note 1)	20		A
E_{AR}	Repetitive Avalanche Energy (Note 1)	20.5		mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	4.5		V/ns
P_D	Power Dissipation ($T_C = 25^\circ\text{C}$) - Derate above 25°C	205	35	W
		1.67	0.3	W/ $^\circ\text{C}$
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +150		$^\circ\text{C}$
T_L	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds	300		$^\circ\text{C}$

* Drain current limited by maximum junction temperature.

Thermal Characteristics

Symbol	Parameter	SSP20N60S	SSF20N60S	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	0.6	3.6	$^\circ\text{C/W}$
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink Typ.	0.5	--	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	62	62	$^\circ\text{C/W}$

Electrical Characteristics TC = 25°C unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Off Characteristics						
BVDSS	Drain-Source Breakdown Voltage	VGS = 0V, ID = 250μA, TJ = 25°C	600	--	--	V
		VGS = 0V, ID = 250μA, TJ = 150°C	--	650	--	V
ΔBVDSS / ΔTJ	Breakdown Voltage Temperature Coefficient	ID = 250μA, Referenced to 25°C	--	0.6	--	V/°C
IDSS	Zero Gate Voltage Drain Current	VDS = 600V, VGS = 0V VDS = 480V, TC = 125°C	--	--	1 10	μA μA
IGSSF	Gate-Body Leakage Current, Forward	VGS = 30V, VDS = 0V	--	--	100	nA
IGSSR	Gate-Body Leakage Current, Reverse	VGS = -30V, VDS = 0V	--	--	-100	nA
On Characteristics						
VGS(th)	Gate Threshold Voltage	VDS = VGS, ID = 250μA	2.5	--	4.5	V
RDS(on)	Static Drain-Source On-Resistance	VGS = 10V, ID = 5A	--	0.16	0.19	Ω
gFS	Forward Transconductance	VDS = 40V, ID = 5A (Note 4)	--	16	--	S
Dynamic Characteristics						
Ciss	Input Capacitance	VDS = 25V, VGS = 0V, f = 1.0MHz	--	1440	--	pF
Coss	Output Capacitance		--	300	--	pF
Crss	Reverse Transfer Capacitance		--	10	--	pF
Switching Characteristics						
td(on)	Turn-On Delay Time	VDD = 400V, ID = 5A RG = 20Ω(Note 4, 5)	--	25	--	ns
tr	Turn-On Rise Time		--	55	--	ns
td(off)	Turn-Off Delay Time		--	70	--	ns
tf	Turn-Off Fall Time		--	40	--	ns
Qg	Total Gate Charge	VDS = 480V, ID = 10A VGS = 10V (Note 4, 5)	--	70	90	nC
Qgs	Gate-Source Charge		--	7.8	--	nC
Qgd	Gate-Drain Charge		--	9	--	nC
Drain-Source Diode Characteristics and Maximum Ratings						
IS	Maximum Continuous Drain-Source Diode Forward Current		--	--	20	A
ISM	Maximum Pulsed Drain-Source Diode Forward Current		--	--	60	A
VSD	Drain-Source Diode Forward Voltage	VGS = 0V, IF = 10A	--	1	1.5	V
trr	Reverse Recovery Time	VR = 400V, IF = 10A di _r /dt =100A/μs (Note 4)	--	475	--	ns
Qrr	Reverse Recovery Charge		--	5.8	--	μC
Irrm	Peak reverse recovery Current		--	35	--	A

NOTES:

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. L=10.5mH, I_{AS}=10A, VDD=150V, Starting TJ=25 °C
3. I_{SD}≤ID, di/dt ≤ 200A/μs, V_{DD} ≤ BV_{DSS}, Starting TJ = 25 °C
4. Pulse Test: Pulse width ≤ 300μs, Duty Cycle ≤ 2%
5. Essentially Independent of Operating Temperature Typical Characteristics

Typical Performance Characteristics

Figure 1. On-Region Characteristics

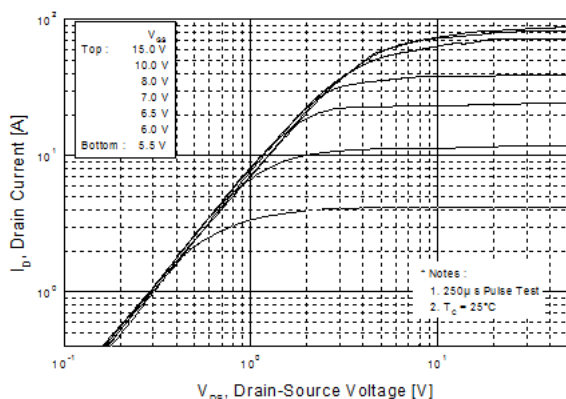


Figure 2. Transfer Characteristics

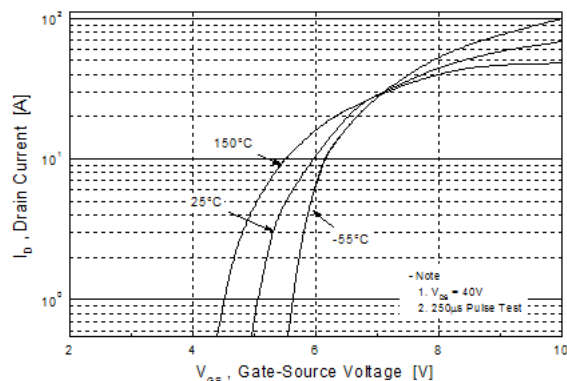


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

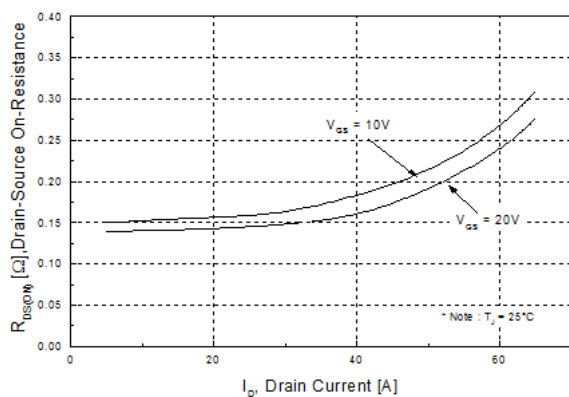


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

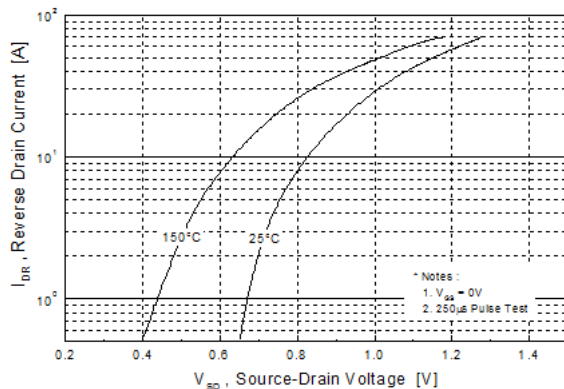


Figure 5. Capacitance Characteristics

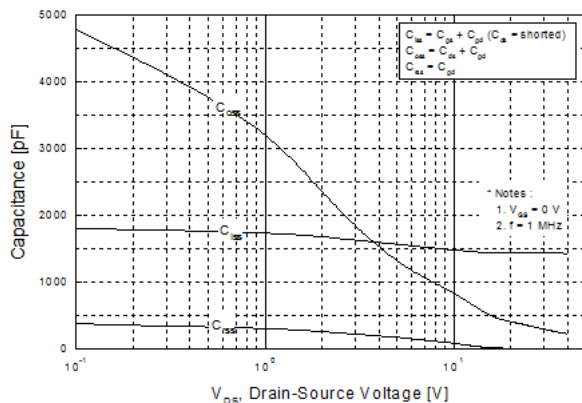
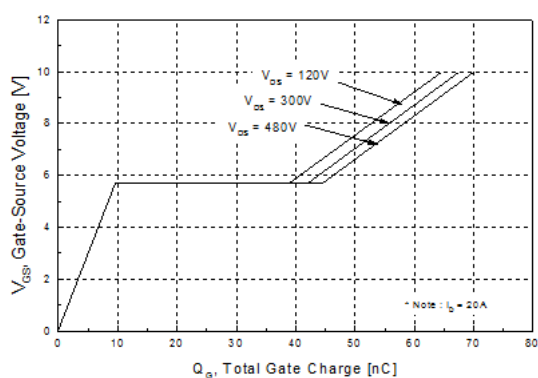


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

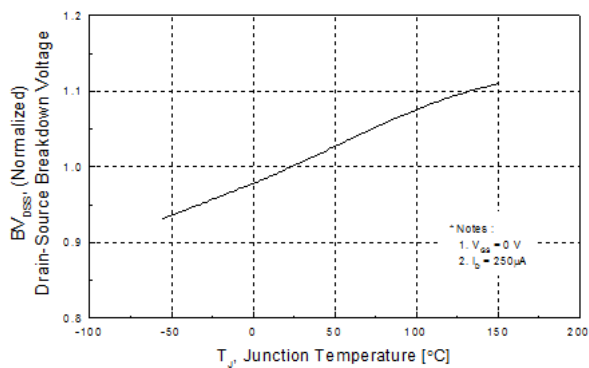


Figure 8. On-Resistance Variation vs. Temperature

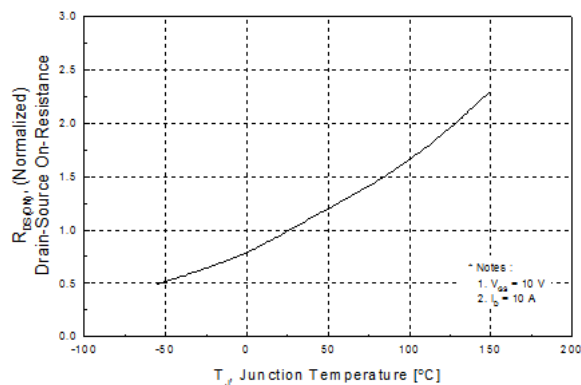


Figure 9-1. Safe Operating Area of SSP20N60S

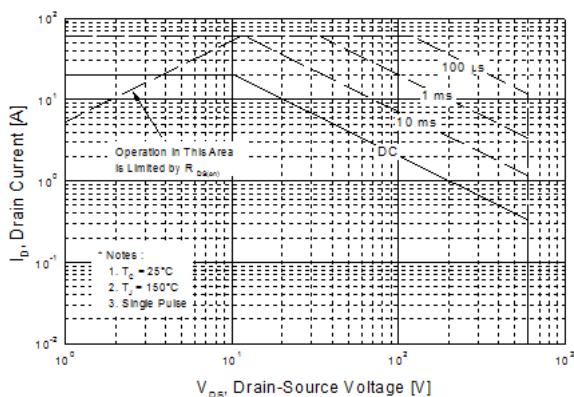


Figure 9-2. Safe Operating Area of SSF20N60S

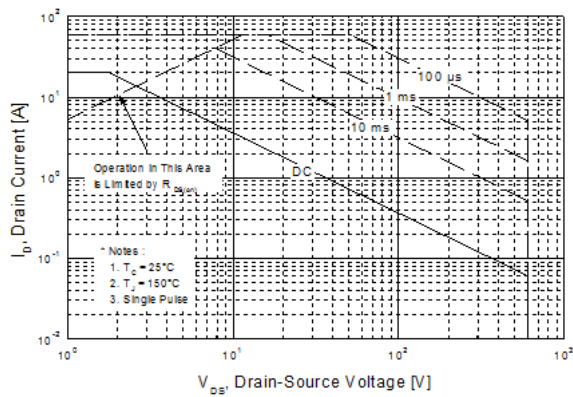
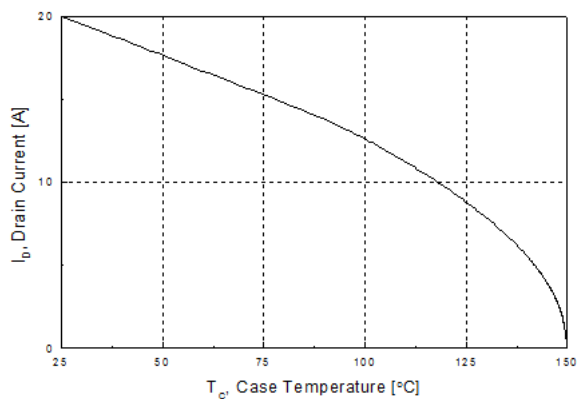


Figure 10. Maximum Drain Current vs. Case Temperature



Typical Performance Characteristics (Continued)

Figure 10-1. Transient Thermal Response Curve of SSP20N60S

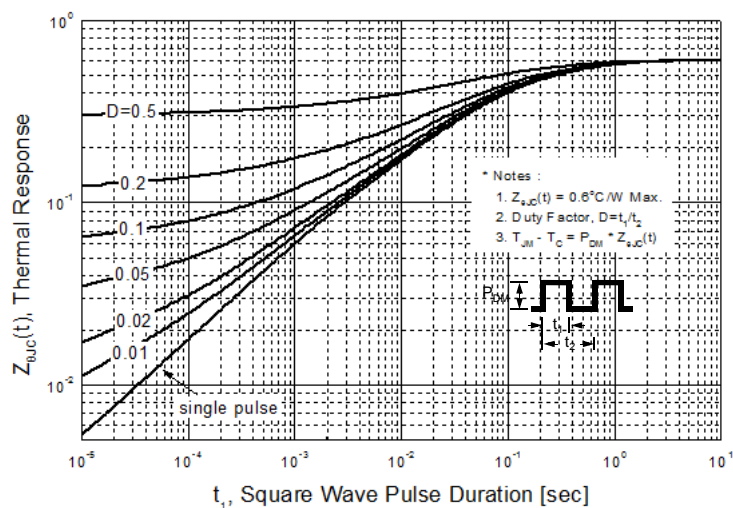
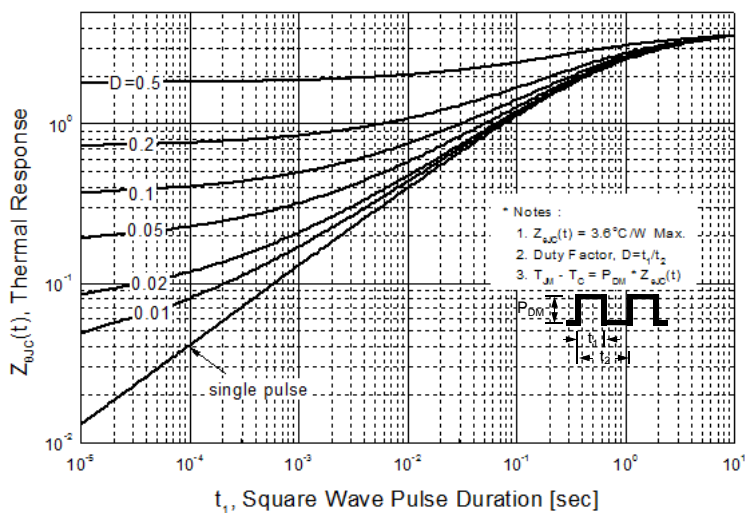
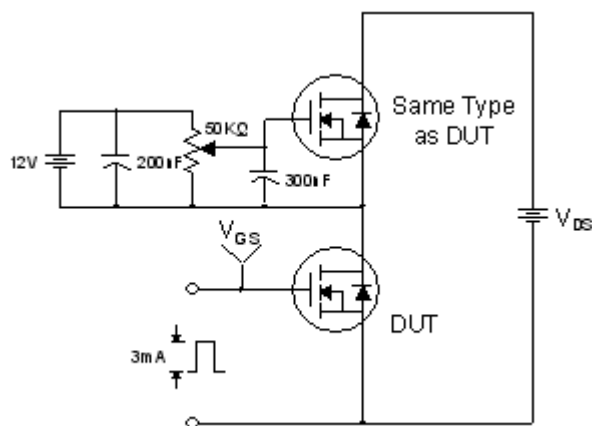


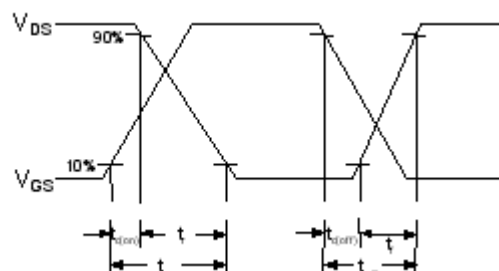
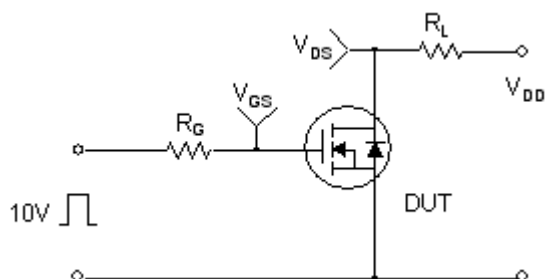
Figure 10-2. Transient Thermal Response Curve of SSF20N60S



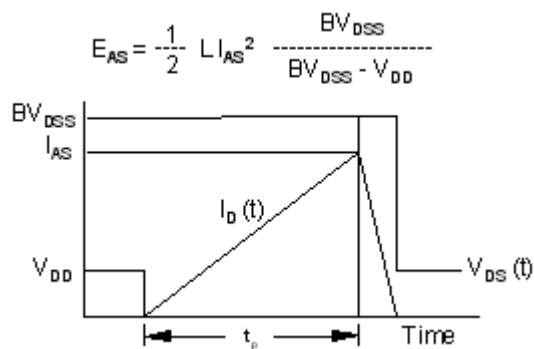
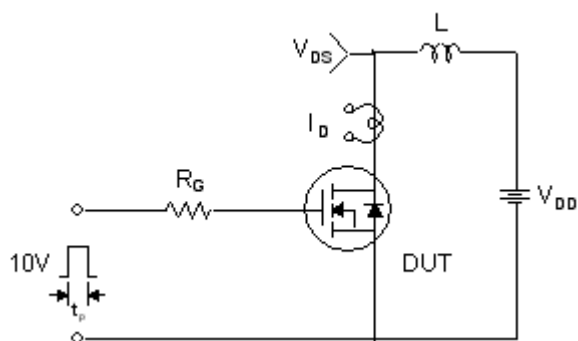
Gate Charge Test Circuit & Waveform

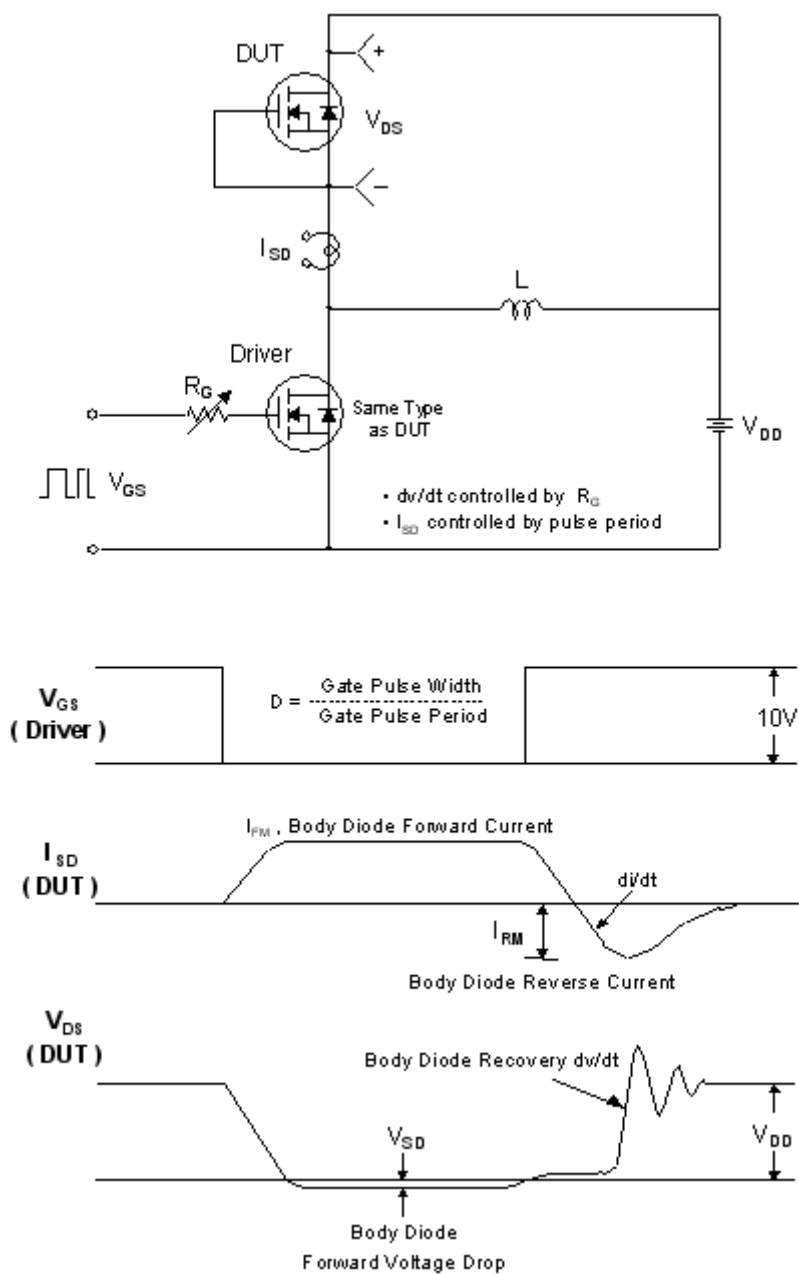


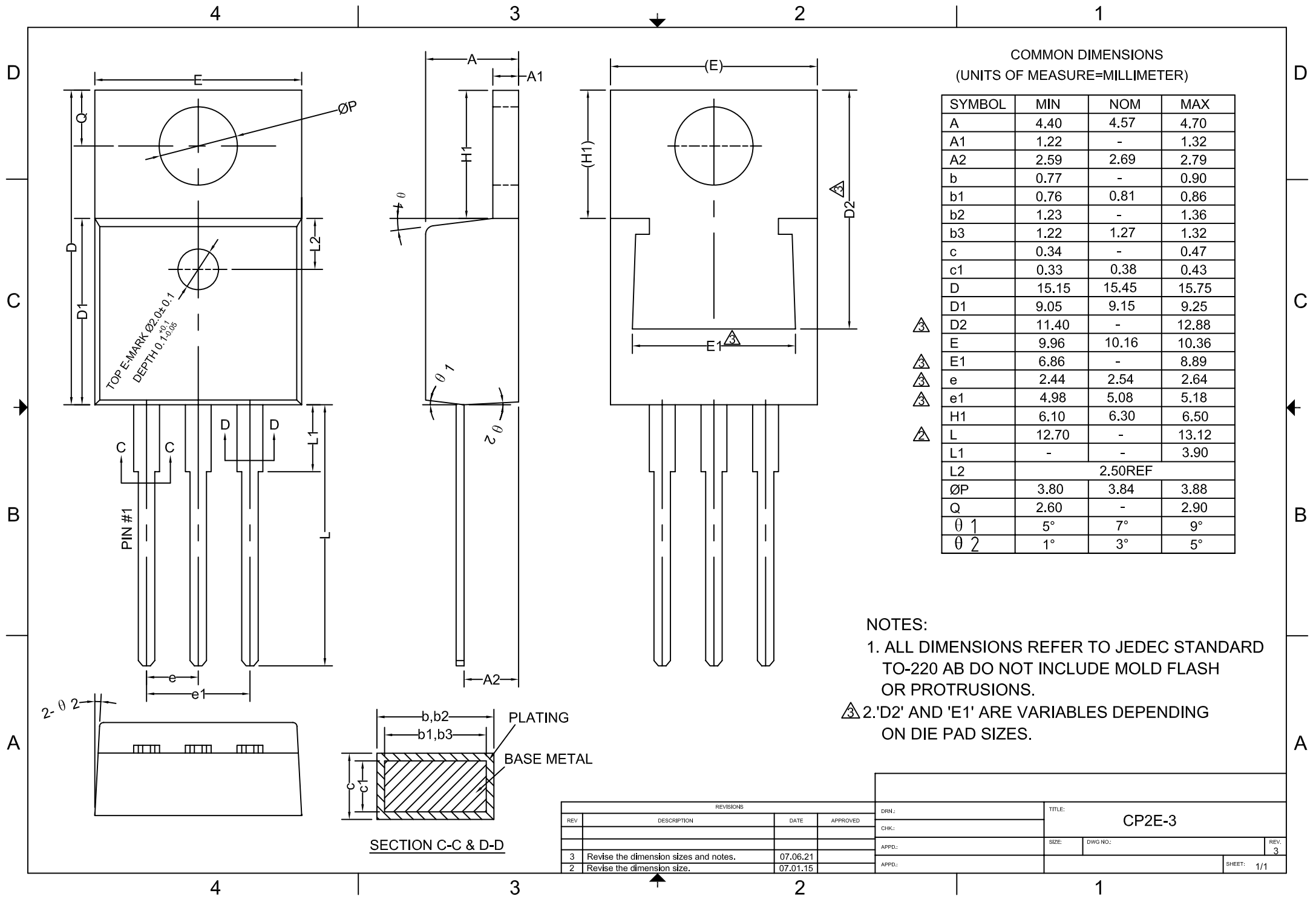
Resistive Switching Test Circuit & Waveforms

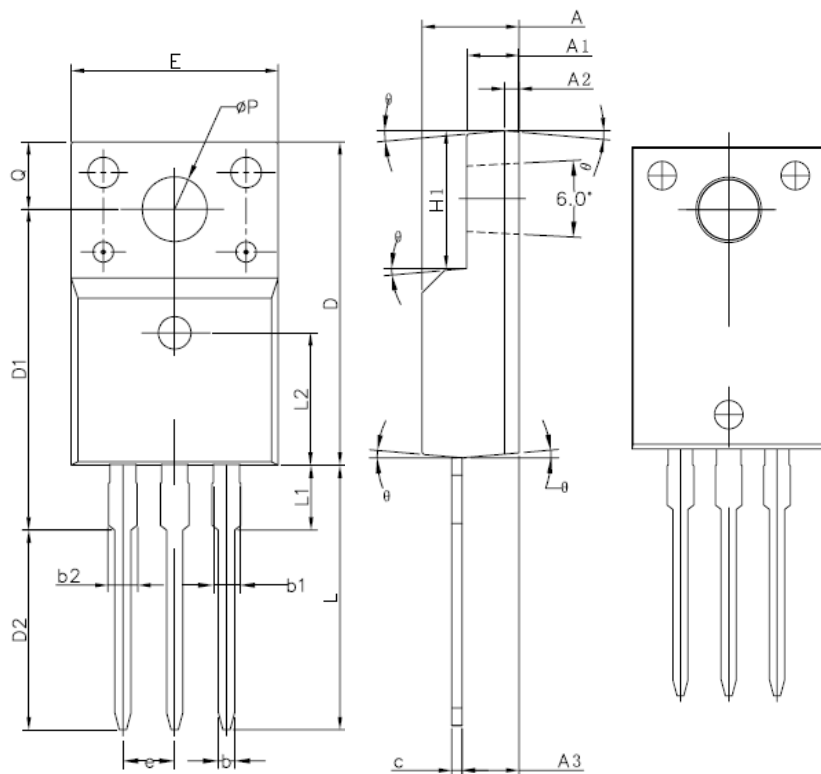


Unclamped Inductive Switching Test Circuit & Waveforms



Peak Diode Recovery dv/dt Test Circuit & Waveforms





COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX
A	4.50	4.70	4.90
A1	2.34	2.54	2.74
A2	0.70 REF		
A3	2.56	2.76	2.96
b	0.70	—	0.90
b1	1.18	—	1.38
b2	—	—	1.47
c	0.45	0.50	0.60
D	15.67	15.87	16.07
D1	15.55	15.75	15.95
D2	9.60	9.80	10.0
E	9.96	10.16	10.36
e	2.54BSC		
H1	6.48	6.68	6.88
L	12.68	12.98	3.50
L1	—	—	
L2	6.50REF		
øP	3.08	3.18	3.28
Q	3.20	—	3.40
θ	3°	5°	7°

