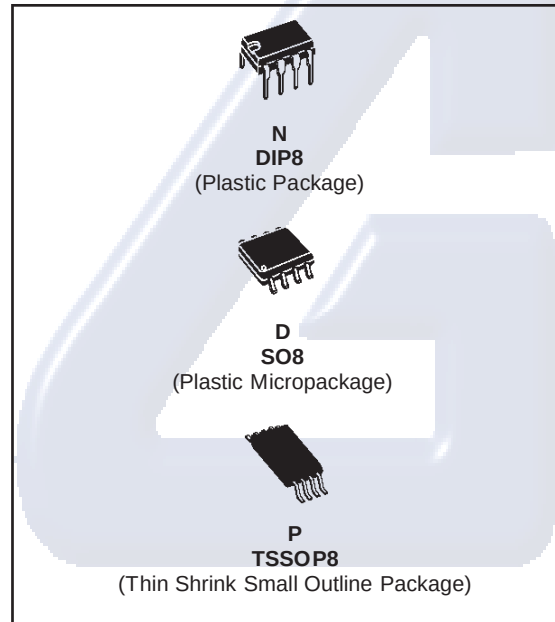




TL082 TL082A - TL082B

GENERAL PURPOSE J-FET DUAL OPERATIONAL AMPLIFIERS

- WIDE COMMON-MODE (UP TO V_{CC}^+) AND DIFFERENTIAL VOLTAGE RANGE
- LOW INPUT BIAS AND OFFSET CURRENT
- OUTPUT SHORT-CIRCUIT PROTECTION
- HIGH INPUT IMPEDANCE J-FET INPUT STAGE
- INTERNAL FREQUENCY COMPENSATION
- LATCH UP FREE OPERATION
- HIGH SLEW RATE : $16V/\mu s$ (typ)



DESCRIPTION

The TL082, TL082A and TL082B are high speed J-FET input dual operational amplifiers incorporating well matched, high voltage J-FET and bipolar transistors in a monolithic integrated circuit.

The devices feature high slew rates, low input bias and offset current, and low offset voltage temperature coefficient.

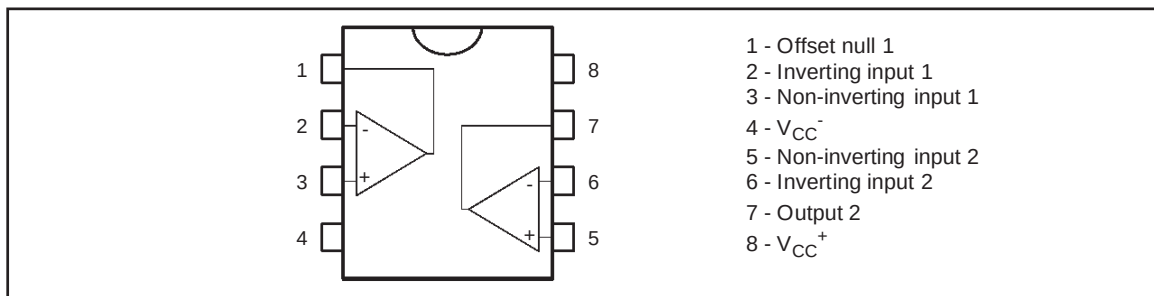
ORDER CODE

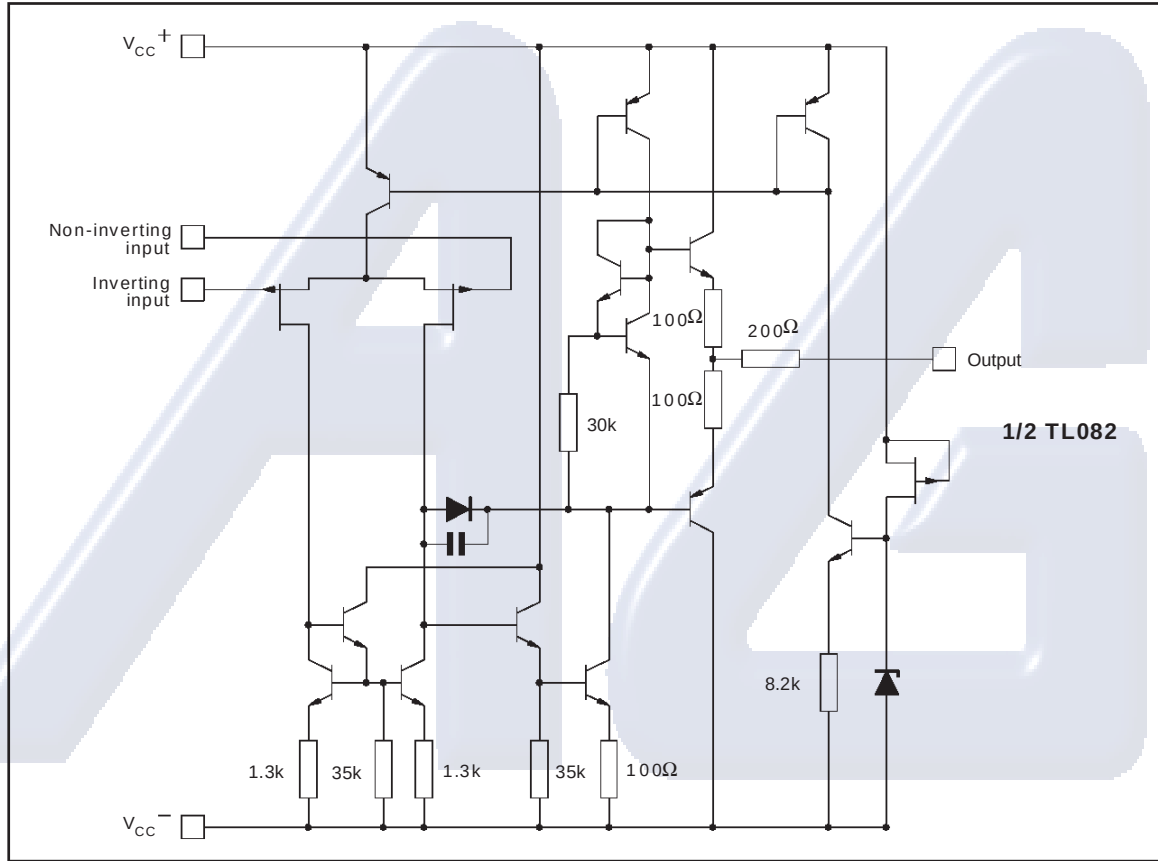
Part Number	Temperature Range	Package		
		N	D	P
TL082M/AM/BM	-55°C, +125°C	•	•	•
TL082I/AI/BI	-40°C, +105°C	•	•	•
TL082C/AC/BC	0°C, +70°C	•	•	•

Example : TL082CD, TL082IN

N = Dual in Line Package (DIP)
D = Small Outline Package (SO) - also available in Tape & Reel (DT)
P = Thin Shrink Small Outline Package (TSSOP) - only available in Tape & Reel (PT)

PIN CONNECTIONS (top view)



TL082 - TL082A - TL082B**SCHEMATIC DIAGRAM****ABSOLUTE MAXIMUM RATINGS**

Symbol	Parameter	TL082M, AM, BM	TL082I, AI, BI	TL082C, AC, BC	Unit
V_{CC}	Supply voltage - note ¹⁾	± 18			V
V_i	Input Voltage - note ²⁾	± 15			V
V_{id}	Differential Input Voltage - note ³⁾	± 30			V
P_{tot}	Power Dissipation	680			mW
	Output Short-circuit Duration - note ⁴⁾	Infinite			
T_{oper}	Operating Free-air Temperature Range	-55 to +125	-40 to +105	0 to +70	°C
T_{stg}	Storage Temperature Range	-65 to +150			°C

1. All voltage values, except differential voltage, are with respect to the zero reference level (ground) of the supply voltages where the zero reference level is the midpoint between V_{CC}^+ and V_{CC}^- .
2. The magnitude of the input voltage must never exceed the magnitude of the supply voltage or 15 volts, whichever is less.
3. Differential voltages are the non-inverting input terminal with respect to the inverting input terminal.
4. The output may be shorted to ground or to either supply. Temperature and/or supply voltages must be limited to ensure that the dissipation rating is not exceeded.

TL082 - TL082A - TL082B

ELECTRICAL CHARACTERISTICS

 $V_{CC} = \pm 15V$, $T_{amb} = +25^{\circ}C$ (unless otherwise specified)

Symbol	Parameter	TL082I,M,AC,AI,AM,BC,BI,BM			TL082C			Unit
		Min.	Typ.	Max.	Min.	Typ.	Max.	
V _{io}	Input Offset Voltage (R _S = 50Ω) T _{amb} = +25°C T _{min} ≤ T _{amb} ≤ T _{max} TL082 TL082A TL082B TL082 TL082A TL082B		3 3 1	10 6 3 13 7 5		3 13	10	mV
DV _{io}	Input Offset Voltage Drift		10			10		μV/°C
I _{io}	Input Offset Current - note 1) T _{amb} = +25°C T _{min} ≤ T _{amb} ≤ T _{max}		5	100 4		5 100 10		pA nA
I _{ib}	Input Bias Current -note 1 T _{amb} = +25°C T _{min} ≤ T _{amb} ≤ T _{max}		20	200 20		20 400 20		pA nA
A _{vd}	Large Signal Voltage Gain (R _L = 2kΩ, V _O = ±10V) T _{amb} = +25°C T _{min} ≤ T _{amb} ≤ T _{max}	50 25	200		25 15	200		V/mV
SVR	Supply Voltage Rejection Ratio (R _S = 50Ω) T _{amb} = +25°C T _{min} ≤ T _{amb} ≤ T _{max}	80 80	86		70 70	86		dB
I _{CC}	Supply Current, no load T _{amb} = +25°C T _{min} ≤ T _{amb} ≤ T _{max}		1.4	2.5 2.5		1.4 2.5 2.5		mA
V _{icm}	Input Common Mode Voltage Range	±11	+15 -12		±11	+15 -12		V
CMR	Common Mode Rejection Ratio (R _S = 50Ω) T _{amb} = +25°C T _{min} ≤ T _{amb} ≤ T _{max}	80 80	86		70 70	86		dB
I _{os}	Output Short-circuit Current T _{amb} = +25°C T _{min} ≤ T _{amb} ≤ T _{max}	10 10	40	60 60	10 10	40 60 60		mA
±V _{opp}	Output Voltage Swing T _{amb} = +25°C T _{min} ≤ T _{amb} ≤ T _{max} RL = 2kΩ RL = 10kΩ RL = 2kΩ RL = 10kΩ	10 12 10 12	12 13.5		10 12 10 12	12 13.5		V
SR	Slew Rate (T _{amb} = +25°C) V _{in} = 10V, R _L = 2kΩ, C _L = 100pF, unity gain	8	16		8	16		V/μs
t _r	Rise Time (T _{amb} = +25°C) V _{in} = 20mV, R _L = 2kΩ, C _L = 100pF, unity gain		0.1			0.1		μs
K _{ov}	Overshoot (T _{amb} = +25°C) V _{in} = 20mV, R _L = 2kΩ, C _L = 100pF, unity gain		10			10		%
GBP	Gain Bandwidth Product (T _{amb} = +25°C) V _{in} = 10mV, R _L = 2kΩ, C _L = 100pF, f = 100kHz	2.5	4		2.5	4		MHz
R _i	Input Resistance		10 ¹²			10 ¹²		Ω

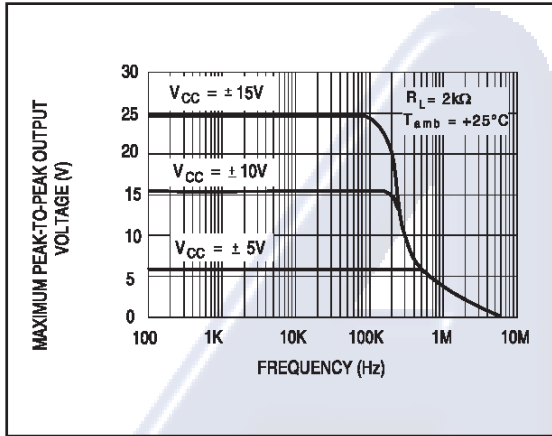
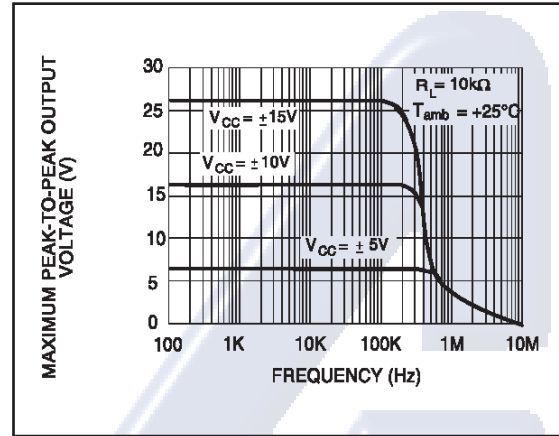
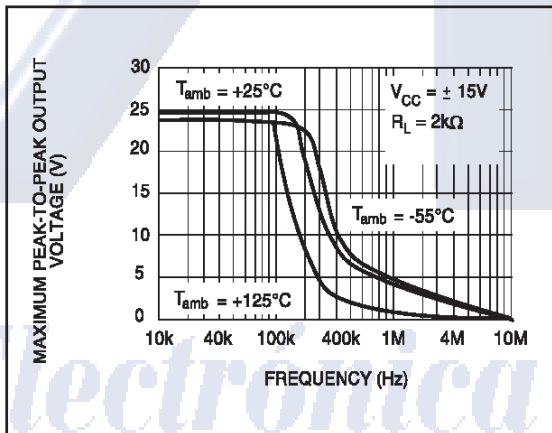
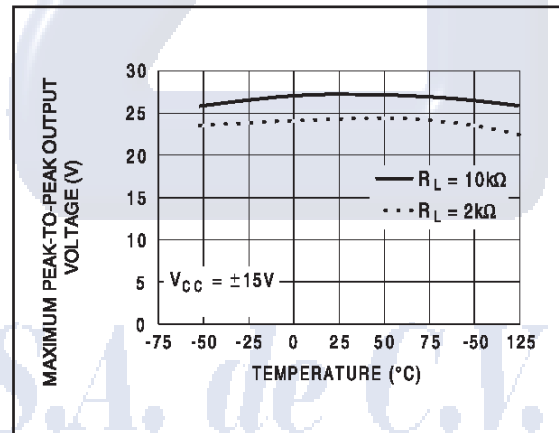
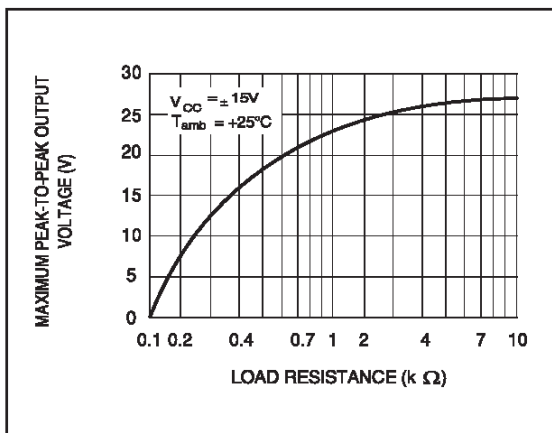
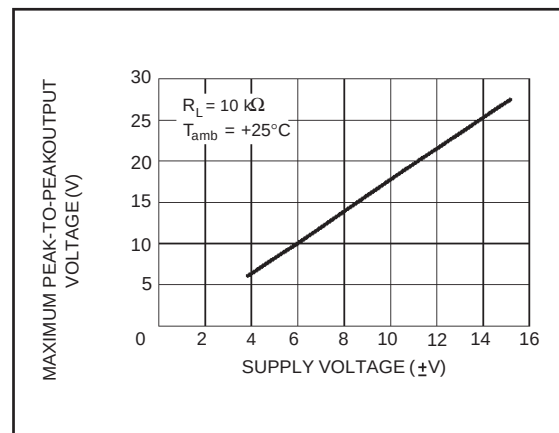
TL082 - TL082A - TL082B

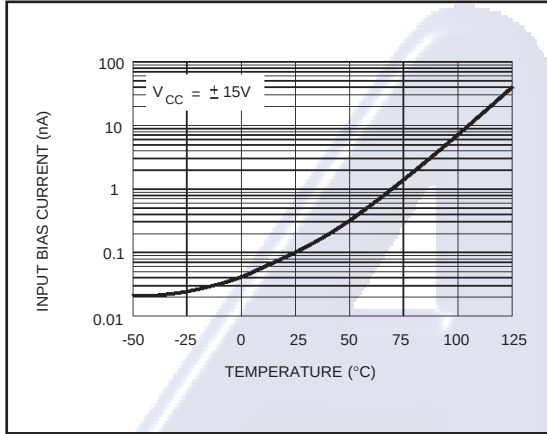
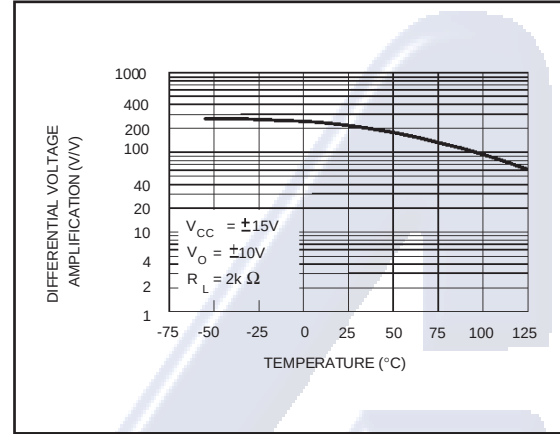
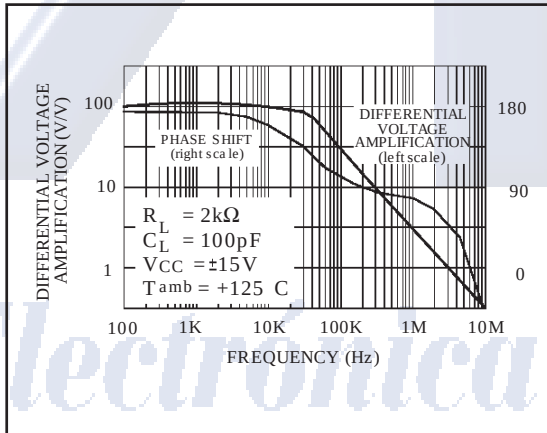
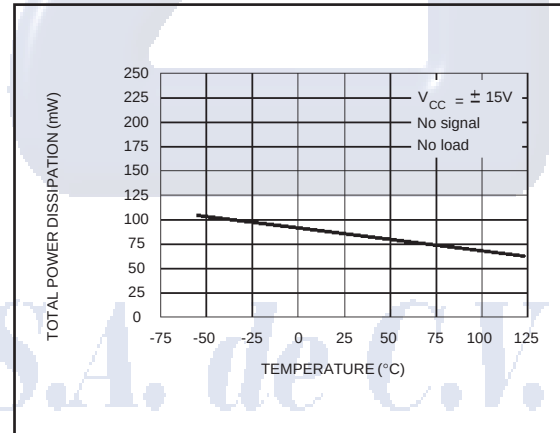
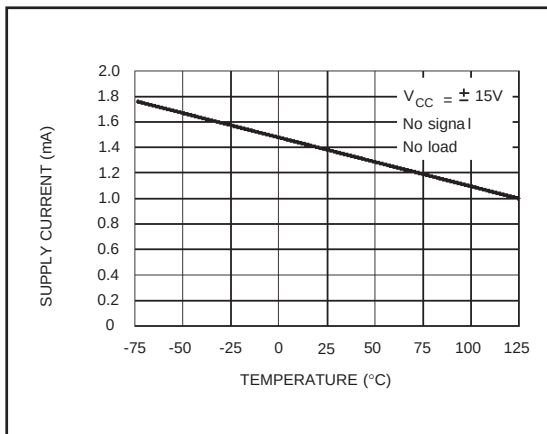
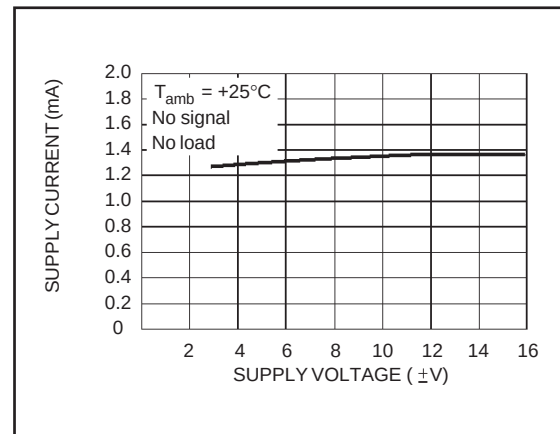
Symbol	Parameter	TL082I,M,AC,AI,AM, BC,BI,BM			TL082C			Unit
		Min.	Typ.	Max.	Min.	Typ.	Max.	
THD	Total Harmonic Distortion ($T_{amb} = +25^{\circ}\text{C}$), $f = 1\text{kHz}$, $R_L = 2\text{k}\Omega$, $C_L = 100\text{pF}$, $A_v = 20\text{dB}$, $V_o = 2V_{pp}$		0.01			0.01		%
e_n	Equivalent Input Noise Voltage $R_S = 100\Omega$, $f = 1\text{kHz}$		15			15		$\frac{\text{nV}}{\sqrt{\text{Hz}}}$
ϕ_m	Phase Margin		45			45		degrees
V_{o1}/V_{o2}	Channel Separation $A_v = 100$		120			120		dB

1. The input bias currents are junction leakage currents which approximately double for every 10°C increase in the junction temperature.

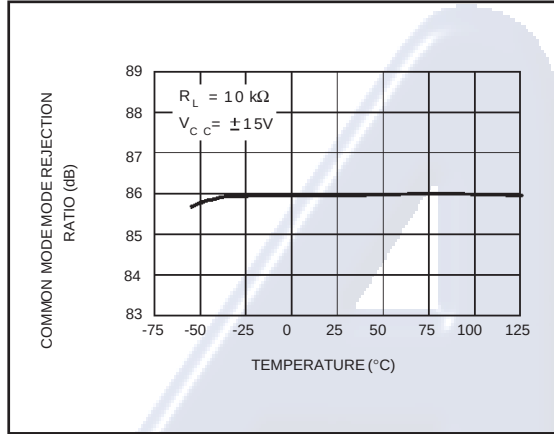
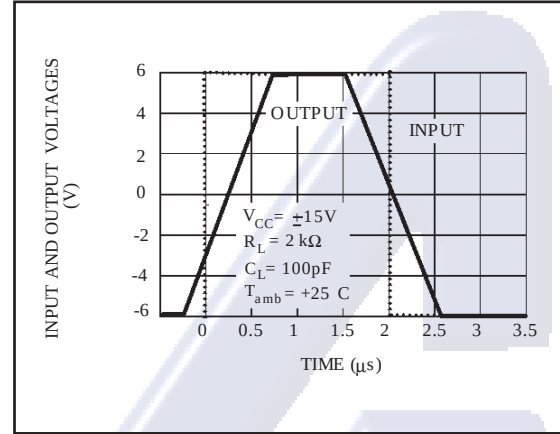
Electrónica S.A. de C.V.

TL082 - TL082A - TL082B

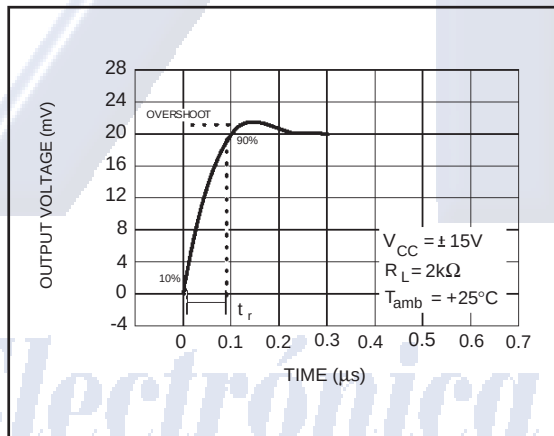
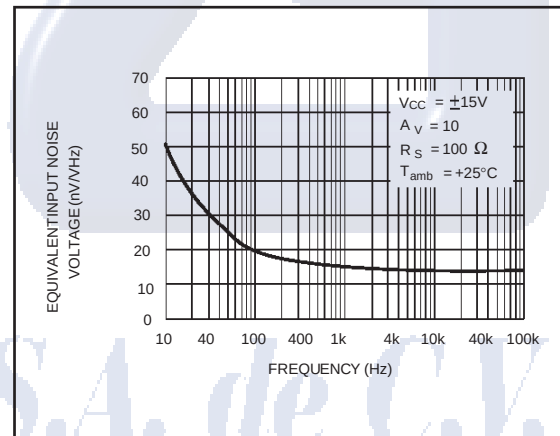
MAXIMUM PEAK-TO-PEAK OUTPUT
VOLTAGE versus FREQUENCYMAXIMUM PEAK-TO-PEAK OUTPUT
VOLTAGE versus FREQUENCYMAXIMUM PEAK-TO-PEAK OUTPUT
VOLTAGE versus FREQUENCYMAXIMUM PEAK-TO-PEAK OUTPUT
VOLTAGE versus FREE AIR TEMP.MAXIMUM PEAK-TO-PEAK OUTPUT
VOLTAGE versus LOAD RESISTANCEMAXIMUM PEAK-TO-PEAK OUTPUT
VOLTAGE versus SUPPLY VOLTAGE

TL082 - TL082A - TL082B**INPUT BIAS CURRENT versus FREE AIR TEMPERATURE****LARGE SIGNAL DIFFERENTIAL VOLTAGE AMPLIFICATION versus FREE AIR TEMP.****LARGE SIGNAL DIFFERENTIAL VOLTAGE AMPLIFICATION AND PHASE SHIFT versus FREQUENCY****TOTAL POWER DISSIPATION versus FREE AIR TEMPERATURE****SUPPLY CURRENT PER AMPLIFIER versus FREE AIR TEMPERATURE****SUPPLY CURRENT PER AMPLIFIER versus SUPPLY VOLTAGE**

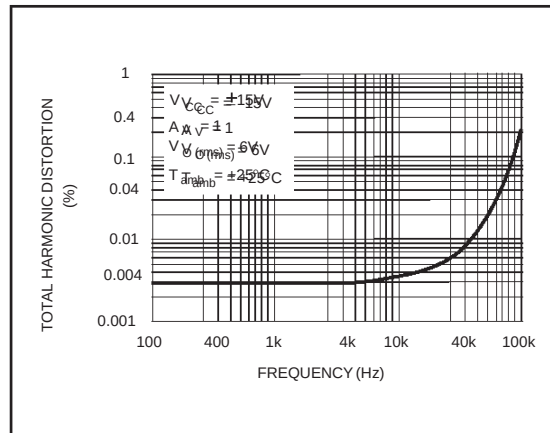
TL082 - TL082A - TL082B

COMMON MODE REJECTION RATIO versus
FREE AIR TEMPERATUREVOLTAGE FOLLOWER LARGE SIGNAL PULSE
RESPONSE

OUTPUT VOLTAGE versus ELAPSED TIME

EQUIVALENT INPUT NOISE VOLTAGE versus
FREQUENCY

TOTAL HARMONIC DISTORTION versus FREQUENCY



TL082 - TL082A - TL082B

PARAMETER MEASUREMENT INFORMATION

Figure 1 : Voltage Follower

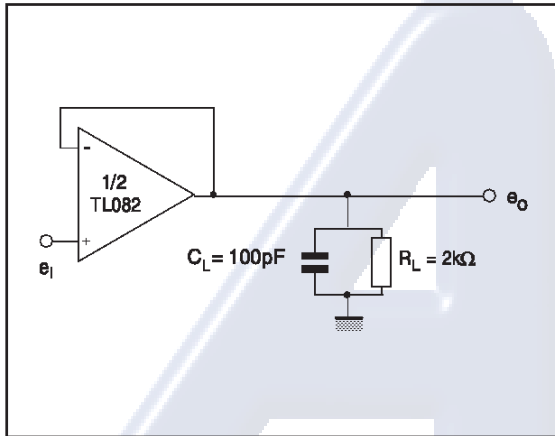
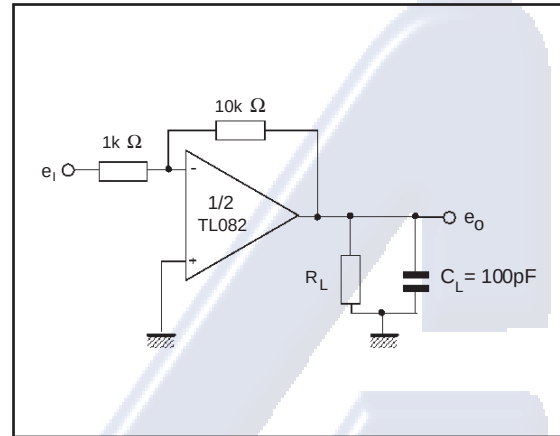
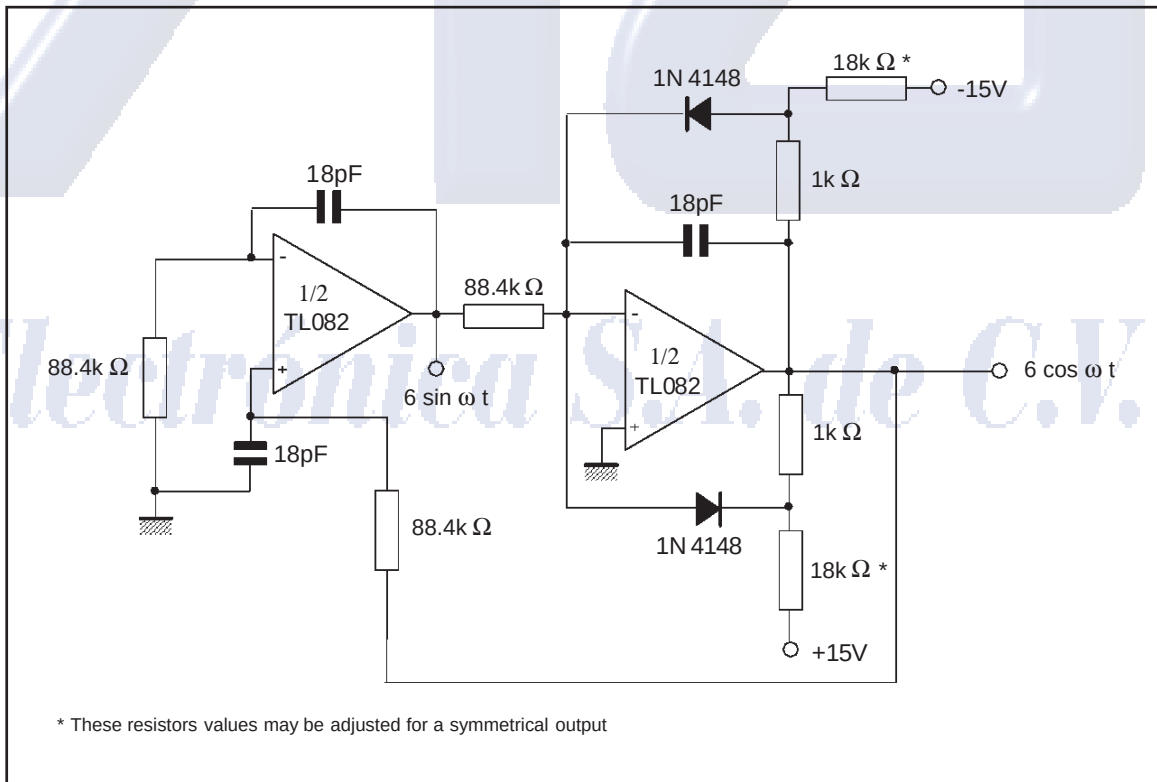


Figure 2 : Gain-of-10 Inverting Amplifier

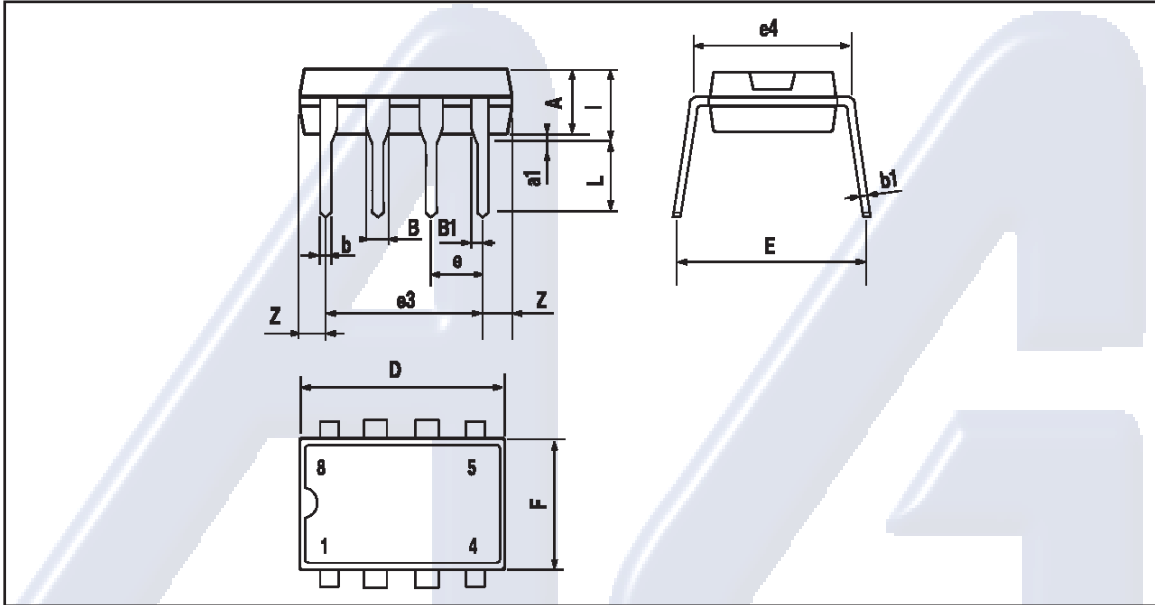


TYPICAL APPLICATIONS

100KHz QUADRUPLE OSCILLATOR



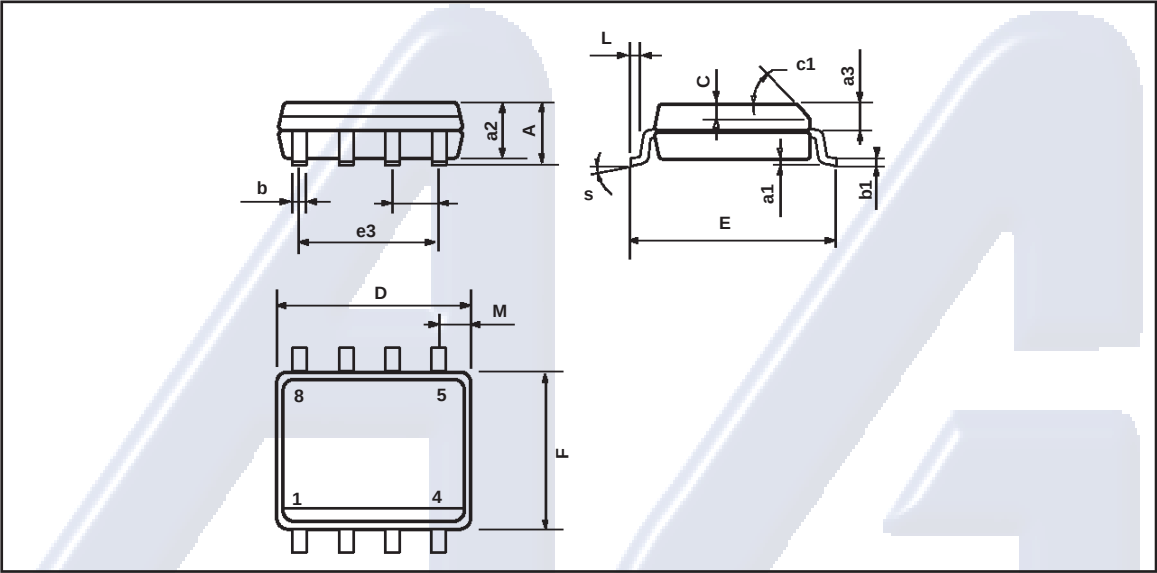
TL082 - TL082A - TL082B

PACKAGE MECHANICAL DATA
 8 PINS - PLASTIC DIP


Dim.	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A		3.32			0.131	
a1	0.51			0.020		
B	1.15		1.65	0.045		0.065
b	0.356		0.55	0.014		0.022
b1	0.204		0.304	0.008		0.012
D			10.92			0.430
E	7.95		9.75	0.313		0.384
e		2.54			0.100	
e3		7.62			0.300	
e4		7.62			0.300	
F			6.6			0.260
i			5.08			0.200
L	3.18		3.81	0.125		0.150
Z			1.52			0.060

TL082 - TL082A - TL082B

PACKAGE MECHANICAL DATA
8 PINS - PLASTIC MICROPACKAGE (SO)

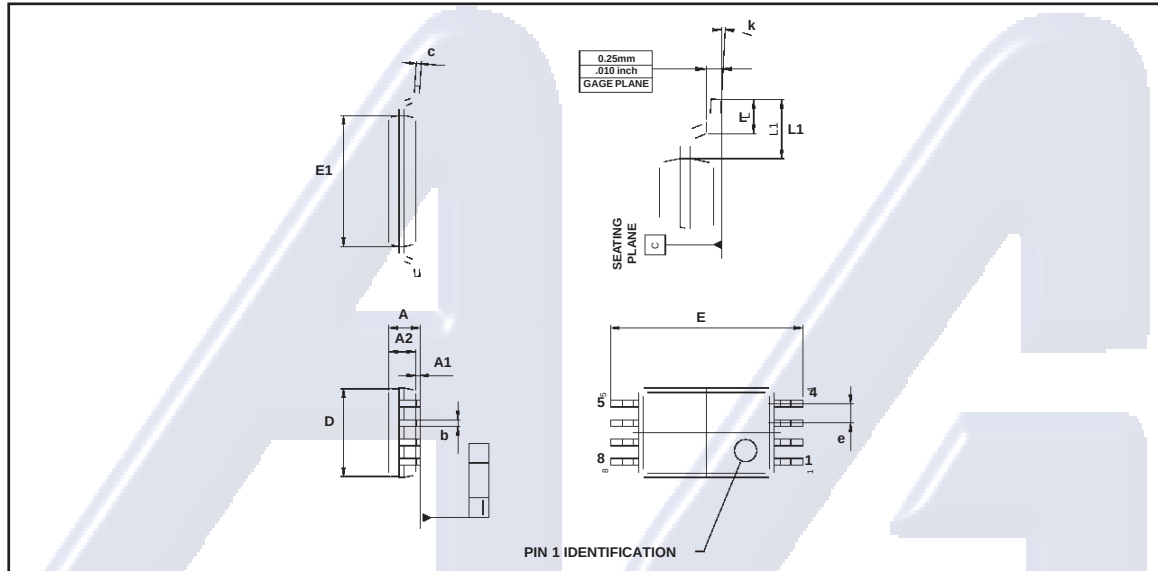


Dim.	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A			1.75			0.069
a1	0.1		0.25	0.004		0.010
a2			1.65			0.065
a3	0.65		0.85	0.026		0.033
b	0.35		0.48	0.014		0.019
b1	0.19		0.25	0.007		0.010
C	0.25		0.5	0.010		0.020
c1	45° (typ.)					
D	4.8		5.0	0.189		0.197
E	5.8		6.2	0.228		0.244
e		1.27			0.050	
e3		3.81			0.150	
F	3.8		4.0	0.150		0.157
L	0.4		1.27	0.016		0.050
M			0.6			0.024
S	8° (max.)					

TL082 - TL082A - TL082B

PACKAGE MECHANICAL DATA

8 PINS - THIN SHRINK SMALL OUTLINE PACKAGE



Dim.	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A			1.20			0.05
A1	0.05		0.15	0.01		0.006
A2	0.80	1.00	1.05	0.031	0.039	0.041
b	0.19		0.30	0.007		0.15
c	0.09		0.20	0.003		0.012
D	2.90	3.00	3.10	0.114	0.118	0.122
E		6.40			0.252	
E1	4.30	4.40	4.50	0.169	0.173	0.177
e		0.65			0.025	
k	0°		8°	0°		8°
l	0.50	0.60	0.75	0.09	0.0236	0.030
L	0.45	0.600	0.75	0.018	0.024	0.030
L1		1.000			0.039	

Information furnished is believed to be accurate and reliable. However, STMicroelectronics assumes no responsibility for the consequences of use of such information nor for any infringement of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of STMicroelectronics. Specifications mentioned in this publication are subject to change without notice. This publication supersedes and replaces all information previously supplied. STMicroelectronics products are not authorized for use as critical components in life support devices or systems without express written approval of STMicroelectronics.

© The ST logo is a registered trademark of STMicroelectronics

© 2001 STMicroelectronics - Printed in Italy - All Rights Reserved
STMicroelectronics GROUP OF COMPANIES

Australia - Brazil - Canada - China - Finland - France - Germany - Hong Kong - India - Israel - Italy - Japan - Malaysia
Malta - Morocco - Singapore - Spain - Sweden - Switzerland - United Kingdom - United States
© <http://www.st.com>

