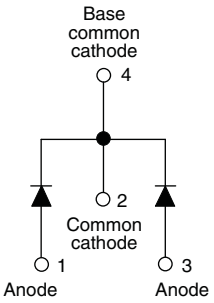
Schottky Rectifier, 2 x 6 A

D-PAK (TO-252AA)



FEATURES

- Low forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability
- Popular D-PAK outline
- Center tap configuration
- Small foot print, surface mountable
- High frequency operation
- AEC-Q101 qualified
- Meets JESD 201 class 2 whisker test
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

RoHS COMPLIANT
HALOGEN FREE

DESCRIPTION

The VS-12CWQ10FNHM3 surface mount, center tap, Schottky rectifier series has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

PRODUCT SUMMARY	
Package	D-PAK (TO-252AA)
$I_{F(AV)}$	2 x 6 A
V_R	100 V
V_F at I_F	0.65 V
I_{RM}	4 mA at 125 °C
T_J max.	150 °C
Diode variation	Common cathode
E_{AS}	6 mJ

MAJOR RATINGS AND CHARACTERISTICS			
SYMBOL	CHARACTERISTICS	VALUES	UNITS
$I_{F(AV)}$	Rectangular waveform	12	A
V_{RRM}		100	V
I_{FSM}	t_p = 5 µs sine	330	A
V_F	6 A_{pk}, T_J = 125 °C (per leg)	0.65	V
T_J	Range	- 55 to 150	°C

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-12CWQ10FNHM3	UNITS
Maximum DC reverse voltage	V_R	100	V
Maximum working peak reverse voltage	V_{RWM}		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum average forward current See fig. 5	per leg	$I_{F(AV)}$	50 % duty cycle at T_C = 135 °C, rectangular waveform	6	A
	per device			12	
Maximum peak one cycle non-repetitive surge current per leg See fig. 7		I_{FSM}	5 µs sine or 3 µs rect. pulse; Following any rated load condition and with rated V_{RRM} applied	330	A
			10 ms sine or 6 ms rect. pulse; Following any rated load condition and with rated V_{RRM} applied	110	
Non-repetitive avalanche energy per leg		E_{AS}	T_J = 25 °C, I_{AS} = 1 A, L = 12 mH	6	mJ
Repetitive avalanche current per leg		I_{AR}	Current decaying linearly to zero in 1 µs Frequency limited by T_J maximum V_A = 1.5 x V_R typical	1	A

ELECTRICAL SPECIFICATIONS

PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop per leg See fig. 1	V_{FM} (1)	6 A	T_J = 25 °C	0.80	V
		12 A		0.95	
		6 A	T_J = 125 °C	0.65	
		12 A		0.78	
Maximum reverse leakage current per leg See fig. 2	I_{RM} (1)	T_J = 25 °C	V_R = Rated V_R	1	mA
		T_J = 125 °C		4	
Threshold voltage	$V_{F(TO)}$	T_J = T_J maximum		0.47	V
Forward slope resistance	r_t			20.68	mΩ
Typical junction capacitance per leg	C_T	V_R = 5 V_{DC}, (test signal range 100 kHz to 1 MHz), 25 °C		183	pF
Typical series inductance per leg	L_S	Measured lead to lead 5 mm from package body		5.0	nH

Note

(1) Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS

PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T_J (1), T_{Stg}		- 55 to 150	°C
Maximum thermal resistance, junction to case	per leg	R_{thJC}	DC operation See fig. 4	3.0	°C/W
	per device			1.5	
Approximate weight				0.3	g
				0.01	oz.
Marking device			Case style D-PAK	12CWQ10FNH	

Note

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

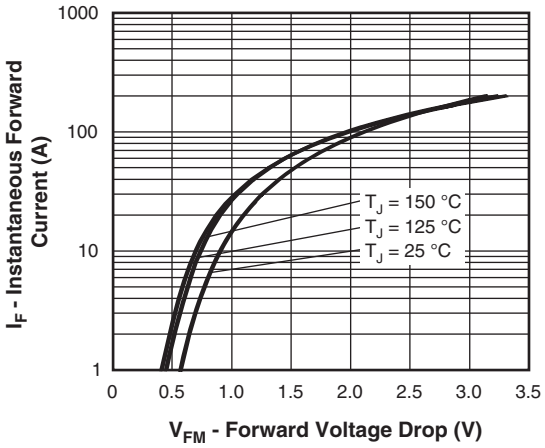


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

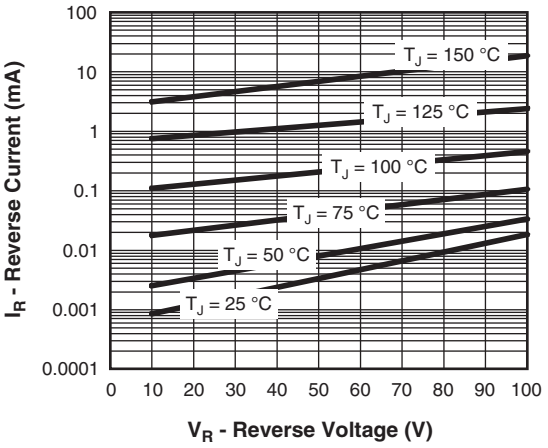


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

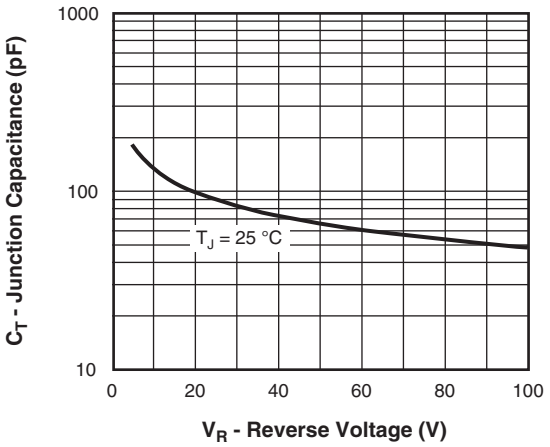


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

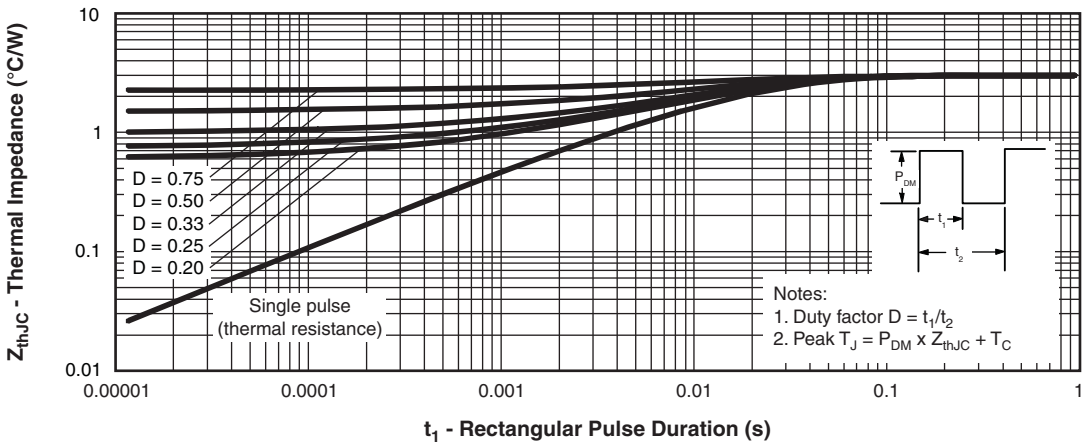


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

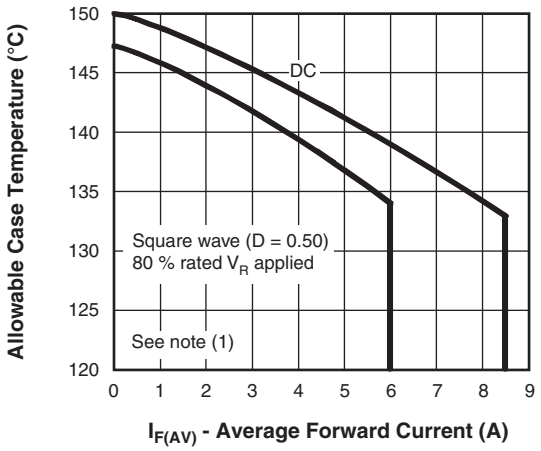


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

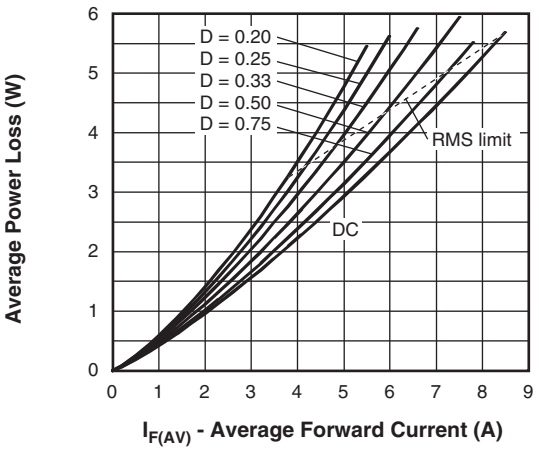


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

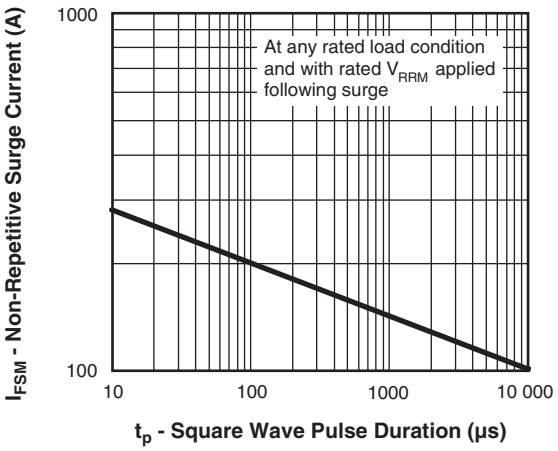


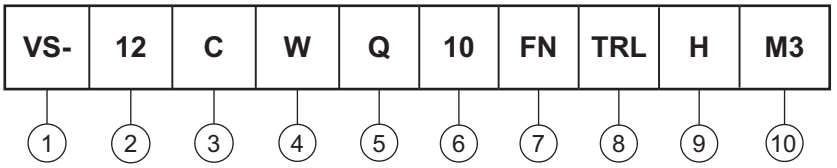
Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

Note

(1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;
Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at ($I_{F(AV)}$/D) (see fig. 6);
Pd_{REV} = Inverse power loss = $V_{R1} \times I_R$ (1 - D); I_R at V_{R1} = 80 % rated V_R

ORDERING INFORMATION TABLE

Device code



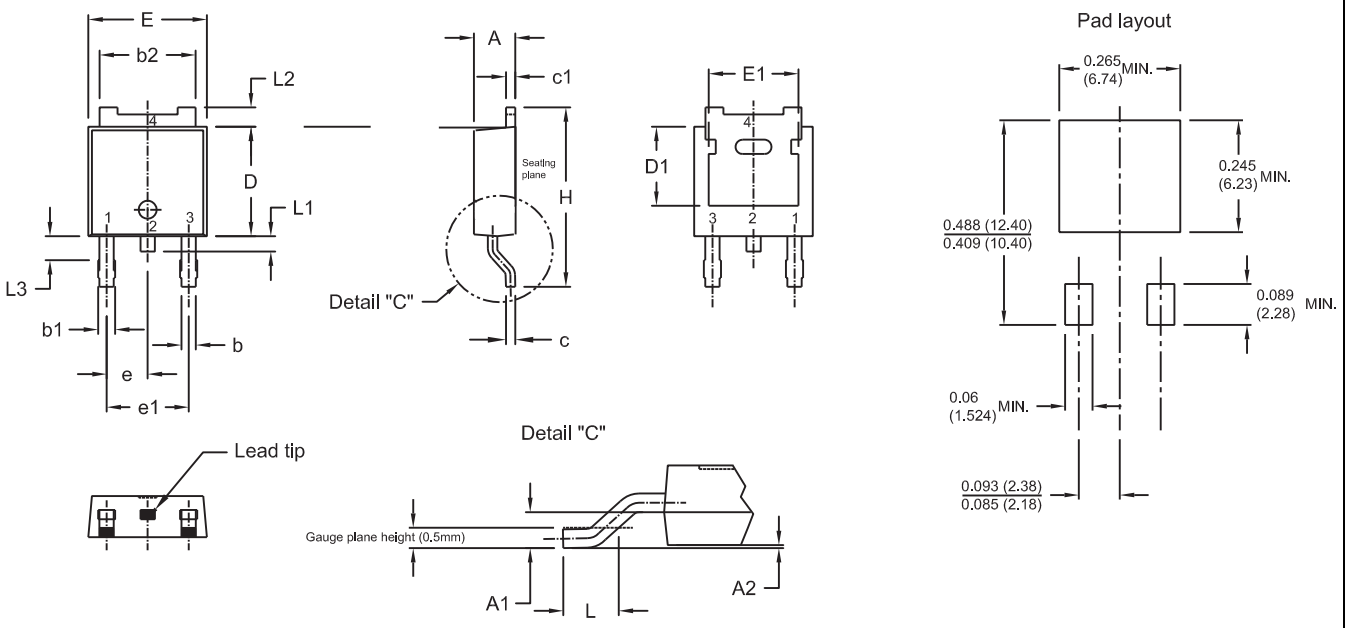
1 - Vishay Semiconductors product
2 - Current rating (12 A)
3 - Center tap configuration
4 - Package identifier:
W = D-PAK
5 - Schottky “Q” series
6 - Voltage rating (10 = 100 V)
7 - FN = TO-252AA
8 - • None = Tube
• TR = Tape and reel
• TRL = Tape and reel (left oriented)
• TRR = Tape and reel (right oriented)
9 - H = AEC-Q101 qualified
10 - Environmental digit:
M3 = Halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)			
PREFERRED P/N	**QUANTITY PER T/R**	**MINIMUM ORDER QUANTITY**	**PACKAGING DESCRIPTION**
VS-12CWQ10FNHM3	75	3000	Antistatic plastic tube
VS-12CWQ10FNTRHM3	2000	2000	13" diameter reel
VS-12CWQ10FNTRRHM3	3000	3000	13" diameter reel
VS-12CWQ10FNTRLHM3	3000	3000	13" diameter reel

LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?95519
Part marking information	www.vishay.com/doc?95518
Packaging information	www.vishay.com/doc?95033

D-PAK (TO-252AA)

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES	SYMBOL	MILLIMETERS		INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.			MIN.	MAX.	MIN.	MAX.	
A	2.21	2.38	0.087	0.094		A1	0.89	1.14	0.035	0.045	
A2	0.03	0.127	0.001	0.005		H	9.65	10.41	0.380	0.410	
b	0.71	0.88	0.028	0.035		L	1.40	1.78	0.055	0.070	
b1	0.76	1.14	0.030	0.045		e	2.28 BSC		0.09 BSC		
b2	5.23	5.44	0.206	0.214		e1	4.57 BSC		0.18 BSC		
C	0.46	0.58	0.018	0.023		L1	0.64	1.02	0.025	0.040	
C1	0.46	0.58	0.018	0.023		L2	0.89	1.27	0.035	0.050	
D	5.97	6.22	0.235	0.2455		L3	1.15	1.52	0.040	0.060	
D1	4.32	4.45	0.170	0.175							
E	6.48	6.73	0.255	0.2655							
E1	4.49	5.50	0.177	0.217							

Notes

(1) Dimensioning and tolerancing as per ASME Y14.5M-1994
(2) Lead dimension uncontrolled in L3 only for reference
(3) Dimension D1, E1, L2 and b2 establish a minimum mounting surface for thermal pad
(4) Dimensions D and E do not include mold flash.
(5) Outline conforms to JEDEC outline TO-252AA

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