

Hyperfast Soft Recovery Diode, 30 A FRED Pt® Gen 4



PRODUCT SUMMARY					
I _{F(AV)}	30 A				
V _R	600 V				
V _F at I _F	1.37 V				
t _{rr} typ.	see Recovery table				
T _J max.	175 °C				
Package	TO-247AD 2L				
Diode variation	Single die				

FEATURES

- Gen 4 FRED Pt® technology
- Low I_{RRM} and reverse recovery charge
- · Very low forward voltage drop
- · Polymide passivated chip for high reliability standard
- 175 °C operating junction temperature
- AEC-Q101 qualified, meets JESD 201 class 1 whisker test
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



COMPLIANT

HALOGEN

FREE

DESCRIPTION

Gen 4 Fred technology, state of the art, ultrafast V_F, soft switching optimized for Discontinuous (Critical) Mode (DCM) and IGBT F/W diode.

The minimized conduction loss, optimized stored charge and low recovery current minimized the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Cathode to anode voltage	V_R		600	V		
Average rectified current	I _{F(AV)}	T _C = 122 °C	30	А		
Single pulse forward current	I _{FSM}	$T_C = 25$ °C, $t_p = 8.3$ ms half sine wave	240	A		
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C		

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	600	-	-			
		I _F = 30 A	-	1.65	2	V		
		I _F = 60 A	-	1.95	-			
Forward voltage	.,	I _F = 30 A, T _J = 125 °C	-	1.44	-			
	V _F	I _F = 60 A, T _J = 125 °C	-	1.78	-			
		I _F = 30 A, T _J = 150 °C	-	1.37	1.6			
		I _F = 60 A, T _J = 150 °C	-	1.68	-			
Developed legislates assument	,	V _R = V _R rated	-	-	50			
Reverse leakage current	I _R	T _J = 150 °C, V _R = V _R rated	-	-	500	μA		
Junction capacitance	C _T	V _R = 600 V	-	18.3	-	pF		



DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	YMBOL TEST CONDITIONS			TYP.	MAX.	UNITS	
Reverse recovery time		T _J = 25 °C	$I_F = 30 \text{ A}$ $dI_F/dt = 1000 \text{ A/}\mu\text{s}$ $V_R = 400 \text{ V}$	-	55	-	ns	
	t _{rr}	T _J = 125 °C		-	75	-		
Dook rocovery ourrent	I _{RRM}	T _J = 25 °C		-	13	-	А	
Peak recovery current		T _J = 125 °C		-	23	-		
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	500	-	nC	
		T _J = 125 °C		-	1250	-	IIC IIC	

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Thermal resistance, junction to case	R _{thJC}		-	-	1	°C/W	
Thermal resistance, case to heat sink	R _{thCS}	Mounting surface, flat, smooth and greased	ı	0.4	-		
Weight			ı	6.0	-	g	
Weight			ı	0.21	-	oz.	
Mounting torque			6.0	_	12	kgf · cm	
Mounting torque			(5)	-	(20)	(lbf · in)	
Marking device		Case style TO-247AD 2L	E4PH3006LH				

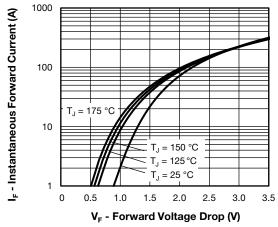


Fig. 1 - Typical Forward Voltage Drop Characteristics

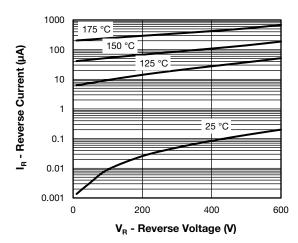


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

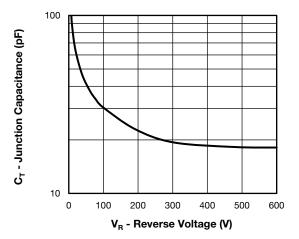


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

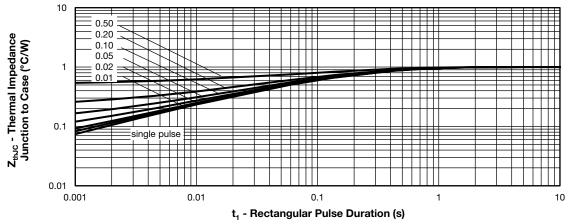


Fig. 4 - Max. Thermal Impedance Z_{thJC} Characteristics

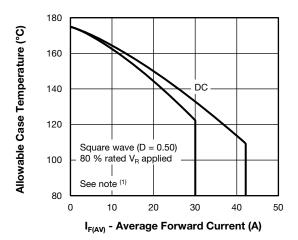


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

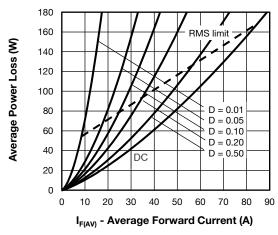


Fig. 6 - Forward Power Loss Characteristics

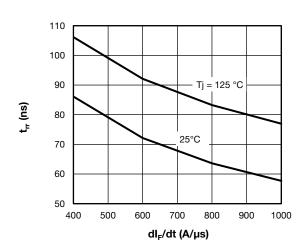


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt

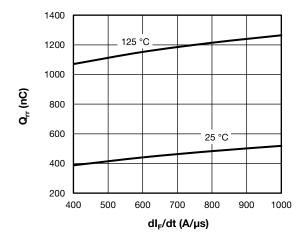


Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (\text{Pd} + \text{Pd}_{\text{REV}}) \times R_{\text{thJC}}; \\ \text{Pd} = \text{Forward power loss} = I_{\text{F(AV)}} \times V_{\text{FM}} \text{ at } (I_{\text{F(AV)}}/D) \text{ (see fig. 5)}; \\ \text{Pd}_{\text{REV}} = \text{Inverse power loss} = V_{\text{R1}} \times I_{\text{R}} \text{ (1 - D)}; I_{\text{R}} \text{ at } V_{\text{R1}} = \text{rated } V_{\text{R}} \\ \end{array}$

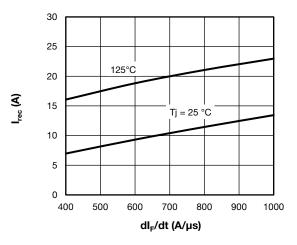
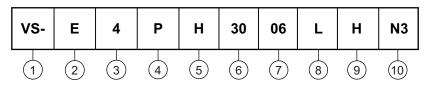


Fig. 9 - Typical Reverse Current vs. dl_F/dt

ORDERING INFORMATION TABLE

Device code



Vishay Semiconductors product

2 - Circuit configuration:

E = single diode, 2 pins

3 - FRED Pt Gen 4

P = TO-247 package

5 - Process type:

H = hyperfast recovery

6 - Current rating (30 = 30 A)

7 - Voltage rating (06 = 600 V)

8 - L = long lead

9 - H = AEC-Q101 qualified

10 - Environmental digit:

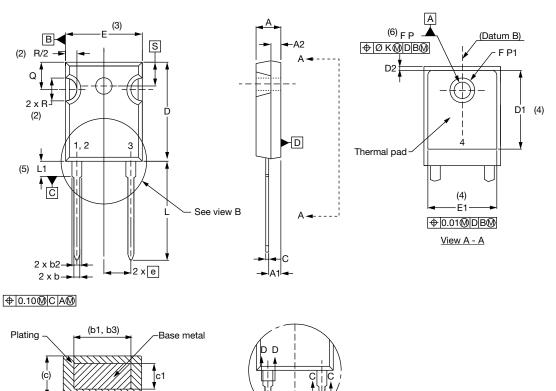
N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N QUANTITY PER TUBE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION						
VS-E4PH3006LHN3	25	500	Antistatic plastic tube			

LINKS TO RELATED DOCUMENTS					
Dimensions	TO-247AD 2L	www.vishay.com/doc?95536			
Part marking information	TO-247AD 2L	www.vishay.com/doc?95648			

TO-247AD 2L

DIMENSIONS in millimeters and inches



View B

SYMBOL	MILLIN	IETERS	INC	NOTES	
STIVIDUL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.50	2.49	0.059	0.098	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.34	0.065	0.092	
С	0.38	0.89	0.015	0.035	
c1	0.38	0.84	0.015	0.033	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4
D2	0.51	1.35	0.020	0.053	

Section C - C, D - D

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STIVIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Е	15.29	15.87	0.602	0.625	3
E1	13.46	-	0.53	-	
е	5.46	BSC	0.215	BSC	
ØK	0.2	0.254		0.010	
L	19.81	20.32	0.780	0.800	
L1	3.71	4.29	0.146	0.169	
ØΡ	3.56	3.66	0.14	0.144	
Ø P1	-	6.98	-	0.275	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	0.178	0.216	
S	5.51 BSC		0.217	'BSC	
		•		•	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC® outline TO-247 with exception of dimension A min., D, E min., Q min., S, and note 4



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